

HFB35HB20C

PD-94326B

Ultrafast, Soft Recovery Diode Thru-Hole (TO-254AA) 200V, 35A

Features

- Reduced RFI and EMI
- Reduced snubbing
- Extensive characterization of recovery parameters
- Hermetic package

Product Summary

Part number: HFB35HB20C

• I_{F(AV)}: 35A

• **V**_{RRM} (per leg): 200V

• **t**_{rr}: 35ns

I_{FSM} @ t_p = 8.33ms half-sine (per leg): 150A

Potential Applications

- DC-DC converter
- Motor drives

Product Validation

Qualified according to MIL-PRF-19500 for space applications



Description

These ultrafast, soft recovery diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. An extensive characterization of the recovery behavior for different values of current, temperature and di/dt simplifies the calculations of losses in the operating conditions. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for power converters, motors drives and other applications where switching losses are significant portion of the total losses.

Ordering Information

Table 1 Ordering options

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Part number	Package	Screening Level			
HFB35HB20C	TO-254AA	COTS			
HFB35HB20CSCV	TO-254AA	JANTXV-equivalent			
HFB35HB20CSCX	TO-254AA	JANTX-equivalent			
HFB35HB20CSCS	TO-254AA	S-level			

HFB35HB20C

FRED Ultrafast, Soft Recovery Diode



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FRED Ultrafast, Soft Recovery Diode



Absolute Maximum Ratings

1 Absolute Maximum Ratings

Table 2 Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V_R	Cathode to anode voltage (per leg)	200	V
I _{F(AV)}	Continuous forward current, T _C =108 °C ¹	35	Α
I _{FSM}	Single pulse forward current, T _c = 25°C (per leg) ²	150	Α
$P_D @ T_C = 25^{\circ}C$	Maximum power dissipation	90	W
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to 150	°C
Wt	Weight	9.3 (Typical)	g

¹ D.C. = 50% rect. wave

 $^{^{2}}$ ½ sine wave, 60 Hz, P.W. = 8.33 ms



Device Characteristics

2 Device Characteristics

2.1 Electrical Characteristics

Table 3 Electrical Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
V_{BR}	Cathode Anode Breakdown Voltage	200	_		V	$I_R = 100 \mu A$
		_	_	1.3	V	I _F = 17.5A, T _J = -55°C
V	Forward Voltage Drop (Per Leg) See Fig. 1	-	_	1.1	V	I _F = 17.5A, T _J = 25°C
V _F		_	_	1.4	V	I _F = 35A, T _J = 25°C
		-	_	1.0	V	I _F = 17.5A, T _J = 125°C
	Reverse Leakage Current	_	_	10	μΑ	$V_R = V_R$ Rated
I_R	(Per Leg) See Fig. 2	_	_	50	μΑ	$V_R = V_R$ Rated, $T_J = 125$ °C
CJ	Junction Capacitance (Per Leg) See Fig. 3	_	_	150	pF	V _R = 200V
Ls	Series Inductance (Per Leg)	_	6.7	_	nH	Measured from anode lead to cathode lead, 6mm (0.025 in) from package

2.2 Dynamic Recovery Characteristics

Table 4 Dynamic Recovery Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Condition	s
t _{rr}	Reverse Recovery Time (Per Leg)	_	_	45	ns	$I_F = 35A, V_R = 160$	V, d _{if} /dt = 200A/μs
t _{rr1}	Reverse Recovery Time	_	28	_	ns	T _J = 25°C	
t _{rr2}	(Per Leg) See Fig. 5	-	46	_	ns	T _J = 125°C	I _F =35A
I _{RRM1}	Peak Recovery Current	_	4.0	_	Α	T _J = 25°C	
I _{RRM2}	(Per Leg) See Fig. 6	_	12.3	_	Α	T _J = 125°C	V _R =160V
Q _{rr1}	Reverse Recovery Charge	_	66	_	nC	T _J = 25°C	
Q _{rr2}	(Per Leg) See Fig. 7	_	190	_	nC	T _J = 125°C	$d_{if}/dt = 200 A/ \mu s$
$di_{(rec)M}/dt1$	Peak Rate of Fall of Recovery	_	410	_	A/ μs	T _J = 25°C	
di _{(rec)M} /dt2	Current During t₅ (Per Leg) See Fig. 8	_	1740	_	A/ μs	T _J = 125°C	

2.3 Thermal-Mechanical Characteristics

 Table 5
 Thermal-Mechanical Characteristics

Symbol	Parameter		Max.	Unit
$R_{ heta JC}$	Junction to Case, Single Leg Conducting		1.4	°C/W

Electrical Characteristics Curves

3 Electrical Characteristics Curves

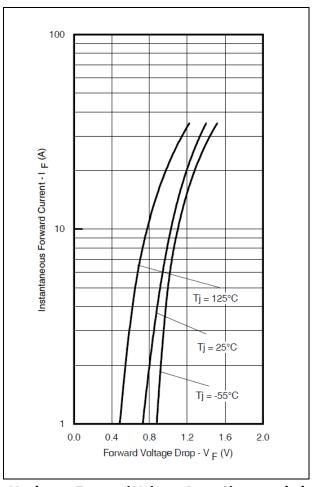


Figure 1 Maximum Forward Voltage Drop Characteristics (Per Leg)

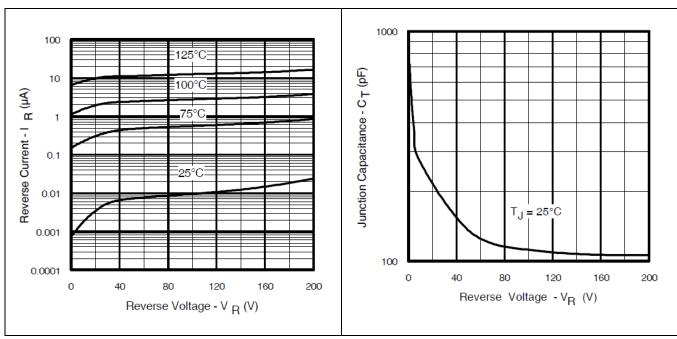


Figure 2 Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

Figure 3

Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

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Electrical Characteristics Curves

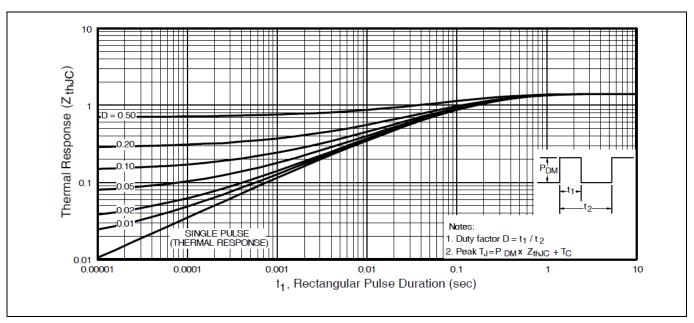


Figure 4 Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

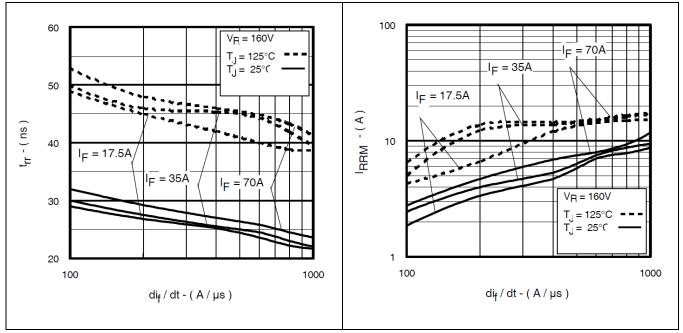


Figure 5 Typical Reverse Recovery Vs. dif/dt (Per Leg)

Figure 6 Typical Recovery Current Vs. dif/dt (Per Leg)

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Electrical Characteristics Curves

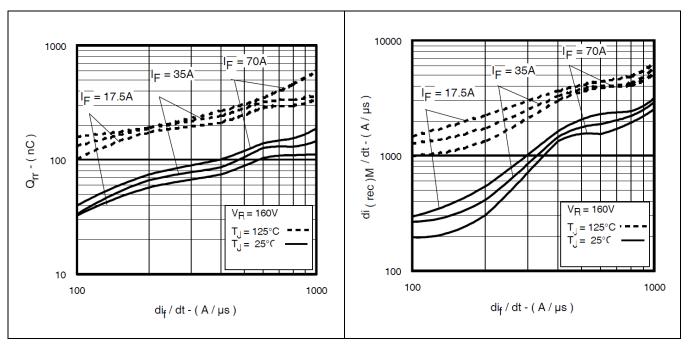


Figure 7 Typical Stored Charge Vs. dif/dt (Per Leg)

Figure 8 Typical di(rec)M/dt Vs. dif/dt (Per Leg)



Test Circuit

4 Test Circuit

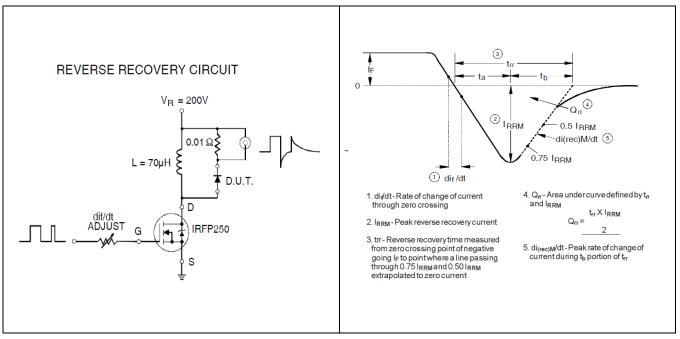


Figure 9 Reverse Recovery Parameter Test
Circuit

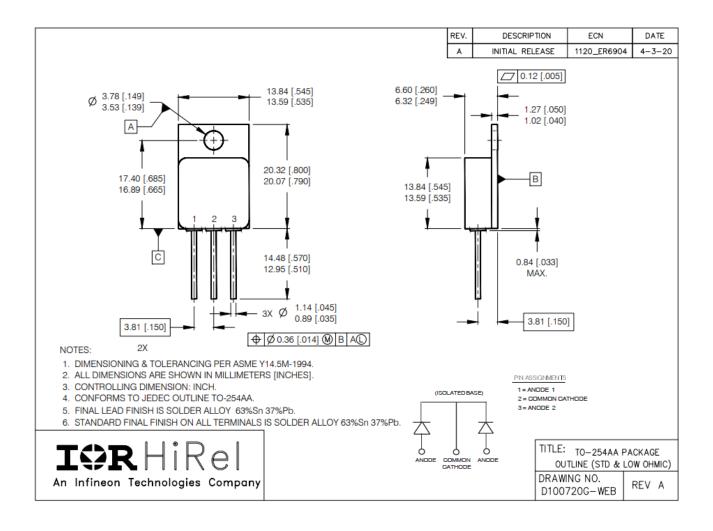
Figure 10 Reverse Recovery Waveform and Definitions



Package Outline

5 Package Outline

Note: For the most updated package outline, please see the website: TO-254AA



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Revision history

Revision history

Document version	Date of release	Description of changes	
	10/18/2001	Final datasheet (PD-94326)	
Rev A	02/20/2006	Updated per ECN-13810	
Rev B	08/10/2021	Updated per ECN-1120-08717	

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