June, 6th 2011
Automotive grade

AUIPS7125R

CURRENT SENSE HIGH SIDE SWITCH

Features
- Suitable for 24V systems
- Over current shutdown
- Over temperature shutdown
- Current sensing
- Active clamp
- Reverse circulation immunization
- Optimized Turn On/Off for EMI
- Reverse battery protection (Mosfet on)

Applications
- 75W Filament lamp
- Solenoid
- 24V loads for trucks

Description
The AUIPS7125R is a fully protected five terminal high side switch specifically designed for driving lamp. It features current sensing, over-current, over-temperature, ESD protection and drain to source active clamp. When the input voltage Vcc - Vin is higher than the specified threshold, the output power Mosfet is turned on. When the Vcc - Vin is lower than the specified Vil threshold, the output Mosfet is turned off. The Ifb pin is used for current sensing. The over-current shutdown is higher than inrush current of the lamp.

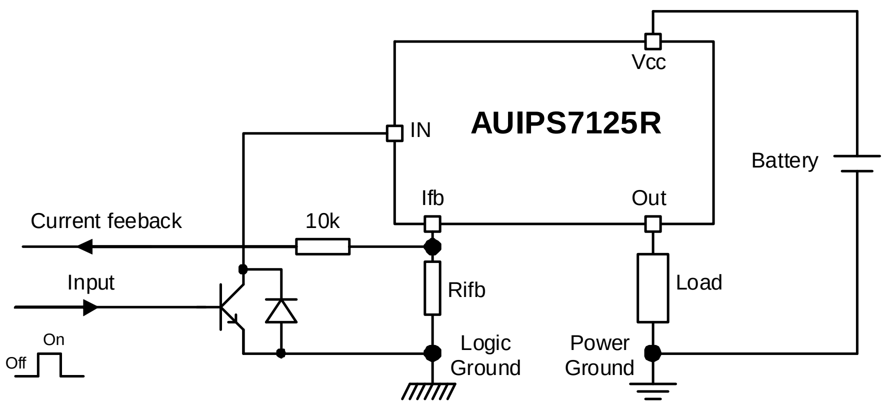
Product Summary

Rds(on)	30mΩ max.
Vclamp	65V
Current shutdown	50A min.

Packages

DPak

Typical Connection



Qualification Information†

Qualification Level		Automotive (per AEC-Q100††)	
		Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.	
Moisture Sensitivity Level		DPAK-5L	MSL1, 260°C (per IPC/JEDEC J-STD-020)
ESD	Machine Model	Class M2 (200 V) (per AEC-Q100-003)	
	Human Body Model	Class H1C (1500 V) (per AEC-Q100-002)	
	Charged Device Model	Class C5 (1000 V) (per AEC-Q100-011)	
IC Latch-Up Test		Class II, Level A (per AEC-Q100-004)	
RoHS Compliant		Yes	

† Qualification standards can be found at International Rectifier's web site http://www.irf.com/
†† Exceptions (if any) to AEC-Q100 requirements are noted in the qualification report.

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. (Tj= -40°C..150°C, Vcc=6..50V unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vout	Maximum output voltage	Vcc-60	Vcc+0.3	V
I rev	Maximum reverse pulsed current (t=100µs) see page 8	—	60	A
Isd cont.	Maximum diode continuous current Tambient=25°C, Rth=70°C/W	—	2.5	
Vcc-Vin max.	Maximum Vcc voltage	-32	60	V
Iifb, max.	Maximum feedback current	-50	10	mA
Vcc sc	Maximum Vcc voltage with short circuit protection see page 8	—	50	V
Pd	Maximum power dissipation (internally limited by thermal protection)			W
	Rth=50°C/W DPack 6cm² footprint	—	2.5	
Tj max.	Max. storage & operating junction temperature	-40	150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
Rth1	Thermal resistance junction to ambient DPak Std footprint	70	—	°C/W
Rth2	Thermal resistance junction to ambient Dpak 6cm² footprint	50	—	
Rth3	Thermal resistance junction to case Dpak	2	—	

Recommended Operating Conditions

These values are given for a quick design.

Symbol	Parameter	Min.	Max.	Units
Iout	Continuous output current, Tambient=85°C, Tj=125°C			A
	Rth=50°C/W, Dpak 6cm² footprint	—	3.8	
Rifb	Ifb resistor	1.5	—	kΩ

Static Electrical Characteristics

Tj=-40°C..150°C, Vcc=6-50V (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Vcc op.	Operating voltage range	6	—	60	V	
Rds(on)	ON state resistance Tj=25°C	—	24	30	mΩ	Ids=2A
	ON state resistance Tj=150°C(2)	—	45	55		
Icc off	Supply leakage current	—	2	4	µA	Vin=Vcc=28V,Vifb=Vgnd Vout=Vgnd, Tj=25°C
Iout off	Output leakage current	—	2	4		
Iin on	Input current when device on	1	3.5	6	mA	Vcc-Vin=28V, Tj=25°C
V clamp1	Vcc to Vout clamp voltage 1	60	64	—	V	Id=10mA
V clamp2	Vcc to Vout clamp voltage 2	60	65	72		Id=20A see fig. 2
Vih(1)	High level Input threshold voltage	—	3.5	5.9		Id=10mA
Vil(1)	Low level Input threshold voltage	1.5	3.2	—		
Rds(on) rev	Reverse On state resistance Tj=25°C	—	25	40	mΩ	Isd=2A
Vf	Forward body diode voltage Tj=25°C	—	0.75	0.85	V	If=3A
	Forward body diode voltage Tj=125°C	—	0.62	0.7		
Rin	Input resistor	180	250	350	Ω	

(1) Input thresholds are measured directly between the input pin and the tab.

Switching Electrical Characteristics

Vcc=28V, Resistive load=6.8Ω, Tj=-40°C..150°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
tdon	Turn on delay time	5	15	30	µs	See fig. 1
tr	Rise time from 20% to 80% of Vcc	5	10	30		
tdoff	Turn off delay time	35	75	120	µs	
tf	Fall time from 80% to 20% of Vcc	6	15	30		

Protection Characteristics

Tj=-40°C..150°C, Vcc=6-50V (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Tsd	Over temperature threshold(2)	150	165	—	°C	See fig. 3 and fig. 11
Isd	Over-current shutdown	50	60	85	A	See fig. 3 and page 7
I fault	Ifb after an over-current or an over-temperature (latched)	2.2	3	5	mA	See fig. 3

Current Sensing Characteristics

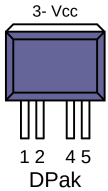
Tj=-40°C..150°C, Vcc=6-50V (unless otherwise specified). Specified 500µs after the turn on. Vcc-Vifb>4V

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Ratio	I load / Ifb current ratio	7050	8500	9950		Iload<14A
Ratio_TC	I load / Ifb variation over temperature(2)	-5%	0	+5	%	Tj=-40°C to +150°C
I offset	Load current offset	-0.06	0	0.06	A	Iout<14A
Ifb leakage	Ifb leakage current	0	1	10	µA	Iout=0A

(2) Guaranteed by design

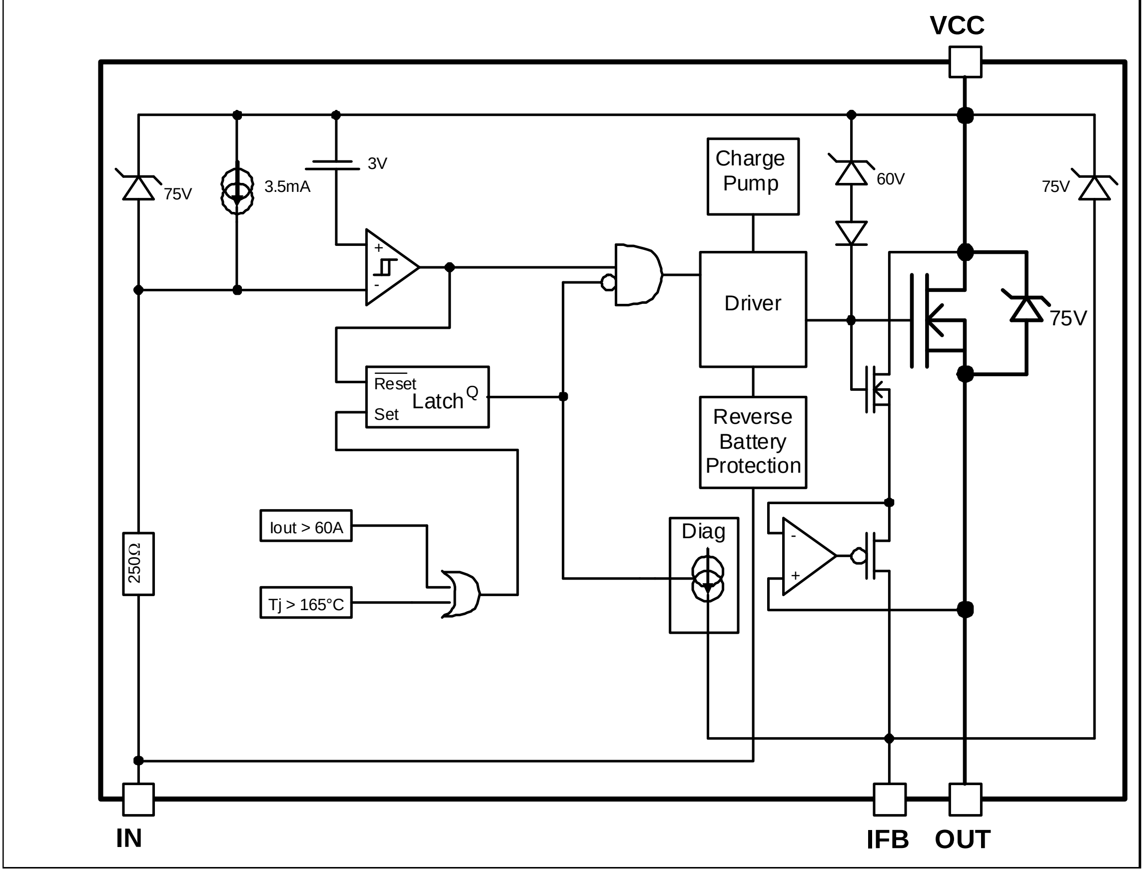
Lead Assignments

1- NC
2- In
3- Vcc
4- Ifb
5- Out



Functional Block Diagram

All values are typical



Truth Table

Op. Conditions	Input	Output	Ifb pin voltage
Normal mode	H	L	0V
Normal mode	L	H	I load x Rfb / Ratio
Open load	H	L	0V
Open load	L	H	Ifb leakage x Rifb
Short circuit to GND	H	L	0V
Short circuit to GND	L	L	I fault x Rifb(latched)
Over temperature	H	L	0V
Over temperature	L	L	I fault x Rifb (latched)

Operating voltage

Maximum Vcc voltage : this is the maximum voltage before the breakdown of the IC process.
Operating voltage : This is the Vcc range in which the functionality of the part is guaranteed. The AEC-Q100 qualification is run at the maximum operating voltage specified in the datasheet.

Reverse battery

During the reverse battery the Mosfet is turned on if the input pin is powered with a diode in parallel of the input transistor.
Power dissipation in the IPS : $P = Rdson\ rev * I\ load^2 + Vcc^2 / 250$ (internal input resistor).
If the power dissipation is too high in Rifb, a diode in serial can be added to block the current.

Active clamp

The purpose of the active clamp is to limit the voltage across the MOSFET to a value below the body diode break down voltage to reduce the amount of stress on the device during switching.
The temperature increase during active clamp can be estimated as follows:

$$\Delta_{Tj} = P_{CL} \cdot Z_{TH}(t_{CLAMP})$$

Where: $Z_{TH}(t_{CLAMP})$ is the thermal impedance at t_{CLAMP} and can be read from the thermal impedance curves given in the data sheets.

$P_{CL} = V_{CL} \cdot I_{CLavg}$: Power dissipation during active clamp

$V_{CL} = 65V$: Typical V_{CLAMP} value

$I_{CLavg} = \frac{I_{CL}}{2}$: Average current during active clamp

$t_{CL} = \frac{I_{CL}}{\left|\frac{di}{dt}\right|}$: Active clamp duration

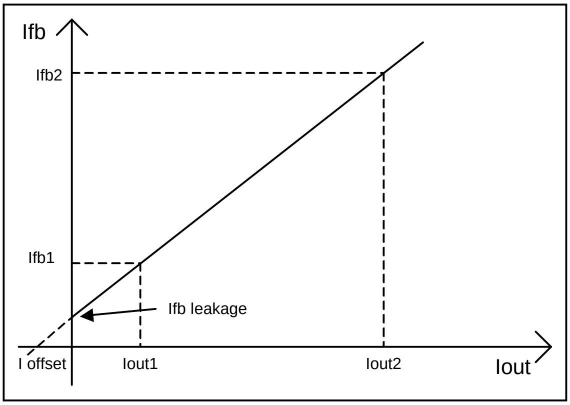
$\frac{di}{dt} = \frac{V_{Battery} - V_{CL}}{L}$: Demagnetization current

Figure 9 gives the maximum inductance versus the load current in the worst case : the part switches off after an over temperature detection. If the load inductance exceeds the curve, a free wheeling diode is required.

Over-current protection

The threshold of the over-current protection is set in order to guarantee that the device is able to turn on a load with an inrush current lower than the minimum of Isd. Nevertheless for high current and high temperature the device may switch off for a lower current due to the over-temperature protection. This behavior is shown in Figure 11.

Current sensing accuracy



The current sensing is specified by measuring 3 points :
- Ifb1 for Iout1
- Ifb2 for Iout2
- Ifb leakage for Iout=0

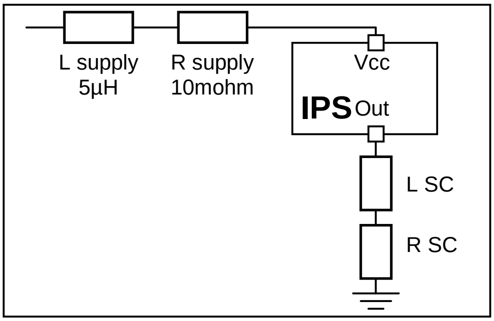
The parameters in the datasheet are computed with the following formula :
Ratio = (Iout2 – Iout1)/(Ifb2 – Ifb1)
I offset = Ifb1 x Ratio – Iout1

This allows the designer to evaluate the Ifb for any Iout value using :
Ifb = (Iout + I offset) / Ratio if Ifb > Ifb leakage

For some applications, a calibration is required. In that case, the accuracy of the system will depends on the variation of the I offset and the ratio over the temperature range. The ratio variation is given by Ratio_TC specified in page 4.
The Ioffset variation depends directly on the Rdson :
I offset@-40°C= I offset@25°C / 0.8
I offset@150°C= I offset@25°C / 1.9

Maximum Vcc voltage with short circuit protection

The maximum Vcc voltage with short circuit is the maximum voltage for which the part is able to protect itself under test conditions representative of the application. 2 kind of short circuits are considered : terminal and load short circuit.



	L SC	R SC
Terminal SC	0.1 µH	10 mohm
Load SC	10 µH	100 mohm

Maximum current during reverse circulation

In case of short circuit to battery, a voltage drop of the Vcc may create a current which circulate in reverse mode. When the device is on, this reverse circulation current will not trigger the internal fault latch. This immunization is also true when the part turns on while a reverse current flows into the device. The maximum current (I rev) is specified in the maximum rating section.

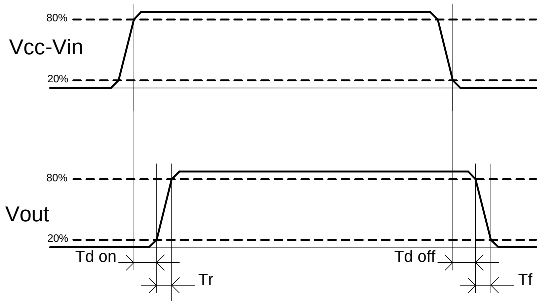


Figure 1 – IN rise time & switching definitions

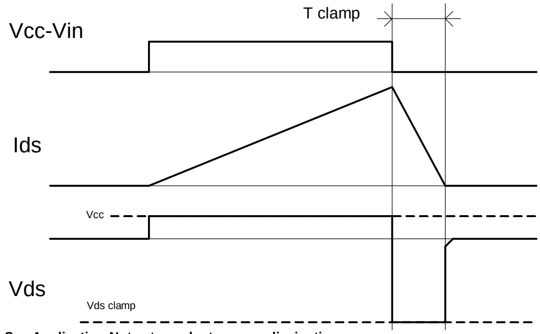


Figure 2 – Active clamp waveforms

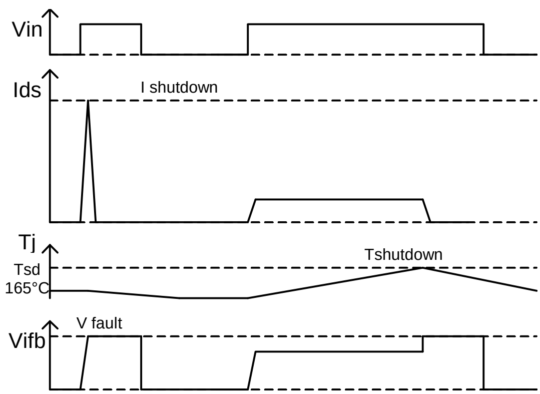


Figure 3 – Protection timing diagram

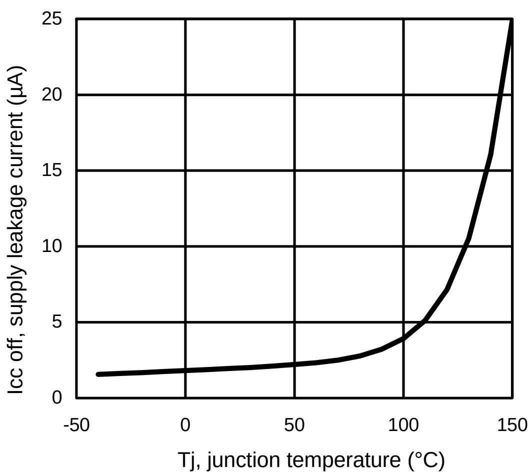


Figure 4 – Icc off (µA) Vs Tj (°C)

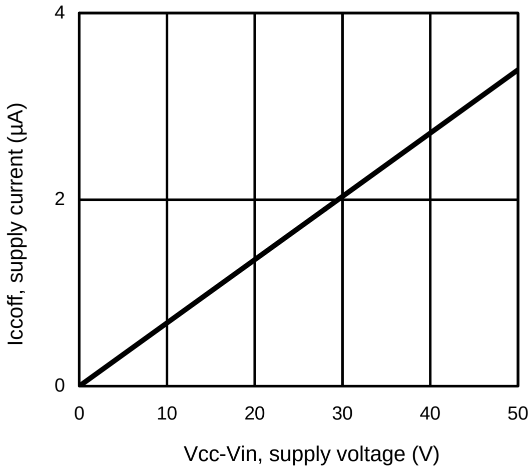


Figure 5 – Icc off(µA) Vs Vcc-Vin (V)

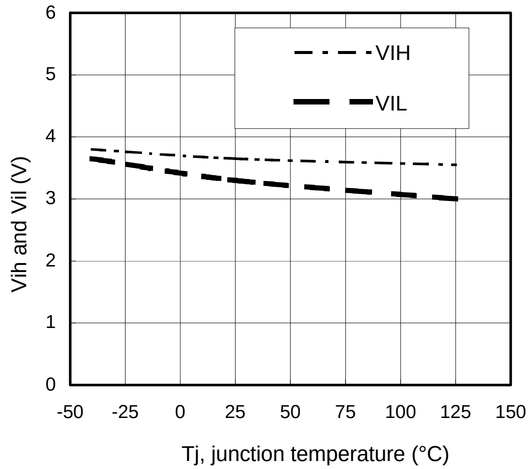


Figure 6 – Vih and Vil (V) Vs Tj (°C)

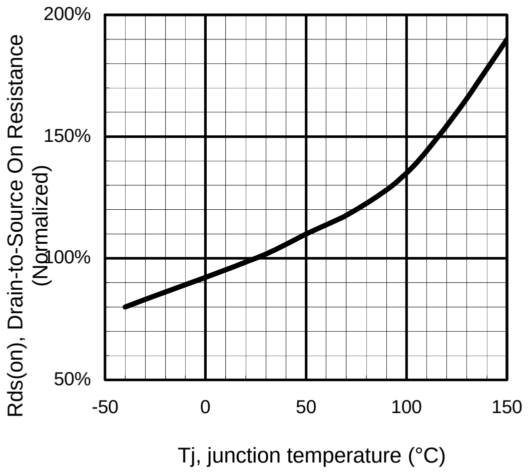


Figure 7 - Normalized Rds(on) (%) Vs Tj (°C)

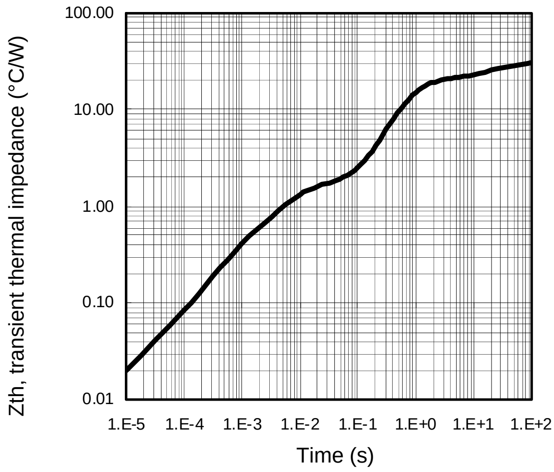


Figure 8 – Transient thermal impedance (°C/W) Vs time (s)

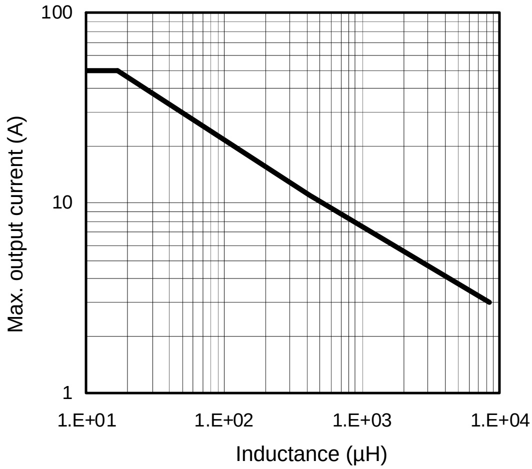


Figure 9 – Max. Iout (A) Vs inductance (µH)

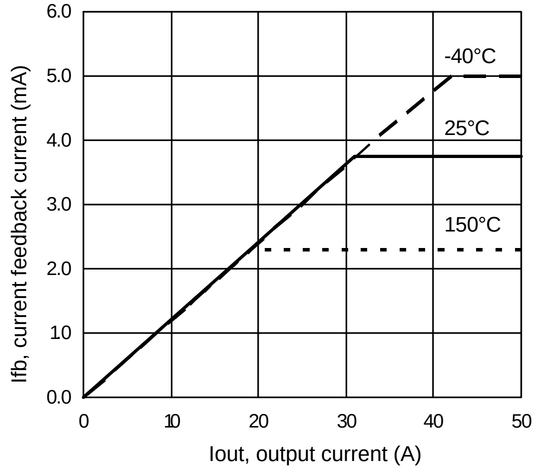


Figure 10 – Ifb (mA) Vs Iout (A)

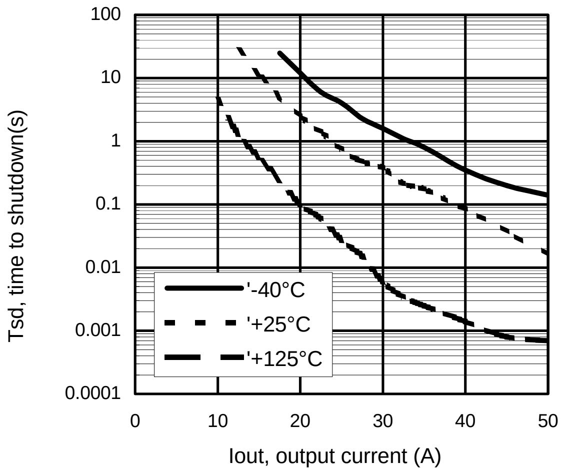
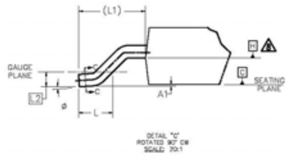
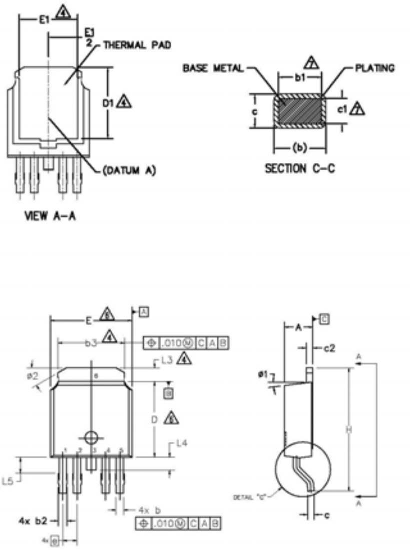


Figure 11 – Tsd (s) Vs I out (A)
SMD with 6cm²

Case Outline 5 Lead – DPAK





SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	2.18	2.39	.086	.094	
A1	–	0.13	–	.005	
b	0.56	0.79	.022	.031	
b1	.056	0.74	.022	.029	2
b2	0.65	0.89	.026	.035	
b3	4.95	5.46	.195	.215	2
c	0.46	0.61	.018	.024	
c1	0.41	0.56	.016	.022	2
c2	0.46	0.89	.018	.035	
D	5.97	6.22	.235	.245	3
D1	5.21	–	.205	–	
E	6.35	6.73	.250	.265	3
E1	4.32	–	.170	–	
e	1.14 BSC		.045 BSC		
H	9.40	10.41	.370	.410	
L	1.40	1.78	.055	.070	
L1	2.74 BSC		.108 REF.		
L2	0.51 BSC		.020 BSC		
L3	0.89	1.27	.035	.050	
L4	–	1.02	–	.040	
L5	1.14	1.52	.045	.060	
ø	0°	10°	0°	10°	
ø1	0°	15°	0°	15°	
ø2	28°	32°	28°	32°	

NOTES:

1.– DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M–1994

2.– DIMENSION ARE SHOWN IN INCHES [MILLIMETERS].

3.– LEAD DIMENSION UNCONTROLLED IN L5.

4.– DIMENSION D1, E1, L3 & b3 ESTABLISH A MINIMUM MOUNTING SURFACE FOR THERMAL PAD.

5.– SECTION C–C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 AND 0.10 [0.13 AND 0.25] FROM THE LEAD TIP.

6.– DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005 [0.13] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.

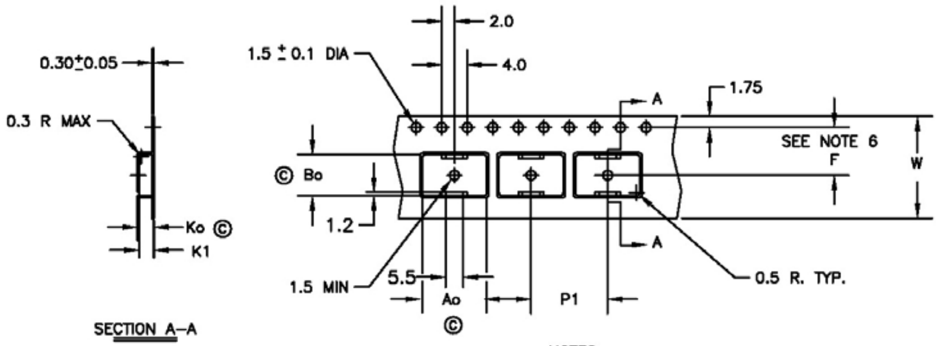
7.– DIMENSION b1 & c1 APPLIED TO BASE METAL ONLY.

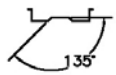
8.– DATUM A & B TO BE DETERMINED AT DATUM PLANE H.

9.– OUTLINE CONFORMS TO JEDEC OUTLINE TO–252.

10. LEADS AND DRAIN ARE PLATED WITH 100% Sn

Tape & Reel 5 Lead – DPAK



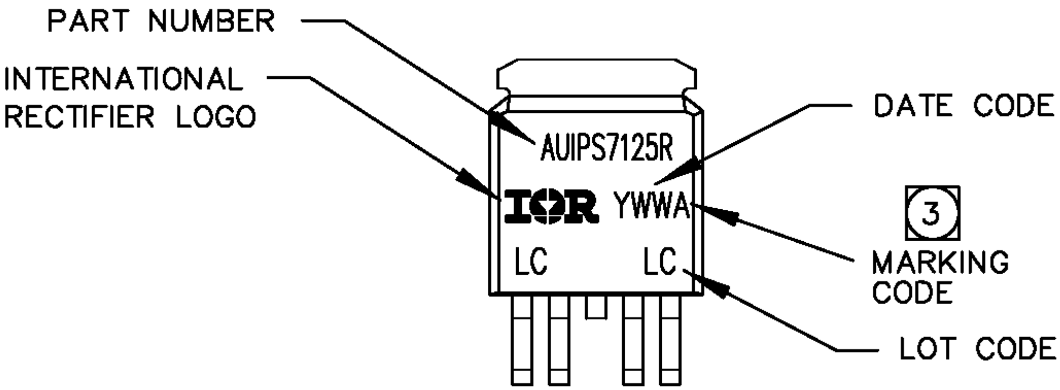


NOTES:

1. 10 SPROCKET HOLE PUNCH CUMULATIVE TOLERANCE ±.02
2. CAMBER NOT TO EXCEED 1mm IN 100mm
3. MATERIAL: CONDUCTIVE BLACK POLYSTYRENE
4. Ao AND Bo MEASURED ON A PLANE 0.3mm ABOVE THE BOTTOM OF THE POCKET
5. Ko MEASURED FROM A PLANE ON THE INSIDE BOTTOM OF THE POCKET TO THE TOP SURFACE OF THE CARRIER
6. POCKET POSITION RELATIVE TO THE SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE
7. VENDOR: (OPTIONAL)
8. MUST ALSO MEET REQUIREMENTS OF EIA STANDARD #EIA–481A, TAPING OF SURFACE–MOUNT COMPONENTS FOR AUTOMATIC PLACEMENT.
9. TOLERANCE TO BE MANUFACTURER STANDARD
10. SURFACE RESISTIVITY OF MOLDED MATL: MUST MEASURE LESS THAN OR EQUAL TO 10^8 OHMS PER SQUARE. MEASURED IN ACCORDANCE TO PROCEDURE GIVEN IN ASTM D–257 & ASTM D–991 (REF. C–9000 SPEC.)
11. TOTAL LENGTH PER REEL MUST BE 79 METERS
12. © CRITICAL DIMENSION

Ao = 10.5 mm
Bo = 7.0 mm
Ko = 2.8 mm
K1 = 2.4 mm
F = 7.5 mm
P1 = 12.0 mm
W = 16.0 ± .3 mm

Part Marking Information



Ordering Information

Base Part Number	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIPS7125R	D-Pak-5-Lead	Tube	75	AUIPS7125R
		Tape and reel	2000	AUIPS7125RTR
		Tape and reel left	3000	AUIPS7125RTRL
		Tape and reel right	3000	AUIPS7125RTRR

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For technical support, please contact IR's Technical Assistance Center
http://www.irf.com/technical-info/
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101 N Sepulbeda Blvd., El Segundo, California 90245
Tel: (310) 252-7105

Revision History

Revision	Date	Notes/Changes
A1	08/03/2010	
A2	29/04/2010	Correct packing information
A3	07/09/2010	Update current sensing capability
A4	31/05/2011	Final release
A5	06/06/2011	Update IR address