

Quickstart Guide for Inverter Evaluation Kit

Inverter Evaluation Kit for HybridPACK[™] Drive G2 Power Modules

About this document

This application note is a Quickstart Guide for the inverter evaluation kits with the HybridPACK[™] Drive G2 power modules. The inverter evaluation kit is available with latest IGBT/Diode as well as with SIC MOSFET chip technologies. An overview about the inverter evaluation kits, short description of most important interfaces and introduction of basic inverter operation is given.

Scope and purpose

The inverter evaluation kits were designed to support customers during their first steps in designing applications with the HybridPACK[™] Drive G2 power modules, EiceDriver[™] gate drivers, Xensiv[™] current sensors and Aurix[™] Microcontrollers. The inverter kits can provide a good starting point for project specific inverter developments. Furthermore, practical experience from the power module operation as well as the gate driver features and performance tests of phase current sensors can be obtained in the lab at a minimum effort by using such evaluation tools. Before getting started it is mandatory to read and understand the safety warnings (section 1.1).

Intended audience

Experienced engineers designing inverters with HybridPACK[™] Drive G2.

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Please read the Important Notice and Warnings at the end of this document

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1 Introduction

The inverter evaluation kits for the HybridPACK[™] Drive G2 power modules support engineers during their designin activities. The kits uses latest EiceDriver[™] gate drivers and Xensiv[™] current sensors implemented in the Swoboda phase current sensors. The inverter kit has open design files, which makes it very convenient for evaluation purpose and cross reference tests.

Please read and understand the manual and following safety warnings in section 1.1.

1.1 Safety Warning for Evaluation Kit

The design operates with unprotected high voltages. Therefore, the Evaluation Tools may only be handled by persons with sufficient electrical engineering training and experience. The customer assumes all responsibility and liability for its correct handling and/or use of the Evaluation Kit and undertakes to indemnify and hold Infineon Technologies harmless from any third party claim in connection with or arising out of the use and/or handling of the Evaluation Kit by the customer.

The Evaluation Kit is a sample to be used by the customer solely for the purpose of evaluation and testing. It is not a commercialized product and shall not be used for series production. The Evaluation Kit is thus not intended to meet any automotive qualifications. Due to the purpose of the system, it is not subjected to the same procedures regarding Returned Material Analysis (RMA), Process Change Notification (PCN) and Product Withdraw (PWD) as regular products. See Legal Disclaimer and Warnings for further restrictions on Infineon Technologies warranty and liability.

European legislation in relation to inter alia the restriction of hazardous substances (RoHS), waste from electrical and electronic equipment (WEEE), electromagnetic compatibility, as well as duties to comply with CE, FCC or UL standards do not apply to the Evaluation Kit and the Evaluation Kit may not fulfill such requirements.

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1.2 Information Export Control Classification 3A225

Frequency changers (a.k.a. converters or inverters) and generators, that are usable as a variable frequency or fixed frequency motor drive and have all of the characteristics described below fall under the 3A225 classification.

Question	Answer	Detailed Information				
Multiphase output providing a power of 40VA or greater	YES	The inverter evaluation kit uses HybridPACK Drive G2 power modules, which can transfer power of several houndred kVA power. Typical application for this power module is 120 to 300kW traction inverters for electric vehicles.				
Operating at a frequency of 600 Hz or more	YES	The evaluation kit does not limit the output frequency. Typical output frequency can be up to 0.1 times of the applied switching frequency. The evaluation kits are designed to operate with 10 up to 20kHz switching frequency (adjustable), which enables practical useable output frequencies of 2000 Hz easily.				
Frequcy control better (less) than 0.2%	YES	The evaluation kit uses a digital control with Aurix [™] Microcontroller. The Microcontroller clock is generated from a standard off-the-shelf crystal with a typical frequency tolerance of +/-15ppm at room temperature and additional +/-25ppm over the operating temperature.				

Table 1 Export control relevant performance data of the evaluation kit

1.3 Block Diagram

The following illustration show a simplified block diagram view of the inverter evaluation kit (shipping content) and the typical test bench setup.



Figure 1 Block Diagram of the Inverter Evaluation Kits for HybridPACK™ Drive G2 power modules



Overview Evaluation Tools and Order Numbers

2 Overview Evaluation Tools and Order Numbers

The inverter evaluation kits in Figure 2 comes with power module, cooler, DC-link capacitor, gate driver board, logicboard, pre-installed software, USB stick.



Figure 2 Inverter Evaluation Kits for HybridPACK™ Drive G2 power modules. Versions with latest IGBT and SIC MOSFET chipsets at 750V and 1200V blocking capability are available. See also Table 2.

The inverter evaluation kits can be orderd at your local Infineon sales representative with the corresponding SP order number. A comprehensive type designation tree for HybridPACK[™] power module evaluation tools can be found in section 2.1.

Туре	Key Components	SP Order No	Order Name
Inverter Kit with	Power Module FS1150R08A8P3	SP005739456	EV INV HPD2 SI FS1150 08
750V/1150A IGBT	Gate Driver Board with 1EDI3025AS		
power module	CSM510HP2x Swoboda Phase Current Sensors		
	Logicboard with Aurix 2G TC3x7		
Inverter Kit with	Power Module FS520R12A8P1	SP005739466	EV INV HPD2 SI FS520 12
1200V/520A IGBT	Gate Driver Board with 1EDI3025AS		
power module	CSM510HP2x Swoboda Phase Current Sensors		
	Logicboard with Aurix 2G TC3x7		
Inverter Kit with	Power Module FS01MR08A8MA2	SP005739468	EV INV HPD2 SIC FS01 08
750V/1 mOhm SIC	Gate Driver Board with 1EDI3035AS		
MOSFET power	CSM510HP2x Swoboda Phase Current Sensors		
module	Logicboard with Aurix 2G TC3x7		
Inverter Kit with	Power Module FS02MR12A8MA2	SP005739445	EV INV HPD2 SIC FS02 12
1200V/2 mOhm SIC	Gate Driver Board with 1EDI3035AS		
MOSFET power	CSM510HP2x Swoboda Phase Current Sensors		
module	Logicboard with Aurix 2G TC3x7		

Table 2 Overview Inverter Evaluation Kits for HybridPACK[™] Drive G2 power modules

Inverter Evaluation Kit for HybridPACK[™] Drive G2 Power Modules

Overview Evaluation Tools and Order Numbers

2.1 Type designation tree for evaluation tools

The product order names of the evaluation tools were changed in Jan 2024 in order to support a comprehensive type designation tree for the available evaluation tools. The SP order numbers remains unchanged. The structure of the new naming is shown in the figure followed by some examples.

EV	TTT FFFF CCC PPPPPP VV
EV	EV: Evaluation Tool
	TTT: Type INV: Inverter Kit GB : Gate Driver Board PCB: any support PCB like adapters, LB : Logic Board GMC: Gate Driver Board + Module + Cooler without Logicboard and Software
FFFF	FFFFF: Product Family HPD2: HybridPACK Drive G2 HPD1: HybridPACK Drive Generation 1 HP DSC: HybridPACK DSC HP SSC: HybridPACK SSC HP 1: HybridPACK INT: multi purpose interface PCB : no entry => fits to all
	CCC: Chip Technology
	SI: Si IGBT/Diode Technology SIC: SiC MOSFET Technology HY: Hybrid with Si IGBT/Diode & SiC MOSFET Technology : no entry => fits to all
РРРРРР	PPPPP: Product
	FS1150: shortcut of related product FS1150R08A8P3 FS520: shortcut of related product FS520R12A8P1 FS01: shortcut of related product FS01MR08A8MA2 FS02: shortcut of related product FS02MR12A8MA2 FF450: THER: thermal test PCB for related product family : no entry => fits to all
VV	VV: Voltage rating x100 08: 750V blocking capability 12: 1200V blocking capability : no entry => fits to all

Figure 3 Type designation tree for the HybridPACK™ power module evaluation tools.

Some Examples:

EV INV HPD2 SI FS520 12: Inverter Evaluation Kit for HybridPACK[™] Drive G2 Si 1200V Automotive Power Module FS520R12A7P1 and Swoboda Phase Current Sensor.

EV INV HPD2 SIC FS02 12: Inverter Evaluation Kit for HybridPACK[™] Drive G2 SiC 1200V Automotive Power Module FS02MR12A8MA2 and Swoboda Phase Current Sensor.

EV GB HPD2 SIC: Evaluation Kit - Gate Drive board for HybridPACK[™] Drive G2 750V & 1200V SIC power module with 1EDI3035AS gate driver.

EV GB HPD2 SI 08: Evaluation Kit - Gate Drive board for HybridPACK[™] Drive G2 750V IGBT power module with 1EDI3025AS gate driver.

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Overview Evaluation Tools and Order Numbers

2.2 Recommended Lab Equipment

For evaluation following lab equipment is recommended:

- Low voltage power supply (within the shipping box or 14V, 2A supply.
- CAN card. Recommended "Peak CAN USB" (PCAN USB opto) [3].
- High Voltage power supply (see section 2.3)
- Oscilloscope & Current Probes depending on loads

2.3 Recommended Operating Conditions

The following recommended operating conditions describe the targeted lab testing environment.

Testing beyond the given area may be possible in specific cases when all individual parts are driven within their specification. On the other hand, the evaluation kit with logic, gate driver board together with the power module should not be regarded as a protected system. It is not a considered product for end customers. The intention of the evaluation kit is to support engineers in their first steps designing with the Infineon HybridPACK[™] Drive G2 power module, EiceDriver[™] gate drivers, Xensiv[™] current sensor. Please see also the section 1.1 & 2.4 in order to understand the limitations.

Туре	Symb	Min	Тур	Мах	Conditions
12V Logic Board	12V_IN_LV	10 V	12 V	18 V	Power supply has to deliver up
Supply			14 V		to 25W.
					Isolated power supply
					recommended to avoid GND
					loops with measurement
HV/Working Voltago		0.1/		500.1/	Ear double pulse tests <250mA
(Capacitor DC Link	VDCIIIK_HV	0 0		(for 7E0 V chipcots)	For double pulse lesis ~250mA
				(IOF 750 V Chipsets)	For inductive dummy loads typ
vollage)				950 V	up to 30kW (depending on load
				(for 1200 V chipcots)	tests)
					For inverter operation up to
					300kW (depending on load
					tests)
Interfaces	Vinterfaces	0 V	05 V	5 V	All interfaces like PWM, Enable,
Logicboard and Gate					Apply higher voltages only to
Driver Board					nins which are specified
					explicitly other.
Ambient	T _{amb}	-40°C		85°C	>65°C not recommended
Temperature					
Coolant Fluid	T _F	-40°C		85°C	Typical flowrate 10L/min with
Temperatuire					50%/50% Water/Ethylenglycol
Switching frequency	f _{sw}		10 kHz	20 kHz	For short term operation
					>20kHz might be possible.
					Please apply appropriate
					cooling to the gate driver
					boards.

Table 3 Operating Conditions



Overview Evaluation Tools and Order Numbers

2.4 Limitations of the Evaluation Kit

The inverter evaluation kit was designed with some basic shutdown routines but due to the free parameter setting and undefined lab setup and environment it should not be regarded as a protected system. Most of the protection features can be disabled by the user (e.g. set limit to 1000°C instead of 175°C), which makes tests also under extreme abnormal conditions possible. The evaluation kit is not protected against:

- Over- & undervoltages on the signal connectors.
- Overvoltages of the 12V Board Supply voltage.
- Overvoltages of the HV working voltage.
- Overtemperature of the PCB and module (e.g. operation will not stop without coolant fluid where Tvj=175°C can lead to damage of the power module).
- Testing at high switching frequencies may require an active cooling of the gate driver board especially at high ambient temperatures.
- For short circuit testing please see [2] for more detailed information.

Please note that the above list is giving examples and should not be seen exhaustive.



Interface Descriptions

3 Interface Descriptions

3.1 Interfaces Logic Board



Figure 4 Connectors of Logicboard LB5.x.



3.2 Interfaces Gate Driver Boards (IGBT Versions)



Figure 5 Connectors of Gate Driver Boards (IGBT Versions).



3.3 Interfaces Gate Driver Boards (SIC MOSFET Versions)



Figure 6 Connectors of Gate Driver Boards (SIC MOSFET Versions).



4 Quickstart Guide

This chapter provide a basic introduction of the most common use cases of lab testing. Please note that this evaluation kit is able to operate under high voltages and high currents and power electronics experienced staff is required for testing such parts.

4.1 Typical Setup for Inverter Load Tests

The typical setup for inverter load tests is shown in Figure 7. For light load tests and test with mainly reactive power, the DC supply is normally suffient via the two center DC tabs only. The USB connection is recommended to be disconnected for load tests.



Figure 7 Typical setup for Inverter Load Tests.



4.2 **DEMO Mode (Inverter Operation without Communication)**

For enabling the DEMO-MODE following conditions are required:

- 1) Turn-off all supplies.
- 2) Connect supplies to the inverter evaluation kit (see section 4.1).
- 3) Place DEMO-ENABLE-PLUG (see Figure 8).
- 4) Place DEMO-CONTROL-PLUG (see Figure 8).
- 5) Turn potentiometers for modulation index and output frequency to low position. (i.e. pin 1 & pin2 are connected to GND; see Figure 8)
- 6) Turn-on logic board supply (8..18V). Within the next 3 seconds the inverter starts the operation.
- 7) Turn-on high voltage supply (respect the limits of the evaluation kit).
- 8) The inverter is fully operating and can now be controlled by the potis or external analog 0...5V signals (modulation index and output frequency).



Figure 8 Connection to enable the DEMO-MODE in order to operate the inverter kit without communication in open loop operation.

Only the modulation index and the output frequency can be adjusted in the DEMO-MODE. All other parameters, like switching frequency, dead times, gate driver setting, etc. are set to pre-defined default values from the evaluation kit software. Thus, it should be clear that such default values should not be regarded as optimized parameters, but can be used as a system design starting point and would be compatible to most load and operating conditions.

The parameter values can be configured and non-volatile stored by usage of graphical user interface with ONEEYE tool and USB or CAN bus communication (see section 5.1).



4.2.1 DEMO Mode: Adjusting the Modulation Index

The modulation index can be adjusted by the analog 0..5V signal on pin 1 on the DEMO-CONTROL-PLUG. At the beginning this signal must be low, otherwise the PWM will not be enabled. Than the signal can be adjusted between 0 and 5V and the modulation index of the space vector modulation follows linear from 0% and 100%. In order to ensure a smooth operation a simple ramp-up and ramp-down function is implemented as it can be seen in Figure 9.

At Figure 9 time 0ms the modulation set value is adjusted rapidly from 0V to 5V. The internal ramp function limits the speed and controls the modulation index within 4 seconds from 0% to 100%. In the zoom pictures the resulting phase to phase output voltage at 400V dc working voltage can be seen. At about 3.5 seconds it can be seen, that the space vector modulation comes in the so called overmodulation range resulting in a slight trapeziodial waveform. Not shown but similar would be a ramp down event.

In summary, the operator can adjust the phase to phase output voltage by the modulation index within the limits of the applied working voltage (i.e. DC-Link voltage). By adjusting the output voltage, the currents are also changed depending on the impedance of the load.



Figure 9 Measured event, where operator changes the modulation set value rapidly from 0V to 5V. The internal ramp function ensures a smooth transition from 0% to 100% modulation index. The modulated phase to phase output voltage is consequently increased within the limits of the applied system working voltage.



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DEMO Mode: Adjusting the Output Frequency 4.2.2

The output frequency can be adjusted in similar way as the modulation index. A real example is shown in Figure 10. The 0..5V voltage signal on pin 2 of the DEMO-CONTROL-PLUG sets the output frequency to the load. A ramp function also ensure a smooth transition when a new setpoint is applied (see smooth actual fout value in green). The frequency is ramped within 10 seconds from nearly 0Hz to 100Hz, which are the pre-defined standard values in the software.



Figure 10 Measured event, where operator changes the output frequency set value rapidly from 0V to 5V. The internal ramp function ensures a smooth transition from nearly 0Hz to 100Hz output.



4.3 Inverter Tests (Introduction Open Loop mode)

The easiest way to start with the 1st tests requires:

- Latest OneEye Tool (can be downloaded from the Infineon website under the developer center launcher application)
- OneEye GUI configuration file for the inverter evaluation kit (see USB stick of the shipping content)
- Power supply (8..18V) for the logicboard
- Peak CAN card recommended (USB does not support to monitor the status of the evaluation kit)

If CAN connection is ready, it is very simple to start inverter open loop operation. Please see Figure 12 where the relevant sequence is indicated. After booting the evalkit, the device is in configuration mode.

- 1. You can exit the configuration status by clicking **Exit Configuration**. The status page already shows the measured values.
- 2. Select now the **Open Loop Mode**
- 3. You can now click the **Run** button
- 4. Finally, you can adjust the **Amplitude** (Modulation Index) and the **Speed** (in rpm or in Hz output frequency)

The GUI bring a basic CAN datalogger as well as a software scope function. With the software scope function, it is possible to measure the most relevant paramaters synchronous to the PWM pattern. The data can be saved also



4.4 Inverter Kit Parameter Adjustment

The OneEye GUI brings already a very convenient tool for adjusting parameters of the evaluation kit. Parameters can be read at any time. Please note that most parameters can be adjusted any time but save to ECU is only possible in configuration mode (entered after BOOT, or stop operation and click shutdown and save than the parameters). After the next startup the new parameters will be used for the operation.

You can easily ajust the PWM frequence, deadtime, overvoltage shutdown level, sensor gains etc.

Please note that the part is for evaluation purpose only. The SW will not check if the applied parameters can damage the evaluation kit. It was explicitly desiged with minium protection mechanism in order to make it possible to test the devices to their limits or even beyond their limits.

OneEye	- 🗆 ×
- File Options View Help	
Toolbox	
Home Signalflow (Speed control) Graphs Oscilloscope Configuration Scripts Status	OpenLoop Operation
Read from ECU	(Script) Prep open loop PhCurrent Sense
Save to ECU	(Script) Prep open loop TempSense
Note: press "Read from ECU" to update the OneEye GUI with the values curretly present in the ECU	ON Run DC link voltage 0.0 V
Note: Changing values in this tab will update values of the configuration file in RAM only Notes press Save be ECUT to save modified value from the ECU RAM to the SEC UZEFROM.	Mode Open loop
Ensure the application is sither in "Configuration" or "Shuddown" state for the command to be valid. Note: The below tab gives access and locamenters. Only the tab corresponding to the used hardware are valid	Open loop
Entry item label color code: - green: value is taken over immediately while in run state	Speed 600 rpm (rpm)-1000 1000
- blue: value is taken over only when leaving the configuration state	fout_Hz 50
New Board Init Legicboard Driver board Application Communication	Amplitude 10 % (%)0 100
HPDive 62	CAN DATALOG
Inverter Sensors PBM Vd measurement	enable/disable logging
PWM frequency 10000 Hz VOC analoggain 1 VV	
Deadtime Le-6s s VOC analog offset 0 V Minimum pube 6.999999e-07 s VOC analog firequency 10000 Hz	
Vdc.nominal 400 V	Exit configuration Clearerror Shutdown Controller Reset
	Motor control Wizard IO data Command shortcuts
	Run Run
	Mode Open loop Speed 600.00 rpm
	Open loop Nm
- Sweboda/#XPhase current measurement	This tab is to be used for 'Open loop' and 'open loop with position' mode only
Phase current sensor mode lasmi differential -	Speed 600 rpm (rpm)-1000 1000
Uver autometic setection in plut pULDI	Amplitude 10 %6 (%6) 0 4
Te4973 current sensor sensitivity 55 (galan=30.113.479)	Console
Lumits Phase current Xeef filter cutoff frequency 2000 hts	
Maximum allowed CC link voltage 475 V Phase current sensor Aout gain U 510 A/V Phase current sensor Viel gain U 510 A/V	System [0x7001d6cc: Motor control] control application started ! setup send
Minimum allowed Chi Themperium 550 Degree C. Bhase current sensor Andre Jahr S10 A/V Phase current sensor Andre Jahr S10 A/V	! setup 1b p 6291464 0 ! setup 1b p 6291465 1
Minimum allowed K68 Temperature 300 Degree C	<pre>! setup 1b p 7364609 1.0000000 ! setup 1b p 7364609 0.0000000</pre>
DBD Maximum allowed phase current (0 A DBD PhU Abut current calibrated offset) V DBD PhU Verif current calibrated offset) V	setup 1b p 7364612 0.0000000
DBDP/Wate current calibrated offset 0 V DBDP/Werk current calibrated offset 0 V	! setup db0 p 56623128 0.0000000 ! setup db0 p 56623143 0
	Shell>
Modula temperature measurement exit ikan dAuto Offiet Gain Offiet	System [0x7001d6cc: Motor control] control application stopped
Drivers/CC2 analog plin [7,559999] UV Drivers/CC2 analog effet[4v	System [0x7001d6cc: Motor control] control application started
Dode Tt. 5.5 mvK CPC2 Temp analog pair/270 "CV" COPC2 Temp analog pair/270 "C Vid redundate analog pair 0 NV Vid redundate analog pair/270 "C	Sustan (Aw7001dSec: Motor control) control application storped
CCB Temp U analog grin 146.7 V/ DCB Temp U analog effet [55 V/	System [vx/ourdect: motor control] control application stopped
temptene vi at 32-C (V)[23] "C Temp Vanaleg gain [36,7] "CV" (DCB Temp Vanaleg defet [49] "C	System [0x7001d6cc: Motor control] control application started
UCB temp translogging 1364. JPV DCB Temp translogging 1364. JPV DCB Temp translogging 1364. JPC	>
OneEye Version 2.48.0	

Figure 11 OneEye GUI for adjusting parameters of the evaluation kit.



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Information for Calibration of Phase Current Sensors 4.5

The integrated sensors in the driver board can be calibrated. The most practical way for calibration is with a DC current source connected with the polarity as shown in the example. The current will flow over the power module freewheeling diodes which can be done for some seconds without cooling the power module. If the reference sensor does not match to the current sensor readout from the evaluation kit the gain parameter can be adjusted in the OneEYE GUI under Configuration/DriverBoard. Do not forget to save the values. The same principle can be applied to the other phases



Figure 12 Setup for Gain Parameter Calibration of the Current Sensors (shown only example for phase W).



5 Trouble Shooting and further detailed Information

In case of problems we recommend to use communication to see the SHELL report if any errors are reported by the inverter kit. A next step can be flashing new software and apply initial parameters again. The information for these steps can be found in the following sections.

5.1 Communication with the Evaluation Kit

The inverter evaluation kit has two options for the communction: and USB serial communication and CAN bus communication. In case of operation under high voltage it is highly recommended to use isolated communication (like opto isolated CAN card or opto isolated USB/USB connectors). For operation under high load and high voltages it is known that USB serial connection might fail. CAN bus communication is always the more stable and recommended option.

5.1.1 USB serial Communication

The evaluation kit logicboard can be connected by USB mini cable to the PC. Please ensure that the virtual COM port drivers of the FDTI chipset is installed at your computer. The FDTI drivers will be automatically installed with the Infineon MEMTool. Latest drivers or in case of problems the device drivers can be installed from the manufacturer website: <u>https://ftdichip.com/drivers/vcp-drivers/</u>

After physical connection and power up the logicboard the connection can be established by the Infineon ONEEYE tool (see GUI file prepared for USB) or any available shell program like Tera Term, Putty, etc.

- Port: Typically the COM port with the highest number
- Speed: 115200 kbaud
- Data: 8 bit
- Parity: none
- Stop bits: 1 bit
- Flow control: none
- New-line Receive: CR+LF
- New-line Transmit: CR+LF
- Coding: UTF-8

Trouble Shooting Hint 1: In case of connection problems look at the device manager of your PC operating system. Check if the virtual COM port appears without error indication and reinstall the device drivers if not.

Trouble Shooting Hint 2: Look in ONEEYE Tool under Configuration/Communication. Select COM port and reconnect. Power cycle the board and check if SHELL is reporting a communication.

5.1.2 CAN bus Communication

The recommended communication with the evaluation kit logicboard is via CAN bus communication. The evalkit communication was tested with PEAK CAN cards but others might work too. The correct wiring can be seen in Figure 4. Please ensure that appropriate device drivers are installed at your PC operating system. https://www.peak-system.com/

After phasical connection and power up the logicboard the connection can be established by the Infineon ONEEYE tool (using GUI prepared for CAN). If other than ONEEYE tool is used, the CAN Message catalog can be found in section 5.4.

Quickstart Guide for Inverter Evaluation Kit Inverter Evaluation Kit for HybridPACK[™] Drive G2 Power Modules Trouble Shooting and further detailed Information



Trouble Shooting Hint 1: In case of connection problems look at the device manager of your PC operating system. Check if the CAN card is detected and operating.

Trouble Shooting Hint 2: Look in ONEEYE Tool under Configuration/Communication. Check if CAN card is detected and correct baud rate is set. Please check the DIP switch 1 (see Figure 4) if it matches to the communication baud rate. Click reconnect. Power cycle the board and check if SHELL is reporting a communication.

Trouble Shooting Hint 3: If CAN communication still fails. Try to connect via USB serial communication and check if Logicboard and Software still running.



5.2 Software flashing into Logicboards via Memtool

For flashing a compiled software (.hex) file into the Logicbaords you need:

- Latest Memtool on the PC (download from the Infineon website)
- Power supply (8..18V) for the logicboard
- USB mini cable from Logicboard to the PC

For 1st time use you need to set the correct Microcontroller (target device). A configuration file can be found in the folders of USB stick, which is part of the shipping content of the inverter evaluation kits. After Memtool start please click on Target / Change Copy the configuration file into the folder to browse and select the LB 5.x (Memtool_LB5x.cfg) file. See Figure 13 for a short example how to set the target device.

💑 Infineon - Memtool 4.9 on LB 5.x	– 🗆 🗙	👗 Infineon - Memtool 4.9 on LB 5.x	- 🗆 X
File Target Device Log Help		File Target Device Log Help	
Process Owney Process Setup Open File Process Process Setup Open File Open File Not State Setup Open File Open File Open File Setup Open File Open File Open File <th>Program all Program all Program all Verify all OW Product UCDs Vote Soluto Soluto Soluto Soluto</th> <th>Fig: El & SALCTE2- Manono Chasta Select Target Configuration Last Used Brows Cuber Documents Infraord/MT 4 Stragets </th> <th>Propin al Very al Vory al</th>	Program all Program all Program all Verify all OW Product UCDs Vote Soluto Soluto Soluto Soluto	Fig: El & SALCTE2- Manono Chasta Select Target Configuration Last Used Brows Cuber Documents Infraord/MT 4 Stragets	Propin al Very al Vory al
Connect Target not connected	Help Ext	Default New Copy Edit Remove OK Cancel Help	Help Exit

Figure 13 Prepare Memtool for the Target Device.

Once you have set the correct target device please click first on connect. If the status message appear as shown in the Figure 14 "Ready for Memtool Command" please continue with.

2) Open File.. (select the .hex file in the popup dialog)

- 3) Select All
- 4) Add Sel >>
- 5) Program all

6) if all is OK and no errors reported you can disconnect the device and close Memtool.

Kinfineon - Memtool 4.9 on LB 5.x		- 🗆 X
File :	FLASH/OTP - Memory Device PFLASH: 10 MByte OnChip Program FLASH Ind Start End 0 0xA0000000 0xA0000000 0xA0003FFF 1 0xA0000000 0xA00008000 0xA0003FFF 2 0xA0000000 0 0xA000000 0 0xA0000000 0 0xA000000 0 0xA000000 0 0xA000000 0 0xA000000 0 0xA000000 0 0xA0013FFF 16K 16K 5 0xA0014000 0 0xA0010000 0 0xA0012FFF 16K 16K 7 0xA0012000 0xA0012000 0xA0023FFF 10 0xA0022000 0xA0022000 0xA0023FFF 11 0xA0022000 0xA0022000 0xA0023FFF 12 0xA0030000 0xA0033FFF 16K 12 0xA0030000 0xA0033FFF 16K 12 Remove Sel. <th>Frase Program Verify Verify W Protect UCBs Info State Setup</th>	Frase Program Verify Verify W Protect UCBs Info State Setup
Cinfineon 1 Tool Disconnect	Ready for Memtool Command	Help Exit

Figure 14 Flash a compiled software (.hex) file into the target device.



5.3 New Boards Inizialization

In the OneEye GUI you can find under Configuration the New Board Init structure. Simply follow this sequence and the part will initialize with default parameters (see Figure 15). The boards are now ready for the 1st tests.



Figure 15 OneEye GUI for New Board Init sequence.



5.4 Extract CAN0 Messages

5.4.1 RX Messages (Receive by the Logicboard)

CAN ID	DLC	Interval	Size (bit)	Offset (bit)	Field	Description
0x01	2	10 ms	1	9	cmd.clearError	Requect to clear the errors and exit the error state
			1	11	cmd.configExit	Request to exit the configuration state
0.00		10	32	0	cmd.currentId	Controller: id command (A)
0x02	8	10 ms	32	32	cmd.currentlq	Controller: iq command (A)
0x04	4	10 ms	32	0	cmd.torque	Controller: torque command (Nm)
0x05	4	10 ms	32	0	cmd.speed	Controller: speed command (rpm)
0x0E	4	10 ms	32	0	cmd.amplitude	Controller: amplitude command (0-100)
0.40		10	8	0	cmd.mode	Controller: mode command
0x13	2	10 ms	1	8	cmd.run	Controller: run command
0xFE	1	1 ms	8	0	protocol.out	Shell protocol (OUT)

5.4.2 TX Messages (Transmitted by the Logicboard)

CAN ID	DLC	Interval	Size (bit)	Offset (bit)	Field	Description
			1	9	status.error	Error status
			1	10	status.clearError	Clear error request status
0x06	4	10 ms	1	11	status.configExit	Exit configuration state status
			8	24	status.ecuState	ECU state
0.07	0	10 ma	32	0	status.torque	Controller: torque (Nm)
0x07	0	10 1115	32	32	status.speed	Controller: speed (rpm)
0x08 8			32	0	status.vdc	Controller: Vdc (V)
	8	10 ms	32	32	status.currentW_DB	Controller: Driverboard Phase W current (A)
	0	10 mg	32	0	status.switchTemp_ U_HS	Controller: Switch Temp HS Phase U (°C)
0x09	o	i o ms	32	32	status.switchTemp_ V_HS	Controller: Switch Temp HS Phase V (°C)
0x0A	4	10 ms	32	0	status.switchTemp_ W_HS	Controller: Switch Temp HS Phase W (°C)
0.40F	0	1	32	0	status.currentU_DB	Controller: Driverboard Phase W current (A)
UXUF	o	n ms	32	32	status.currentV_DB	Controller: Driverboard Phase W current (A)
0x10	8	10 ms	32	32	status.mechPos	Controller: position (rad)
0.4.0	0	10 ma	8	0	status.mode	Controller: mode status
UXIZ	2	TO MS	1	8	status.run	Controller: run status
0x15	8	100 ms	32	0	status.kl30	KL30
0x16	4	100 ms	32	0	status.motorTemp0	Motor temp 0



Quickstart Guide for Inverter Evaluation Kit

Inverter Evaluation Kit for HybridPACK[™] Drive G2 Power Modules

Trouble Shooting and further detailed Information



CAN ID	DLC	Interval	Size (bit)	Offset (bit)	Field	Description
0x17	8	100 ms	32	0	status.ain0	LB analog input 0
	U	100 113	32	32	status.ain1	LB analog input 1
0x18	8	100 ms	32	0	status.ain2	LB analog input 2
	S .		32	32	status.ain3	LB analog input 3
0v10	8	100 ms	32	0	status.ain4	LB analog input 4
0,15	0	100 113	32	32	status.ain5	LB analog input 5
0x1A	2	100 ms	8	0	status.din	LB digital inputs
	Z	100 113	8	8	status.dout	LB digital outputs
0x1B	Q	10 ms	32	0	status.currentU_LB	Controller: External Logicboard Phase U current (A)
	0	10 113	32	32	status.currentV_LB	Controller: External Logicboard Phase V current (A)
			32	0	status.currentW_LB	Controller: External Logicboard Phase W current (A)
0x1D	8	10 ms	32	32	status.switchTemp_ U_LS	Controller: Switch Temp LS Phase U (°C) or VCC2 driver supply at SIC MOSFET
			32	0	status.switchTemp_ V_LS	Controller: Switch Temp LS Phase V (°C) or driver board temp at SIC MOSFET
0x1E	8	10 ms	32	32	status.switchTemp_ W_LS	Controller: Switch Temp LS Phase V (°C) or redundant Vdc measurement at SIC MOSFET
0×1E	Q	10	32	0	status.currentSensor TempU	Driver Board Current Sensor Chip Temp Phase U
		10 116	32	32	status.currentSensor TempV	Driver Board Current Sensor Chip Temp Phase V
0x26	4	10 ms	32	0	status.currentSensor TempW	Driver Board Current Sensor Chip Temp Phase W
0xFF	8	1 ms	8	0	protocol.in	Shell protocol (IN)



5.5 FAQ Frequent Asked Questions

Q1: Inverter Kit shows a wrong scaling of e.g. Vdc voltages, currents, etc.

A1: Maybe a wrong gain parameter was applied. Please check in the configuration tab in OneEYE tool the corresponding gain/offset parameters and adjust if needed.



References and Revision History

6 **References and Revision History**

The referenced application notes can be found at http://www.infineon.com

- [1] Infineon Application Note AN-G2-ASSEMBLY, "Assembly Instructions for the HybridPACK[™] Drive G2 power modules".
- [2] Infineon Application Note AN-G2-SICSC, "Automotive SiC MOSFET G2 short circuit detection and turn-off with 1EDI3035AS gate driver".
- [3] Peak-Systems, <u>https://www.peak-system.com/</u>

Revision History					
Date	Version	Changed By	Change Description		
2023-09	0.1	T. Reiter	Initial Version		
2024-01	0.2	T. Reiter	Minor update due to change of product order names following a comprehensive type designation tree.		

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