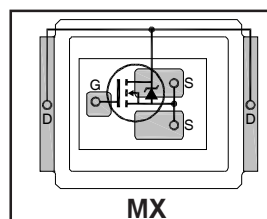


- RoHS Compliant Containing No Lead and Halogen Free ①
- Integrated Monolithic Schottky Diode
- Low Profile (<0.7 mm)
- Dual Sided Cooling Compatible ①
- Ultra Low Package Inductance
- Optimized for High Frequency Switching ①
- Ideal for CPU Core DC-DC Converters
- Optimized for Sync. FET socket of Sync. Buck Converter①
- Low Conduction and Switching Losses
- Compatible with existing Surface Mount Techniques ①
- 100% R_g tested

HEXFET® Power MOSFET plus Schottky Diode ②
Typical values (unless otherwise specified)

V _{DSS}		V _{GS}		R _{DS(on)}		R _{DS(on)}	
30V max		±20V max		1.8mΩ@ 10V		2.8mΩ@ 4.5V	
Q _{g tot}	Q _{gd}	Q _{gs2}	Q _{rr}	Q _{oss}	V _{gs(th)}		
25nC	6.7nC	3.0nC	29nC	22nC	1.8V		



Applicable DirectFET Outline and Substrate Outline (see p.7,8 for details)①

SQ	SX	ST		MQ	MX	MT	MP			
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Description

The IRF8306MPbF combines the latest HEXFET® Power MOSFET Silicon technology with the advanced DirectFET™ packaging to achieve the lowest on-state resistance in a package that has the footprint of a SO-8 and only 0.7 mm profile. The DirectFET package is compatible with existing layout geometries used in power applications, PCB assembly equipment and vapor phase, infra-red or convection soldering techniques. Application note AN-1035 is followed regarding the manufacturing methods and processes. The DirectFET package allows dual sided cooling to maximize thermal transfer in power systems, improving previous best thermal resistance by 80%.

The IRF8306MPbF balances industry leading on-state resistance while minimizing gate charge along with ultra low package inductance to reduce both conduction and switching losses. This part contains an integrated Schottky diode to reduce the Q_{rr} of the body drain diode further reducing the losses in a Synchronous Buck circuit. The reduced losses make this product ideal for high frequency/high efficiency DC-DC converters that power high current loads such as the latest generation of microprocessors. The IRF8306MPbF has been optimized for parameters that are critical in synchronous buck converter's Sync FET sockets.

Orderable part number	Package Type	Standard Pack		Note
		Form	Quantity	
IRF8306MTRPbF	DirectFET MX	Tape and Reel	4800	"TR" suffix
IRF8306MTR1PbF	DirectFET-MX	Tape and Reel	1000	"TR1" suffix EOL notice # 264

Absolute Maximum Ratings

	Parameter	Max.	Units
V _{DS}	Drain-to-Source Voltage	30	V
V _{GS}	Gate-to-Source Voltage	±20	
I _D @ T _A = 25°C	Continuous Drain Current, V _{GS} @ 10V ③	23	A
I _D @ T _A = 70°C	Continuous Drain Current, V _{GS} @ 10V ③	18	
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V ④	140	
I _{DM}	Pulsed Drain Current ⑤	180	
E _{AS}	Single Pulse Avalanche Energy ⑥	230	mJ
I _{AR}	Avalanche Current ⑤	18	A

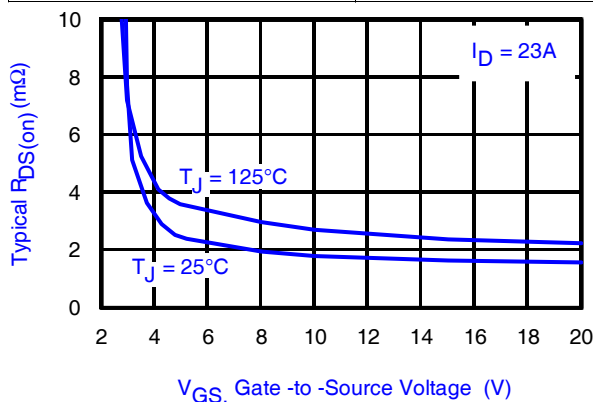


Fig 1. Typical On-Resistance vs. Gate Voltage

Notes:

- ① Click on this section to link to the appropriate technical paper.
- ② Click on this section to link to the DirectFET Website.
- ③ Surface mounted on 1 in. square Cu board, steady state.

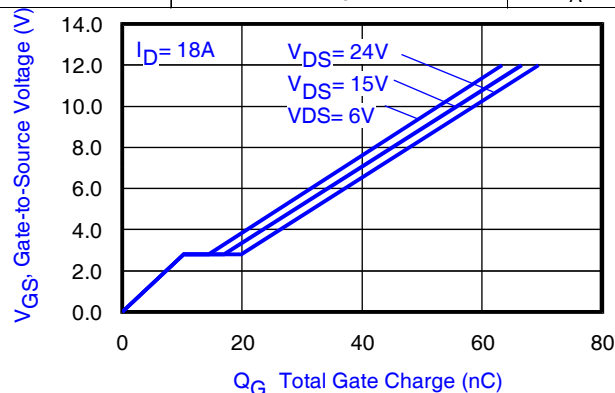


Fig 2. Typical Total Gate Charge vs. Gate-to-Source Voltage

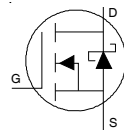
- ④ T_C measured with thermocouple mounted to top (Drain) of part.
- ⑤ Repetitive rating; pulse width limited by max. junction temperature.
- ⑥ Starting T_J = 25°C, L = 1.37mH, R_G = 50Ω, I_{AS} = 18A.

Static @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
B _V DSS	Drain-to-Source Breakdown Voltage	30	—	—	V	V _{GS} = 0V, I _D = 1.0mA
ΔB _V DSS/ΔT _J	Breakdown Voltage Temp. Coefficient	—	2.7	—	mV/°C	Reference to 25°C, I _D = 6mA
R _{DS(on)}	Static Drain-to-Source On-Resistance	—	1.8	2.5	mΩ	V _{GS} = 10V, I _D = 23A ⑦
		—	2.8	3.6		V _{GS} = 4.5V, I _D = 18A ⑦
V _{GS(th)}	Gate Threshold Voltage	1.35	1.8	2.35	V	V _{DS} = V _{GS} , I _D = 100μA
ΔV _{GS(th)} /ΔT _J	Gate Threshold Voltage Coefficient	—	-4.8	—	mV/°C	V _{DS} = V _{GS} , I _D = 10mA
I _{DSS}	Drain-to-Source Leakage Current	—	—	500	μA	V _{DS} = 24V, V _{GS} = 0V
		—	—	5.0	mA	V _{DS} = 24V, V _{GS} = 0V, T _J = 125°C
I _{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	V _{GS} = 20V
	Gate-to-Source Reverse Leakage	—	—	-100		V _{GS} = -20V
g _{fs}	Forward Transconductance	61	—	—	S	V _{DS} = 15V, I _D = 18A
Q _g	Total Gate Charge	—	25	38	nC	V _{DS} = 15V V _{GS} = 4.5V I _D = 18A See Fig. 15
Q _{gs1}	Pre-V _{th} Gate-to-Source Charge	—	7.3	—		
Q _{gs2}	Post-V _{th} Gate-to-Source Charge	—	3.0	—		
Q _{gd}	Gate-to-Drain Charge	—	6.7	—		
Q _{godr}	Gate Charge Overdrive	—	8.0	—		
Q _{sw}	Switch Charge (Q _{gs2} + Q _{gd})	—	9.7	—		
Q _{oss}	Output Charge	—	22	—	nC	V _{DS} = 16V, V _{GS} = 0V
R _G	Gate Resistance	—	1.3	—	Ω	
t _{d(on)}	Turn-On Delay Time	—	16	—	ns	V _{DD} = 15V, V _{GS} = 4.5V ⑦ I _D = 18A R _G = 1.8Ω See Fig. 17
t _r	Rise Time	—	34	—		
t _{d(off)}	Turn-Off Delay Time	—	19	—		
t _f	Fall Time	—	19	—		
C _{iss}	Input Capacitance	—	4110	—	pF	V _{GS} = 0V V _{DS} = 15V f = 1.0MHz
C _{oss}	Output Capacitance	—	970	—		
C _{rss}	Reverse Transfer Capacitance	—	340	—		

Diode Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	—	—	23	A	MOSFET symbol showing the integral reverse p-n junction diode.
I _{SM}	Pulsed Source Current (Body Diode) ⑤	—	—	180		
V _{SD}	Diode Forward Voltage	—	0.7	0.75	V	T _J = 25°C, I _S = 18A, V _{GS} = 0V ⑦
t _{rr}	Reverse Recovery Time	—	21	32	ns	T _J = 25°C, I _F = 18A
Q _{rr}	Reverse Recovery Charge	—	29	44	nC	di/dt = 300A/μs ⑦


Notes:

⑦ Pulse width ≤ 400μs; duty cycle ≤ 2%.

Absolute Maximum Ratings

	Parameter	Max.	Units
P_D @ $T_A = 25^{\circ}\text{C}$	Power Dissipation ③	2.1	W
P_D @ $T_A = 70^{\circ}\text{C}$	Power Dissipation ③	1.3	
P_D @ $T_C = 25^{\circ}\text{C}$	Power Dissipation ④	75	
T_P	Peak Soldering Temperature	270	$^{\circ}\text{C}$
T_J	Operating Junction and	-40 to + 150	
T_{STG}	Storage Temperature Range		

Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Junction-to-Ambient ③⑩	—	60	$^\circ\text{C/W}$
$R_{\theta JA}$	Junction-to-Ambient ⑧⑩	12.5	—	
$R_{\theta JA}$	Junction-to-Ambient ⑨⑩	20	—	
$R_{\theta JC}$	Junction-to-Case ④⑩	—	1.66	
$R_{\theta J-PCB}$	Junction-to-PCB Mounted	1.0	—	
	Linear Derating Factor ③	0.017		$\text{W}/^\circ\text{C}$

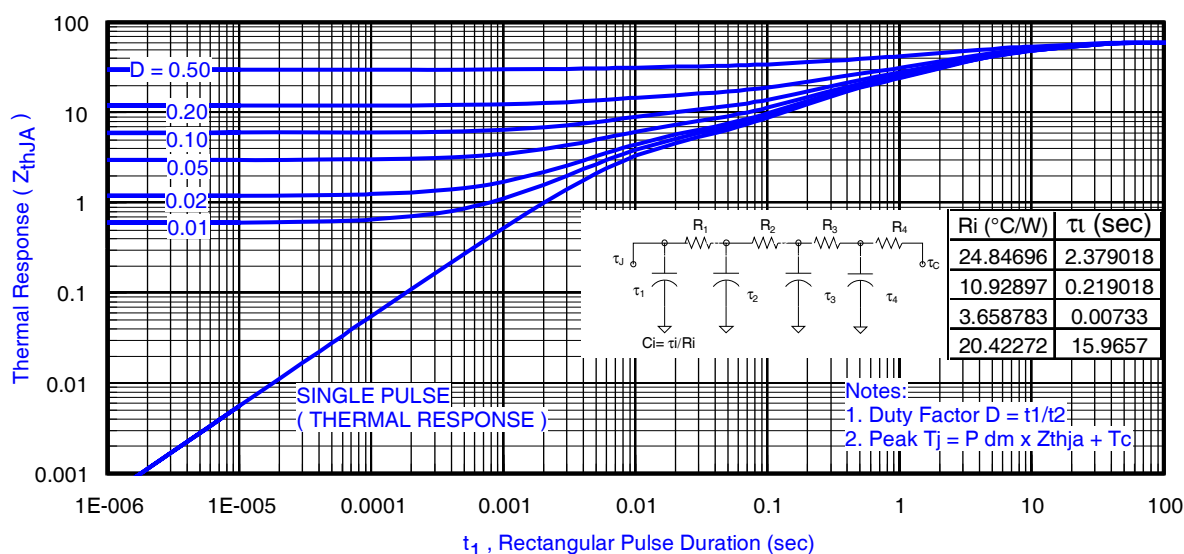
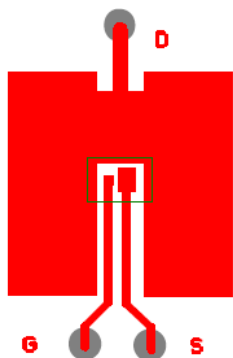


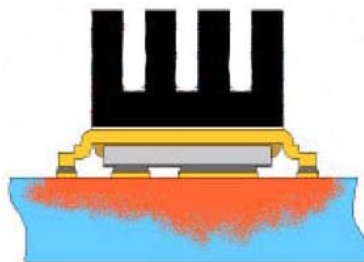
Fig 3. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient ③

Notes:

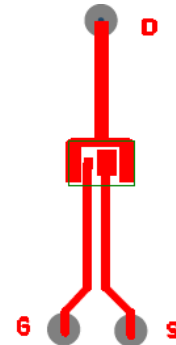
- ③ Used double sided cooling, mounting pad with large heatsink.
- ⑧ R_{θ} is measured at T_J of approximately 90°C .
- ⑨ Mounted on minimum footprint full size board with metalized back and with small clip heatsink.



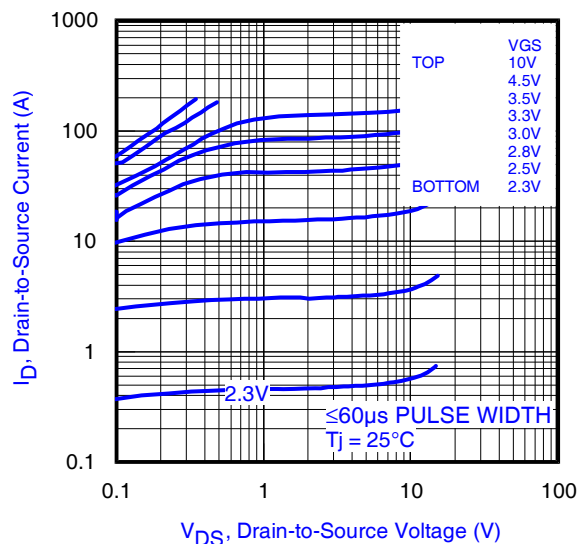
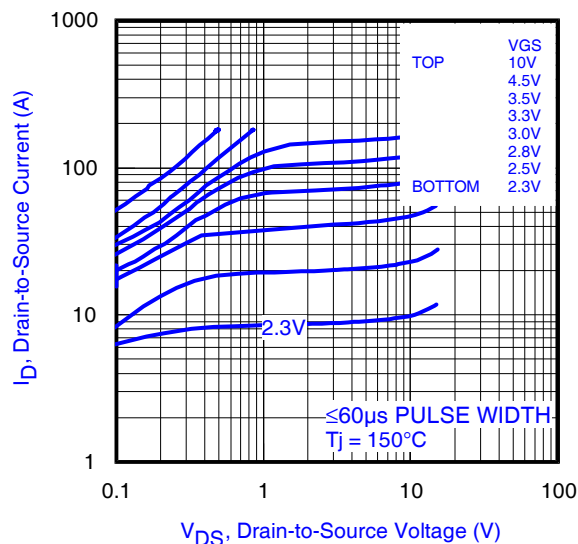
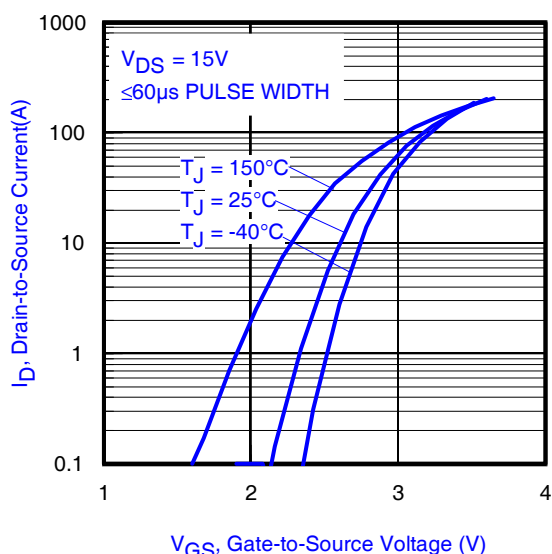
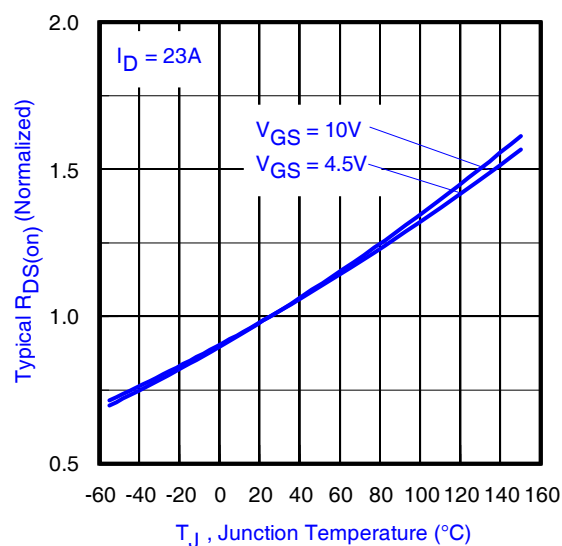
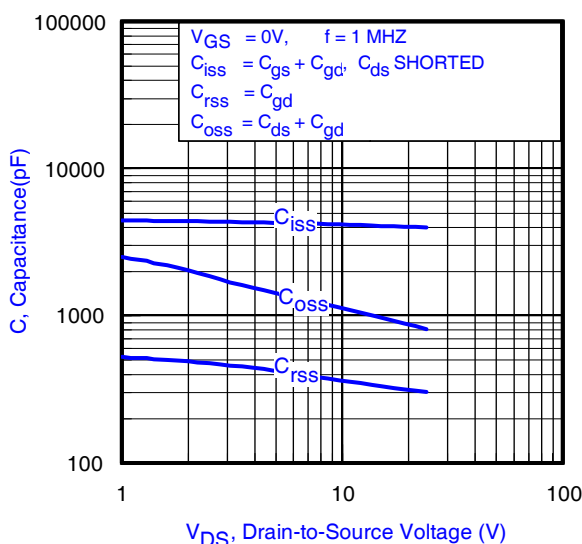
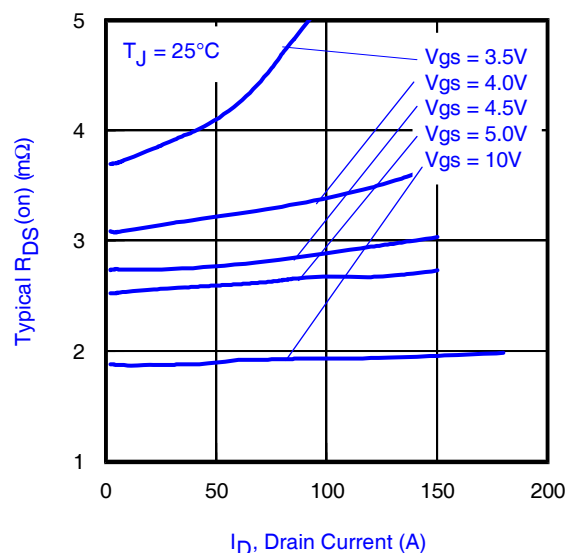
③ Surface mounted on 1 in. square Cu (still air).

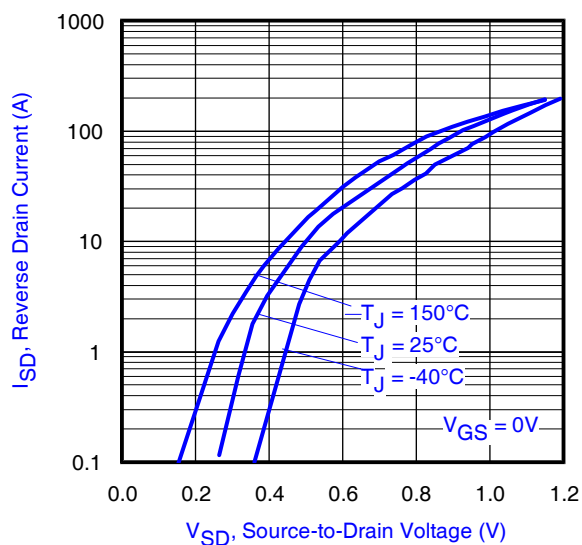
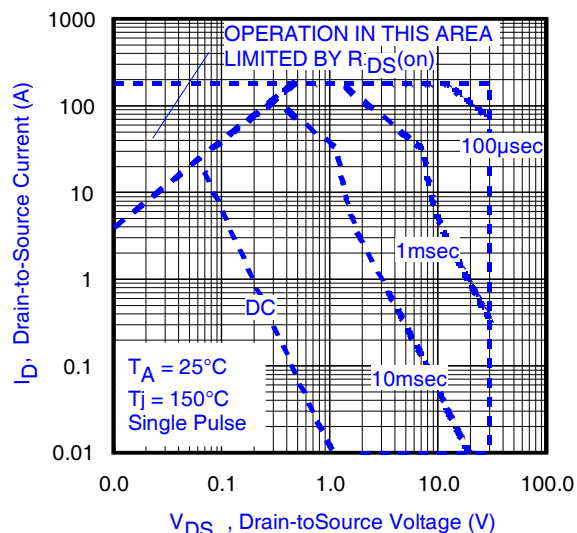
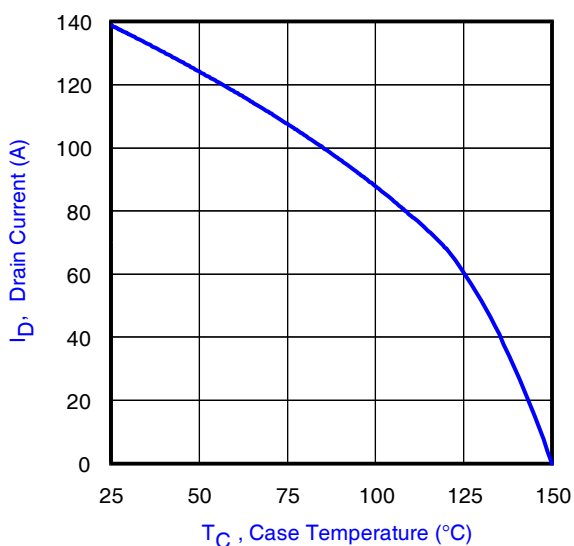
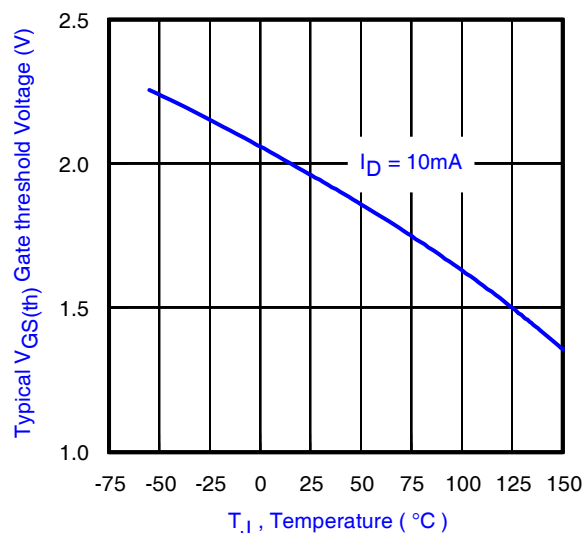
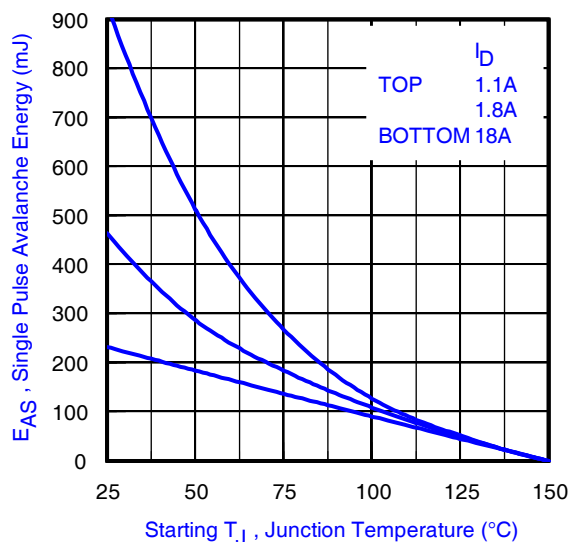


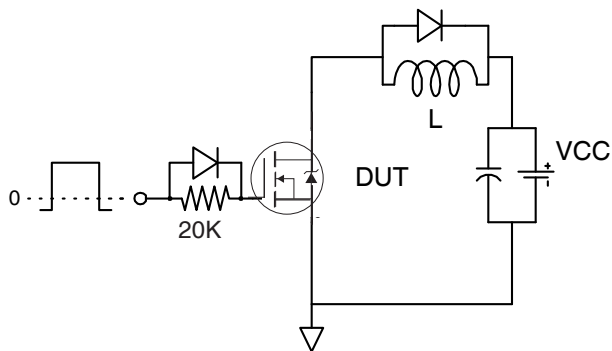
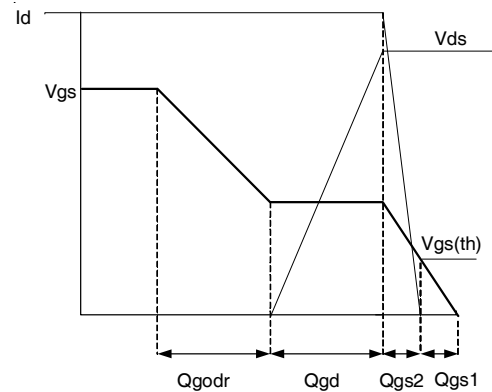
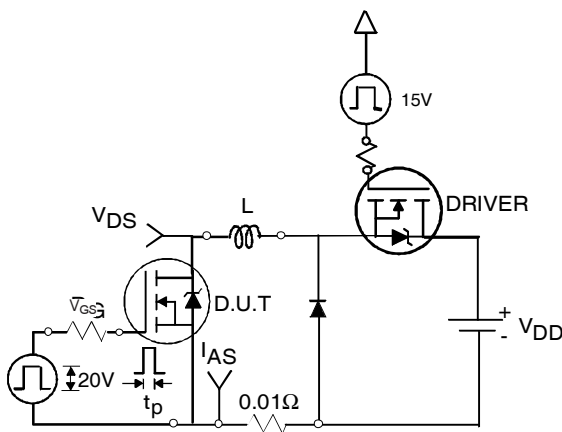
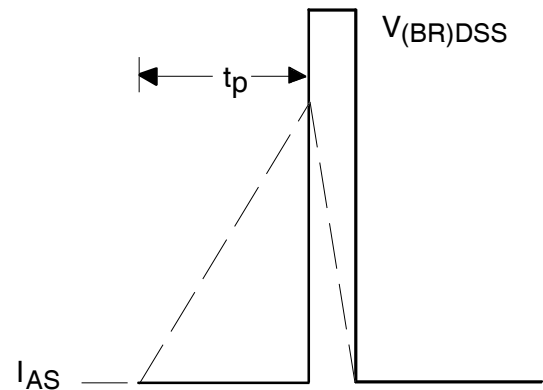
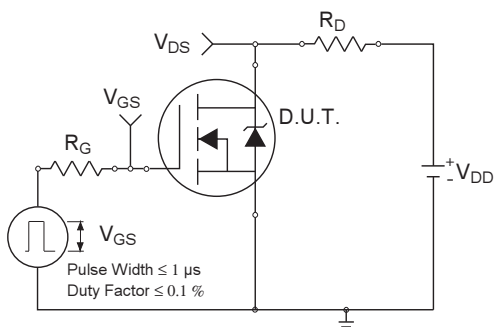
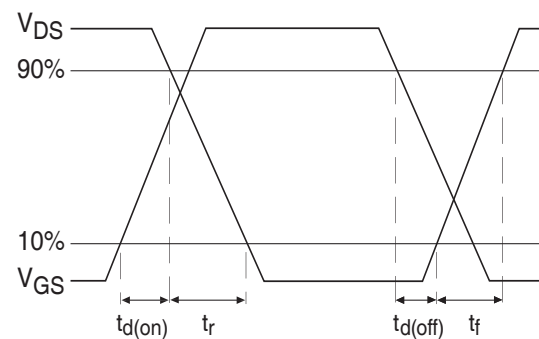
⑧ Mounted to a PCB with small clip heatsink (still air)



⑨ Mounted on minimum footprint full size board with metalized back and with small clip heatsink (still air)


Fig 4. Typical Output Characteristics

Fig 5. Typical Output Characteristics

Fig 6. Typical Transfer Characteristics

Fig 7. Normalized On-Resistance vs. Temperature

Fig 8. Typical Capacitance vs. Drain-to-Source Voltage

Fig 9. Typical On-Resistance vs. Drain Current and Gate Voltage


Fig 10. Typical Source-Drain Diode Forward Voltage

Fig11. Maximum Safe Operating Area

Fig 12. Maximum Drain Current vs. Case Temperature

Fig 13. Typical Threshold Voltage vs. Junction Temperature

Fig 14. Maximum Avalanche Energy vs. Drain Current


Fig 15a. Gate Charge Test Circuit

Fig 15b. Gate Charge Waveform

Fig 16a. Unclamped Inductive Test Circuit

Fig 16b. Unclamped Inductive Waveforms

Fig 17a. Switching Time Test Circuit

Fig 17b. Switching Time Waveforms

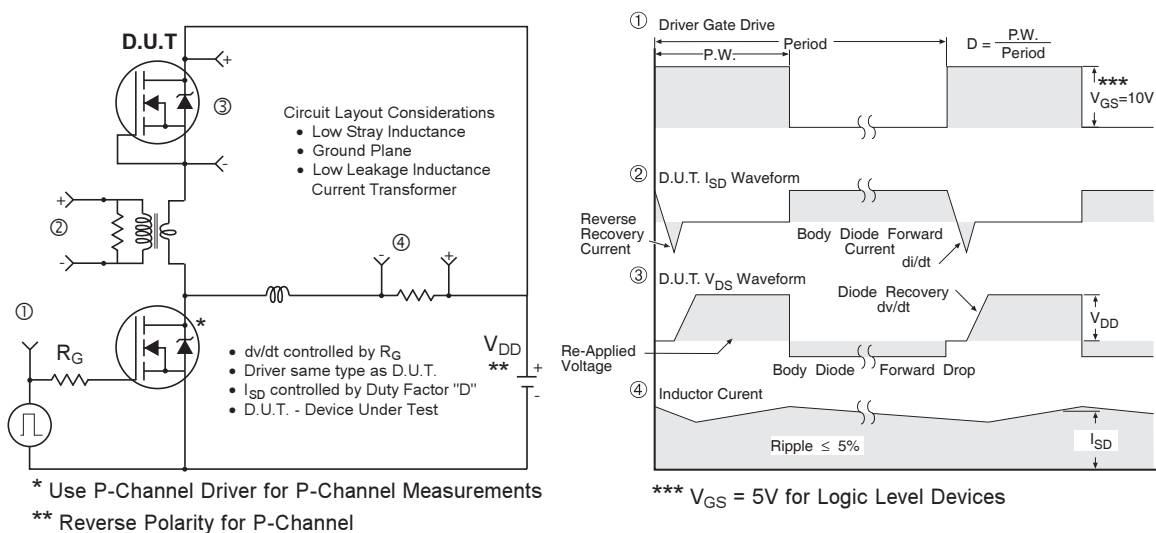
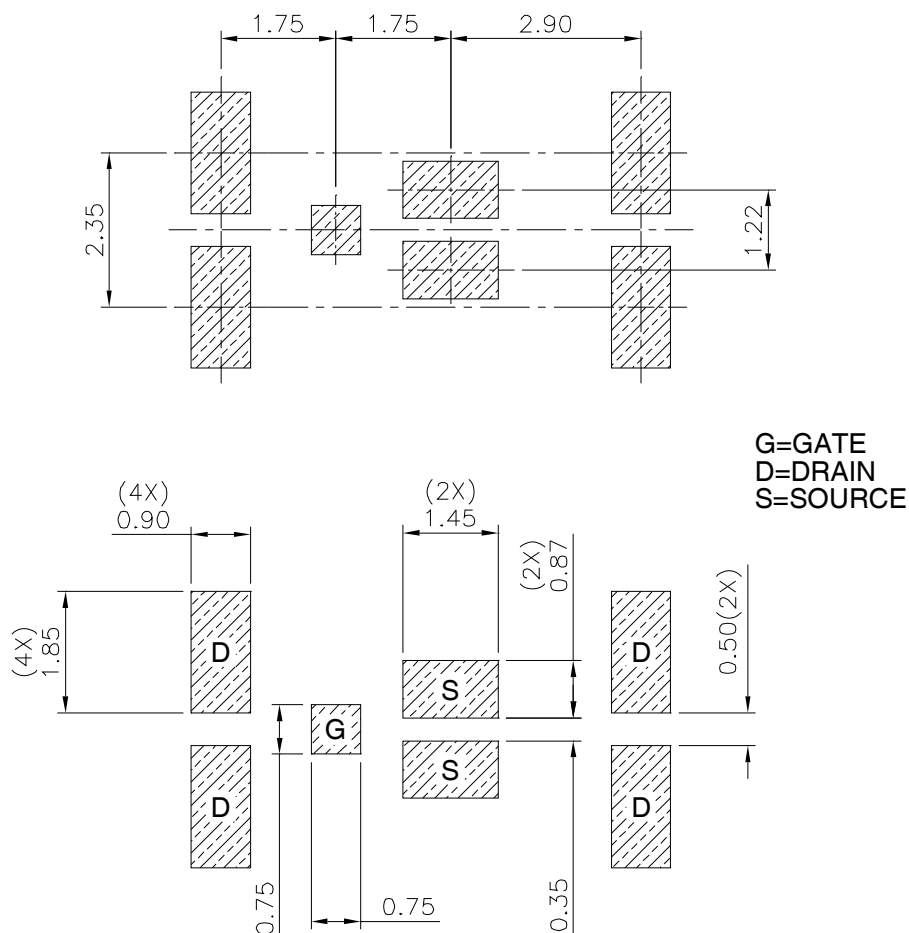


Fig 18. Diode Reverse Recovery Test Circuit for HEXFET® Power MOSFETs

DirectFET™ Board Footprint, MX Outline (Medium Size Can, X-Designation).

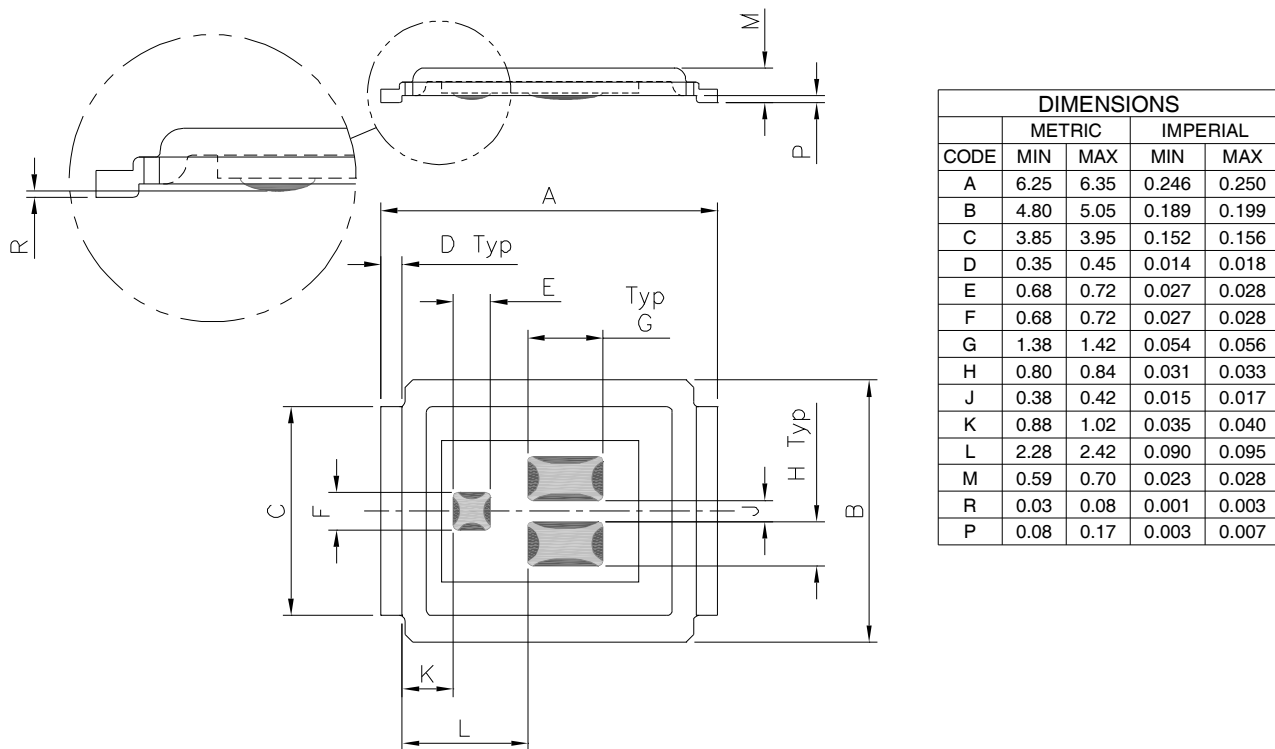
Please see DirectFET application note AN-1035 for all details regarding the assembly of DirectFET. This includes all recommendations for stencil and substrate designs.



Note: For the most current drawing please refer to IR website at: <http://www.irf.com/package/>

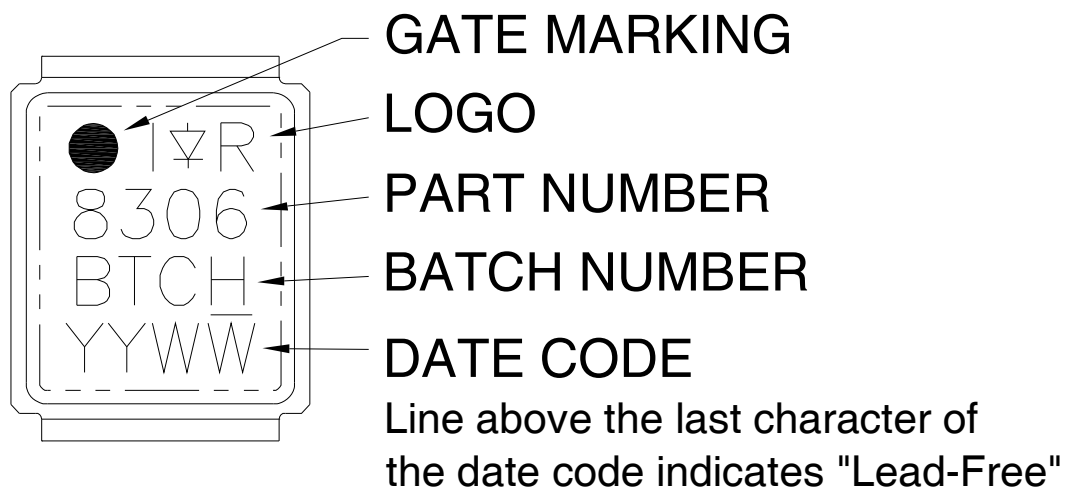
DirectFET™ Outline Dimension, MX Outline (Medium Size Can, X-Designation)

Please see AN-1035 for DirectFET assembly details, stencil and substrate design recommendations



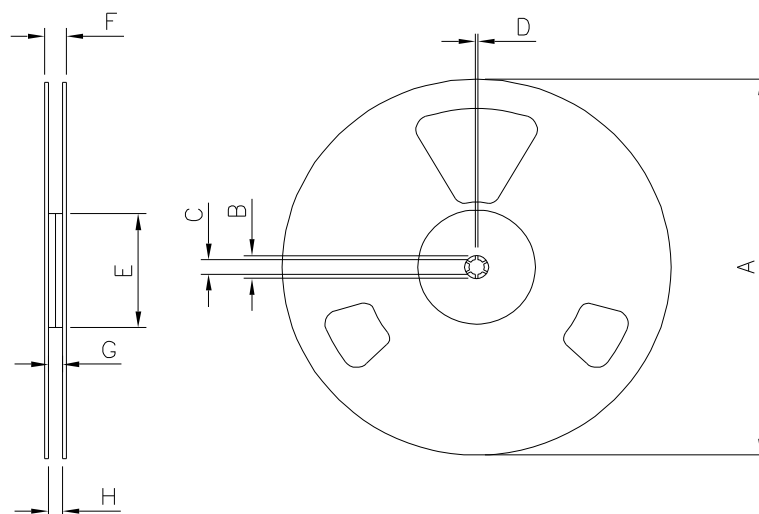
Dimensions are shown in
millimeters (inches)

DirectFET™ Part Marking



Note: For the most current drawing please refer to IR website at: <http://www.irf.com/package/>

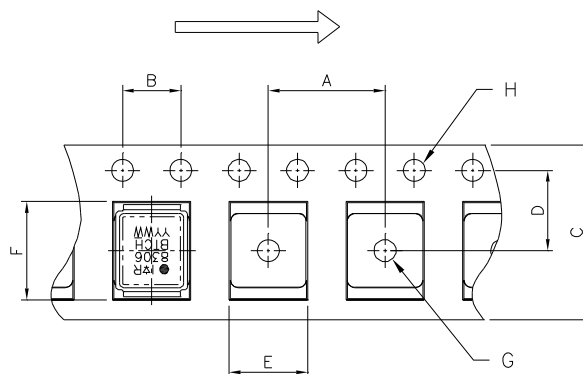
DirectFET™ Tape & Reel Dimension (Showing component orientation).



NOTE: Controlling dimensions in mm Std reel quantity is 4800 parts. (ordered as IRF8306MPBF).

REEL DIMENSIONS				
STANDARD OPTION(QTY 4800)				
	METRIC		IMPERIAL	
CODE	MIN	MAX	MIN	MAX
A	330.0	N.C	12.992	N.C
B	20.2	N.C	0.795	N.C
C	12.8	13.2	0.504	0.520
D	1.5	N.C	0.059	N.C
E	100.0	N.C	3.937	N.C
F	N.C	18.4	N.C	0.724
G	12.4	14.4	0.488	0.567
H	11.9	15.4	0.469	0.606

LOADED TAPE FEED DIRECTION



NOTE: CONTROLLING DIMENSIONS IN MM

DIMENSIONS				
	METRIC		IMPERIAL	
CODE	MIN	MAX	MIN	MAX
A	7.90	8.10	0.311	0.319
B	3.90	4.10	0.154	0.161
C	11.90	12.30	0.469	0.484
D	5.45	5.55	0.215	0.219
E	5.10	5.30	0.201	0.209
F	6.50	6.70	0.256	0.264
G	1.50	N.C	0.059	N.C
H	1.50	1.60	0.059	0.063

Note: For the most current drawing please refer to IR website at: <http://www.irf.com/package/>

Qualification Information[†]

Qualification level	Consumer ^{††}	
	(per JEDEC JESD47F ^{†††} guidelines)	
	Comments: This family of products has passed JEDEC's Industrial qualification. IR's Consumer qualification level is granted by extension of the higher Industrial level.	
Moisture Sensitivity Level	DFET	MSL1 (per JEDEC J-STD-020D ^{†††})
RoHS Compliant	Yes	

[†] Qualification standards can be found at International Rectifier's web site

<http://www.irf.com/product-info/reliability>

^{††} Higher qualification ratings may be available should the user have such requirements.

Please contact your International Rectifier sales representative for further information:

<http://www.irf.com/whoto-call/salesrep/>

^{†††} Applicable version of JEDEC standard at the time of product release.

Revision History

Date	Comments
5/7/2014	• Updated ordering information to reflect the End-Of-life (EOL) of the mini-reel option (EOL notice #264). • Updated data sheet based on corporate template.

International
 Rectifier

IR WORLD HEADQUARTERS: 101 N. Sepulveda Blvd., El Segundo, California 90245, USA

To contact International Rectifier, please visit <http://www.irf.com/whoto-call/>

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