

Low dropout voltage regulator



Features

- Output voltages: 3.3 V, 5.0 V
- Output current up to 400 mA
- Low current consumption
- Wide input voltage functional range up to 40 V / max. rating 45 V
- · Low dropout voltage
- · Output current limitation
- · Reverse polarity protection
- Overtemperature shutdown
- Wide temperature range, -40°C to 125°C
- Green product (RoHS compliant)

Potential applications

- Manufacturing automation
- Appliances
- HDTV and game consoles
- Network routers

Product validation

Qualified for industrial applications according to the relevant tests of JEDEC.

Description

The IFX25001 is a low dropout linear voltage regulator available as 3.3 V and 5 V version. Capable of supplying continuous output currents up to 400 mA and offering a wide functional input voltage range up to 40 V the IFX25001 is suitable for a large variety of applications. In addition it is also protected against overload, short circuit and overtemperature conditions.

Туре	Package	Marking
IFX25001 ME V33	PG-SOT223-4	25001B
IFX25001 TF V33	PG-TO252-3	2500133
IFX25001 TF V50	PG-TO252-3	2500150
IFX25001 TC V50	PG-TO263-3	25001V50





Block diagram

Block diagram 1

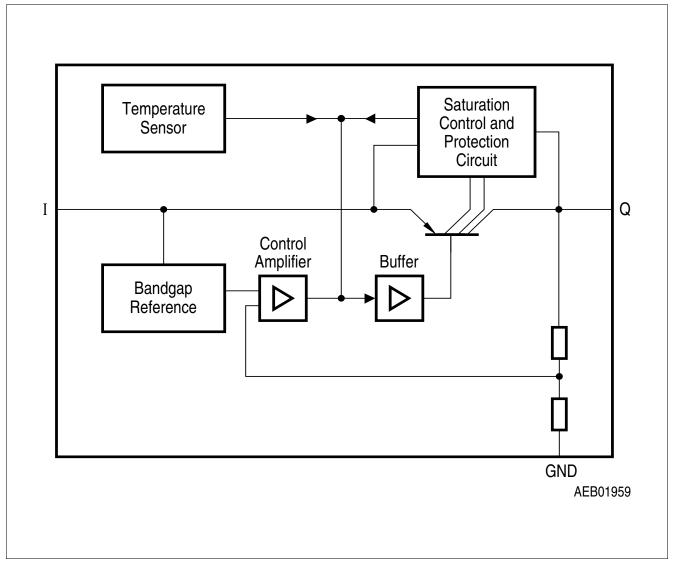


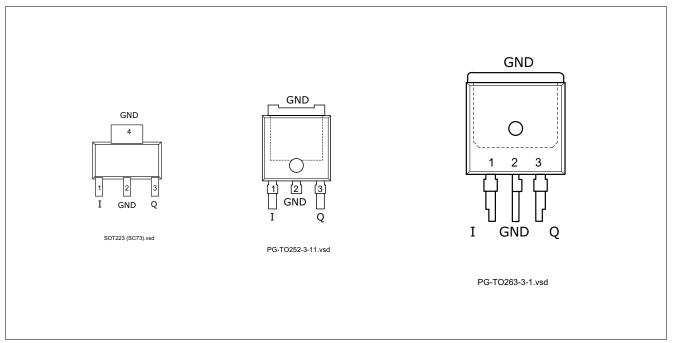
Figure 1 Block diagram



Pin configuration

Pin configuration 2

Pin assignment PG-SOT223-4, PG-TO252-3, PG-TO263-3 2.1



Pin Configuration (top view) Figure 2

Pin definitions and functions PG-SOT223-4, PG-TO252-3, PG-TO263-3 2.2

Pin No.	Symbol	Function
1	I	Input connect Input pin to positive DC voltage source (e.g. battery); a small filter capacitor connected close to the Input pin and GND is recommended
2	GND	Ground internally connected to heat slug pin
3	Q	Output connect a capacitor close to the Output pin and GND according to the values specified in "Functional range" on Page 4
4 / Heat slug	GND	Heat Slug internally connected to GND pin; connect to heatsink to improve thermal performance

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General product characteristics

3 **General product characteristics**

Absolute maximum ratings 3.1

Absolute maximum ratings¹⁾

 T_i = -40 °C to 150 °C; all voltages with respect to ground, (unless otherwise specified)

Pos.	Parameter	Symbol	Lin	nit Values	Unit	Test Condition
			Min.	Max.		
Input I					<u> </u>	
3.1.1	Voltage	V_1	-42	45	V	_
Output	: Q				<u> </u>	
3.1.2	Voltage	V_{Q}	-1	40	V	_
Tempe	rature	,	·	-	1	
3.1.3	Junction temperature	$T_{\rm j}$	-40	150	°C	_
3.1.4	Storage temperature	$T_{ m stg}$	-50	150	°C	_

¹⁾ not subject to production test, specified by design

Stresses above the ones listed here may cause permanent damage to the device. Exposure to Note:

absolute maximum rating conditions for extended periods may affect device reliability.

Note: Integrated protection functions are designed to prevent IC destruction under fault conditions

described in the data sheet. Fault conditions are considered as "outside" normal operating range.

Protection functions are not designed for continuous repetitive operation.

3.2 **Functional range**

Pos.	Parameter	Symbol	Lim	it Values	Unit	Remarks
			Min.	Max.		
3.2.1	Input voltage	V_1	4.7	40	V	IFX25001 ME V33 IFX25001 TF V33
3.2.2		V_1	5.5	40	V	IFX25001 TF V50 IFX25001 TC V50
3.2.5	Output capacitor's	C_{Q}	22	_	μF	1)
3.2.6	Requirements for stability	$ESR(C_{Q})$	_	3	Ω	2)
3.2.7	Junction temperature	$T_{\rm j}$	-40	125	°C	_

¹⁾ the minimum output capacitance requirement is applicable for a worst case capacitance tolerance of 30%

Note:

Within the functional or operating range, the IC operates as described in the circuit description. The electrical characteristics are specified within the conditions given in the Electrical Characteristics table.

²⁾ relevant ESR value at f = 10 kHz

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General product characteristics

Thermal resistance 3.3

Note:

This thermal data was generated in accordance with JEDEC JESD51 standards. For more information, go to www.jedec.org.

Pos.	Parameter	Symbol		Limit Va	lues	Unit	Conditions
			Min.	Тур.	Max.		
PG-TO	252-3	1		1	1		
3.3.1	Junction to case ¹⁾	R_{thJC}	-	4	-	K/W	measured to heat slug
3.3.2	Junction to ambient ¹⁾	R_{thJA}	-	27	_	K/W	2)
3.3.3		R_{thJA}	-	57	-	K/W	300 mm² heatsink area ³⁾
3.3.4		R_{thJA}	-	42	-	K/W	600 mm² heatsink area ³⁾
PG-TO	263-3	,	•		"		
3.3.5	Junction to case ¹⁾	R_{thJC}	-	4	-	K/W	measured to heat slug
3.3.6	Junction to ambient ¹⁾	R_{thJA}	_	22	_	K/W	2)
3.3.7		R_{thJA}	-	42	-	K/W	300 mm² heatsink area ³⁾
3.3.8		R_{thJA}	-	33	-	K/W	600 mm² heatsink area ³⁾
PG-SO	T223-4		•				
3.3.10	Junction to case ¹⁾	R_{thJC}	-	25	-	K/W	measured to heat slug
3.3.11	Junction to ambient ²⁾	R_{thJA}	_	51	_	K/W	2)
3.3.12		R_{thJA}	-	75	-	K/W	300 mm² heatsink area ³⁾
3.3.13		R_{thJA}	-	63	-	K/W	600 mm² heatsink area ³⁾

- 1) Not subject to production test, specified by design.
- 2) Specified $R_{th,JA}$ value is according to Jedec JESD51-2,-5,-7 at natural convection on FR4 2s2p board; The Product (Chip+Package) was simulated on a 76.2 x 114.3 x 1.5 mm³ board with 2 inner copper layers (2 x 70μm Cu, 2 x 35μm Cu). Where applicable a thermal via array under the exposed pad contacted the first inner copper layer.
- 3) Specified $R_{\rm th \, IA}$ value is according to Jedec JESD 51-3 at natural convection on FR4 1s0p board; The Product (Chip+Package) was simulated on a $76.2 \times 114.3 \times 1.5$ mm³ board with 1 copper layer (1 x 70 μ m Cu).

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Electrical characteristics

4 Electrical characteristics

4.1 Electrical characteristics voltage regulator

Electrical Characteristics

 $V_{\rm i}$ =13.5 V; $T_{\rm i}$ = -40 °C to 125 °C; all voltages with respect to ground (unless otherwise specified)

Pos.	Parameter	Symbol	Limit Values			Unit	Measuring Condition
			Min.	Тур.	Max.		
Output	Q .			•	•		
4.1.1	Output voltage	V_{Q}	4.8	5.0	5.2	V	IFX25001 TF V50 IFX25001 TC V50 5 mA < I _Q < 400 mA 6 V < V _I < 28 V
4.1.2	Output voltage	V_{Q}	3.17	3.3	3.44	V	IFX25001 ME V33, IFX25001 TF V33 5 mA < I _Q < 400 mA 4.7 V < V _I < 28 V
4.1.4	Dropout voltage	V_{dr}	-	250	500	mV	IFX25001 TF V50, IFX25001 TC V50, $I_Q = 250 \text{ mA}$ $V_{dr} = V_I - V_Q^{(1)}$
4.1.5	Dropout voltage	V_{dr}	_	0.7	1.2	V	IFX25001 ME V33, IFX25001 TF V33; $I_{Q} = 300 \text{ mA}$ $V_{dr} = V_{I} - V_{Q}^{(1)}$
4.1.7	Load regulation	$\Delta V_{ m Q,lo}$	-	20	50	mV	IFX25001 TF V50, IFX25001 TC V50, $I_Q = 5 \text{ mA to } 400 \text{ mA}$ $V_1 = 6 \text{ V}$
4.1.9	Load regulation	$\Delta V_{ m Q,lo}$	-	40	70	mV	IFX25001 ME V33, IFX25001 TF V33, $I_{\rm Q}$ = 5 mA to 300 mA $V_{\rm I}$ = 6 V
4.1.10	Line regulation	$\Delta V_{ m Q,li}$	-	10	25	mV	$V_{\rm l}$ = 12 V to 32 V $I_{\rm Q}$ = 5 mA
4.1.11	Output current limitation	I_{Q}	400	600	1100	mA	1)
4.1.12	Power supply ripple rejection ²⁾	PSRR	_	60	_	dB	$f_{\rm r}$ = 100 Hz; $V_{\rm r}$ = 0.5 Vpp
4.1.13	Temperature output voltage drift ²⁾	$\frac{dV_{Q}}{dT}$	-	0.5	_	mV/K	_
4.1.14	Overtemperature shutdown threshold	$T_{j,sd}$	151	_	200	°C	$T_{\rm j}$ increasing ²⁾
Curren	t Consumption						
4.1.15	Quiescent current $I_{q} = I_{l} - I_{Q}$	I_{q}	-	100	220	μΑ	$I_{Q} = 1 \text{ mA}$

Low dropout voltage regulator



Electrical characteristics

Electrical Characteristics

 $V_{\rm i}$ =13.5 V; $T_{\rm i}$ = -40 °C to 125 °C; all voltages with respect to ground (unless otherwise specified)

Pos.	Parameter	Symbol	Limit Values		Limit Values		Limit Values l		Measuring Condition
			Min.	Тур.	Max.				
4.1.16	Current consumption	I_{q}	-	8	15	mA	I _Q = 250 mA		
4.1.17	$I_{q} = I_{l} - I_{Q}$	I_{q}	-	20	30	mA	I _Q = 400 mA		

¹⁾ Measured when the output voltage $V_{\rm Q}$ has dropped 100 mV from the nominal value obtained at $V_{\rm I}$ = 13.5 V.

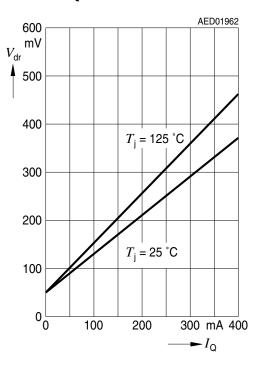
²⁾ not subject to production test, specified by design

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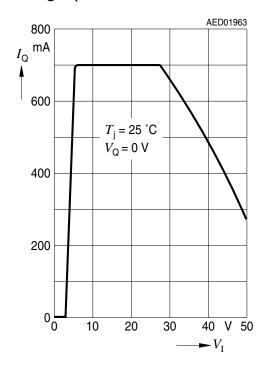
Electrical characteristics

4.2 Typical performance characteristics voltage regulator (V50 variants)

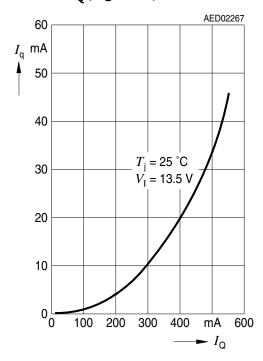
Dropout voltage V_{dr} versus output current I_{Q}



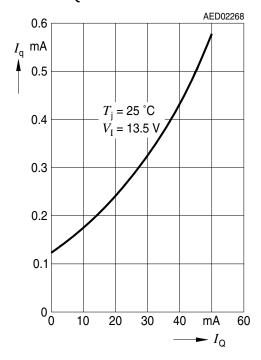
Output current $I_{\rm Q}$ versus input voltage $V_{\rm I}$



Current consumption I_q versus output current I_Q (high load)



Current consumption I_q versus output current I_Q (low load)

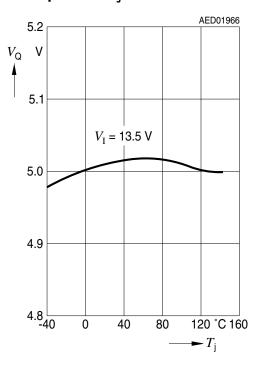




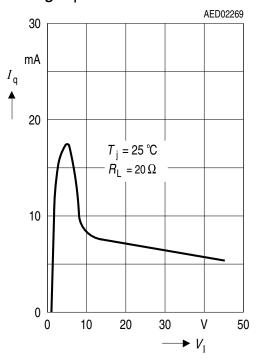
Electrical characteristics

Typical performance characteristics voltage regulator (V50 variants) 4.2.1

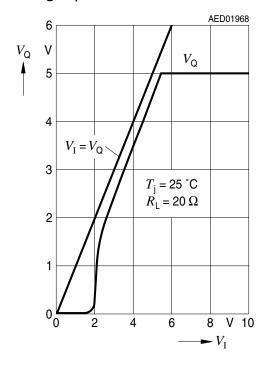
Output voltage $V_{\rm O}$ versus junction temperature $T_{\rm J}$



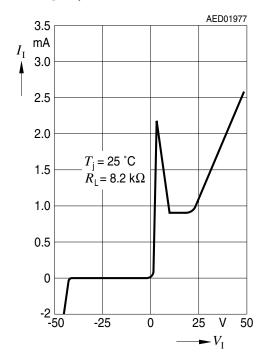
Current consumption I_a versus input voltage $V_{\rm I}$



Output voltage $V_{\rm O}$ versus input voltage V_1



Input current I_1 versus input voltage V_1

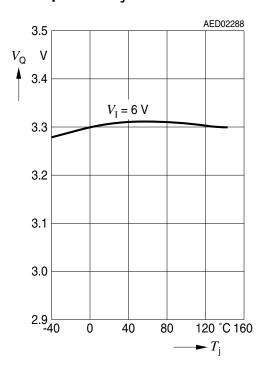




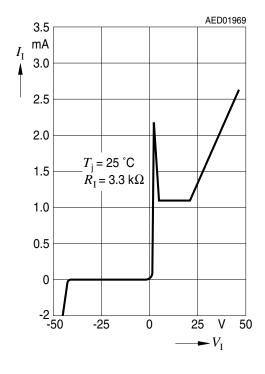
Electrical characteristics

4.2.2 Typical performance characteristics voltage regulator (V33 variants)

Output voltage $V_{\rm Q}$ versus junction temperature $T_{\rm J}$



Input current $I_{\rm I}$ versus input voltage $V_{\rm I}$

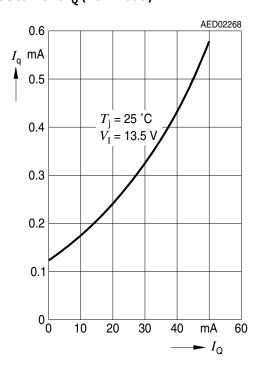


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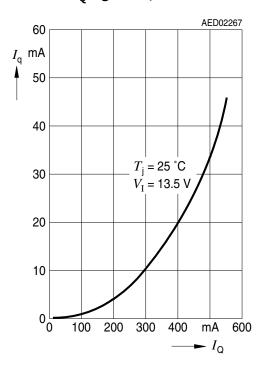
Electrical characteristics

4.2.3 Typical performance characteristics voltage regulator (V33 variants)

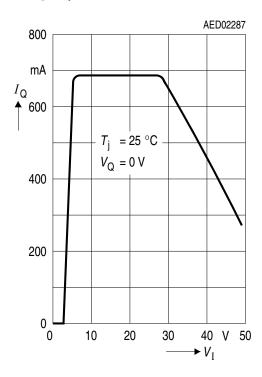
Current consumption $I_{\rm q}$ versus output current $I_{\rm Q}$ (Low Load)



Current consumption I_q versus output current I_Q high load)



Output current $I_{\rm Q}$ versus input voltage $V_{\rm I}$





Package information

5 Package information

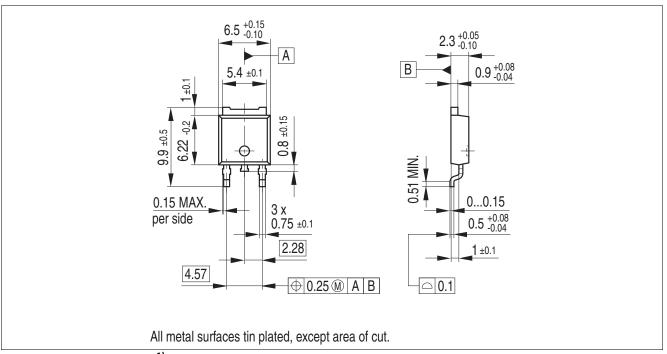


Figure 3 PG-TO252-3 1)

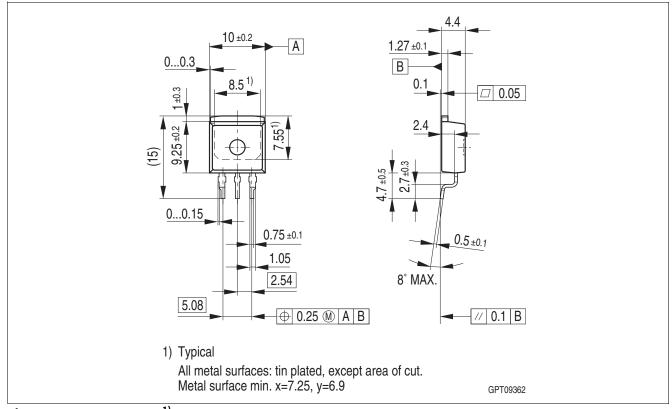


Figure 4 PG-TO263-3 1)

¹⁾ Dimensions in mm

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Package information

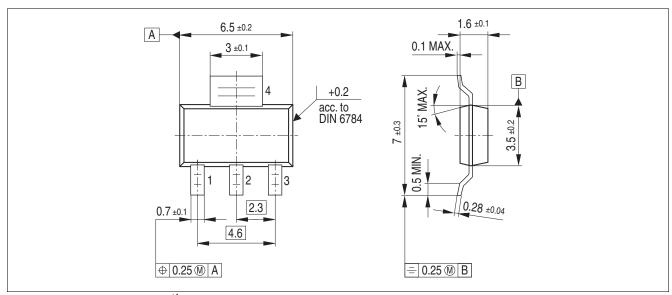


Figure 5 PG-SOT223-4 1)

Green Product (RoHS compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

Further information on packages

https://www.infineon.com/packages

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Revision history

6 Revision history

Table 1 Revision history

Revision	Date	Changes
1.10	2019-04-02	- Discontinued product variants removed from data sheet - Editiorial changes
1.02	2009-05-20	- Editorial change (fig. 2)
1.01	2009-05	- Coverpage changed - Overview page: Inserted reference statement to TLE/TLF series
1.00	2009-04-28	- Initial release

Trademarks

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