



MOSFET

Small-Signal Transistor

Features

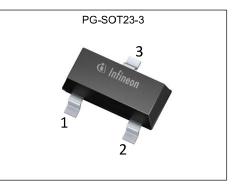
- N-channel
- Enhancement mode
- Logic level
- dv/dt rated
- Pb-free lead-plating; RoHS compliant
 Halogen free according to IEC61249-2-21

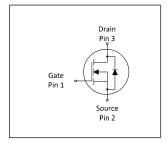
Product validation

Fully qualified according to JEDEC for Industrial Applications

Table 1 **Key Performance Parameters**

Parameter	Value	Unit
V _{DS}	60	V
R _{DS(on),max}	3.5	Ω
ID	0.23	A
ESD Sensitivity, JESD22-A114 (HBM)	Class 0 (<250V)	









Type / Ordering Code	Package	Marking	Related Links
BSS138I	PG-SOT23	Kls	-



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Disclaimer)



1 Maximum ratings at $T_A=25$ °C, unless otherwise specified

Table 2 **Maximum ratings**

	C. mah al		Values			
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note / Test Condition
Continuous drain current	I _D	-	-	0.23 0.18	A	<i>T</i> _A =25 °C <i>T</i> _A =70 °C
Pulsed drain current	I _{D,pulse}	-	-	0.92	А	<i>T</i> _A =25 °C
Reverse diode d <i>v</i> /d <i>t</i>	d <i>v</i> /dt	-	-	6	kV/µs	/ _D =0.23 A, V _{DS} =48 V, d <i>i</i> /d <i>t</i> =200 A/μs / _{J,max} =150 °C
Gate source voltage	V _{GS}	-20	-	20	V	-
Power dissipation	Ptot	-	-	0.36	W	<i>T</i> _A =25 °C
Operating and storage temperature	Tj, T _{stg}	-55	-	150	°C	IEC climatic category; DIN IEC 68-1: 55/150/56

2 **Thermal characteristics**

Table 3 Thermal characteristics

Baramatar	Symbol	Values			11	Note / Toot Condition
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note / Test Condition
Thermal resistance, junction - minimal footprint	R _{thJA}	-	-	350	K/W	-

Electrical characteristics 3

at T_j=25 °C, unless otherwise specified

Table 4Static characteristics

Devenuetor	Symbol		Values			
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note / Test Condition
Drain-source breakdown voltage	V _{(BR)DSS}	60	-	-	V	V _{GS} = 0 V, <i>I</i> _D =250 μA
Gate threshold voltage	$V_{\rm GS(th)}$	0.6	1.0	1.4	V	$V_{\rm GS}=V_{\rm DS}, I_{\rm D}=26~\mu{\rm A}$
Drain-source leakage current	I _{D (off)}	-	-	0.1 5	μA	V _{DS} =60 V, V _{GS} =0 V, T _j =25 °C V _{DS} =60 V, V _{GS} =0 V, T _j =125 °C
Gate-source leakage current	I _{GSS}	-	1	10	nA	V _{GS} =20 V, V _{DS} =0 V
Drain-source on-state resistance	R _{DS(on)}		3.3 3.5 2.2	4.0 6.0 3.5	Ω	V _{GS} =4.5 V, <i>I</i> _D =0.03 A V _{GS} =4.5 V, <i>I</i> _D =0.19 A V _{GS} =10 V, <i>I</i> _D =0.23 A
Transconductance	g_{fs}	0.1	0.2	-	S	V _{DS} >2 I _D R _{DS(on)max} , I _D =0.18 A



Table 5 Dynamic characteristics

Deveryor	Cumula a l		Values			
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note / Test Condition
Input capacitance	Ciss	-	32	-	pF	V _{GS} =0 V, V _{DS} =25 V, <i>f</i> =1 MHz
Output capacitance	Coss	-	7.2	-	pF	V _{GS} =0 V, V _{DS} =25 V, <i>f</i> =1 MHz
Reverse transfer capacitance	C _{rss}	-	2.8	-	pF	V _{GS} =0 V, V _{DS} =25 V, <i>f</i> =1 MHz
Turn-on delay time	t _{d(on)}	-	2.3	-	ns	$V_{\rm DD}$ =30 V, $V_{\rm GS}$ =10 V, $I_{\rm D}$ =0.23 A, $R_{\rm G}$ =6 Ω
Rise time	tr	-	3.0	-	ns	$V_{\rm DD}$ =30 V, $V_{\rm GS}$ =10 V, $I_{\rm D}$ =0.23 A, $R_{\rm G}$ =6 Ω
Turn-off delay time	$t_{\rm d(off)}$	-	6.7	-	ns	$V_{\rm DD}$ =30 V, $V_{\rm GS}$ =10 V, $I_{\rm D}$ =0.23 A, $R_{\rm G}$ =6 Ω
Fall time	t _f	-	8.2	-	ns	$V_{\rm DD}$ =30 V, $V_{\rm GS}$ =10 V, $I_{\rm D}$ =0.23 A, $R_{\rm G}$ =6 Ω

Table 6 Gate charge characteristics

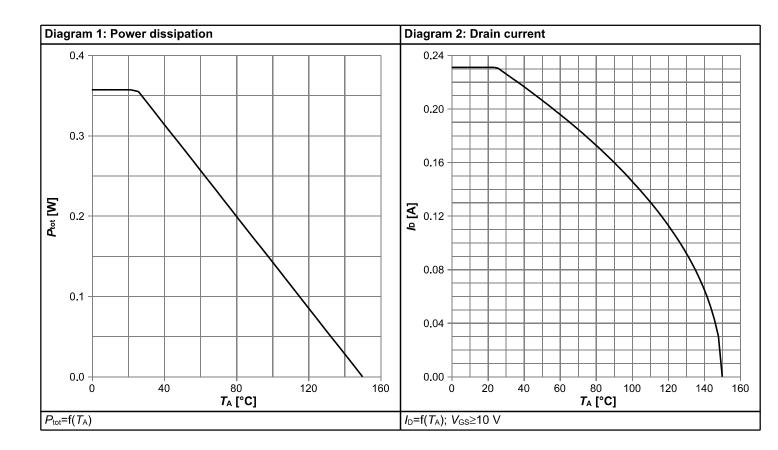
Parameter	Cump hal		Values			
	Symbol	Min.	Тур.	Max.	Unit	Note / Test Condition
Gate to source charge	Q _{gs}	-	0.10	-	nC	V_{DD} =48 V, I_{D} =0.23 A, V_{GS} =0 to 10 V
Gate to drain charge	$Q_{\rm gd}$	-	0.3	-	nC	V_{DD} =48 V, I_{D} =0.23 A, V_{GS} =0 to 10 V
Gate charge total	Qg	-	1.0	-	nC	V_{DD} =48 V, I_{D} =0.23 A, V_{GS} =0 to 10 V
Gate plateau voltage	$V_{ m plateau}$	-	3.3	-	V	V_{DD} =48 V, I_{D} =0.23 A, V_{GS} =0 to 10 V

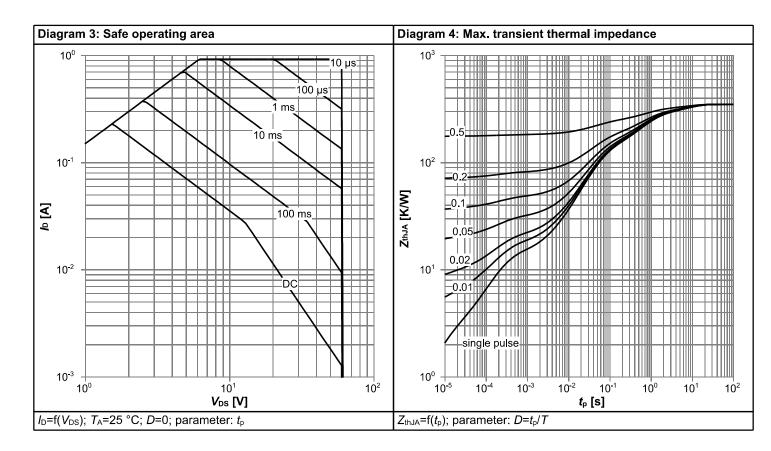
Table 7Reverse diode

Parameter	O		Values			
	Symbol	Min.	Тур.	Max.	Unit	Note / Test Condition
Diode continous forward current	I _S	-	-	0.23	А	<i>T</i> _A =25 °C
Diode pulse current	I _{S,pulse}	-	-	0.92	А	<i>T</i> _A =25 °C
Diode forward voltage	V _{SD}	-	0.83	1.2	V	V _{GS} =0 V, <i>I</i> _F =0.23 A, <i>T</i> _j =25 °C
Reverse recovery time	t _{rr}	-	9.1	14.5	ns	V _R =30 V, <i>I</i> _F =0.23 A, d <i>i</i> _F /d <i>t</i> =100 A/µs
Reverse recovery charge	Qrr	-	3.3	5	nC	V _R =30 V, I _F =0.23 A, di _F /dt=100 A/µs

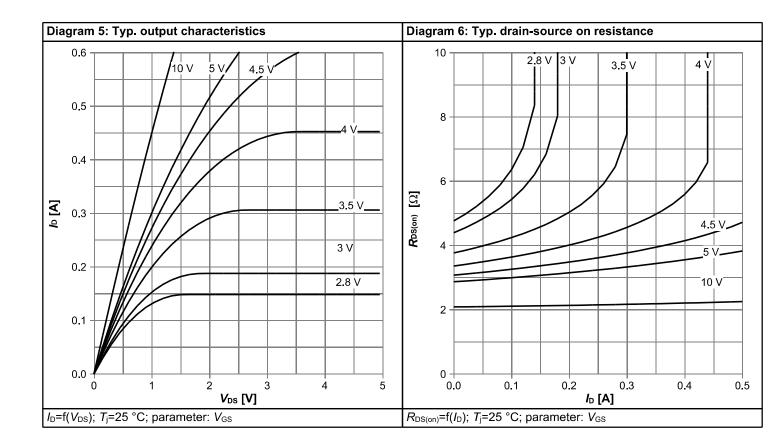


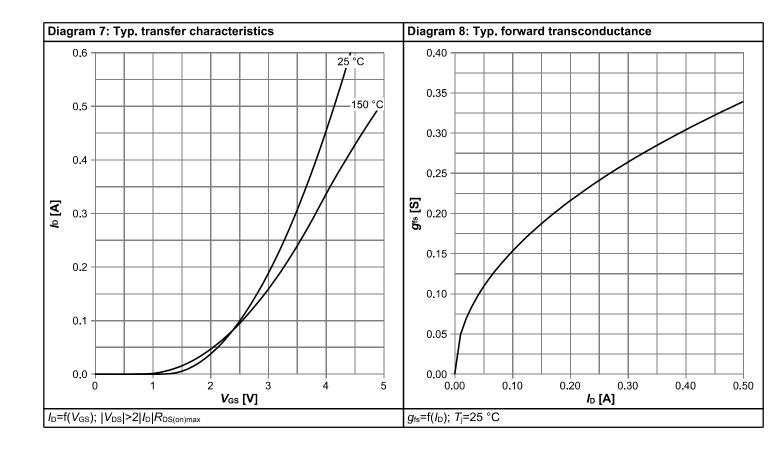
4 Electrical characteristics diagrams



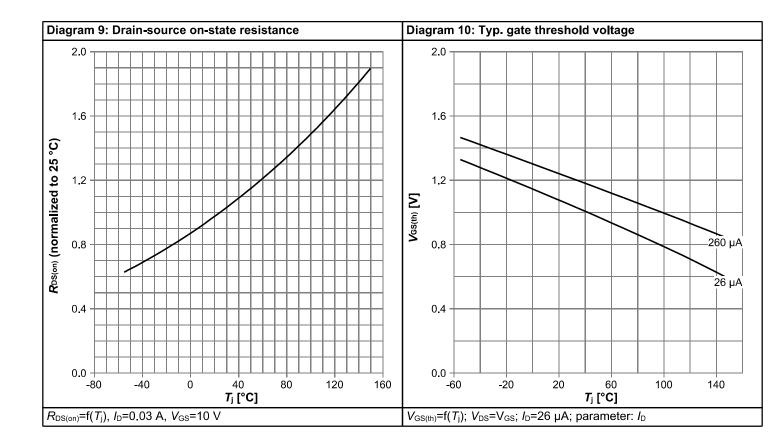


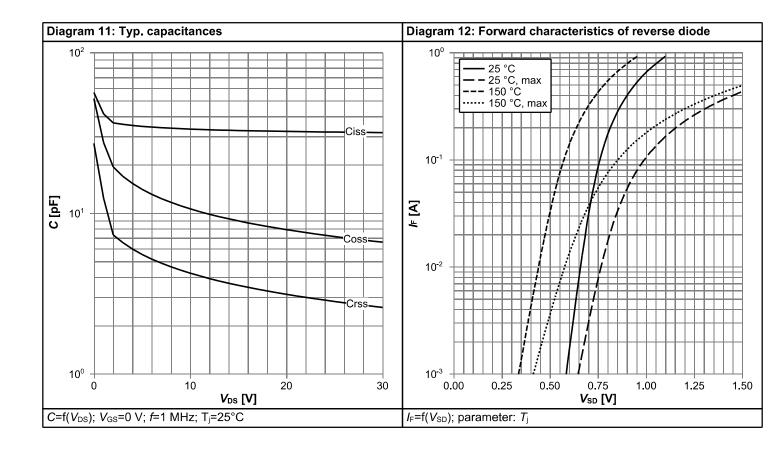




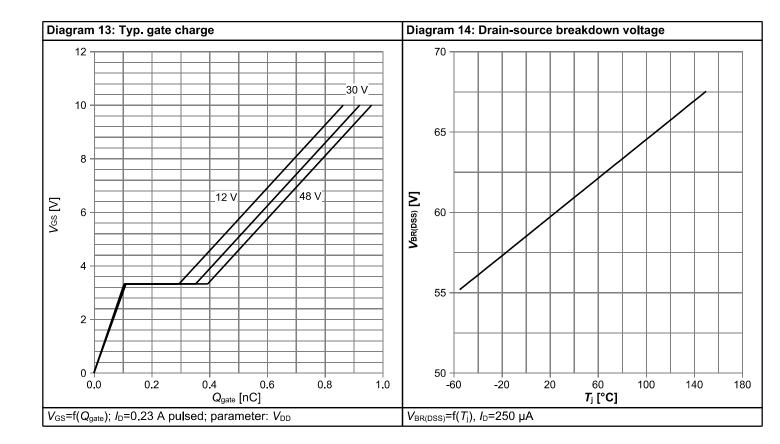


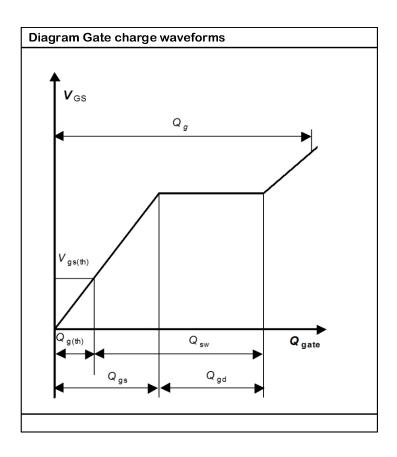






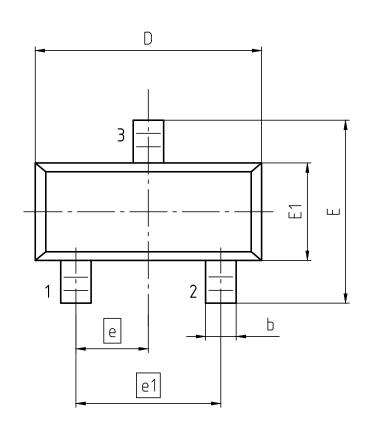


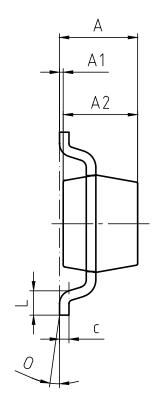






5 Package Outlines





PACKAGE - GROUP NUMBER:	PG-SOT	PG-SOT23-3-U01				
REVISION: 01	DATE: (09.12.2020				
DIMENSIONS	MILLIM	ETERS				
DIMENSIONS	MIN.	MAX.				
Α	0.89	1.12				
A1	0.01	0.10				
A2	0.88	1.02				
b	0.30	0.50				
с	0.08	0.20				
D	2.80	3.04				
E	2.10	2.64				
E1	1.20	1.40				
е	0.95					
e1	1.90					
L	0.15 0.60					
0	0°	8°				

Figure 1 Outline PG-SOT23, dimensions in mm



Revision History

BSS138I

Revision: 2021-03-17, Rev. 2.1

Previous Revision						
Revision	Date	Subjects (major changes since last revision)				
2.0	2021-01-26	Release of final version				
2.1	2021-03-17	Update technology naming				

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