INTELLIGENT POWER LOW SIDE SWITCH

Features

- Over temperature shutdown
- · Over current shutdown
- Active clamp
- Low current & logic level input
- ESD protection
- Optimized Turn On/Off for EMI
- Diagnostic on the input current

Description

The AUIPS2041(R)(L) is a three terminal Intelligent Power Switch (IPS) that features a low side MOSFET with overcurrent, over-temperature, ESD protection and drain to source active clamp. This device offers protections and the high reliability required in harsh environments. The switch provides efficient protection by turning OFF the power MOSFET when the temperature exceeds 165°C or when the drain current reaches 5A. The device restarts once the input is cycled. A serial resistance connected to the input provides the diagnostic. The avalanche capability is significantly enhanced by the active clamp and covers most inductive load demagnetizations.

Product Summary

 $\begin{array}{ll} \text{Rds(on)} & 130\text{m}\Omega\,(\text{max.}) \\ \text{Vclamp} & 68\text{V} \\ \text{Ishutdown} & 5\text{A (typ.)} \end{array}$

Packages

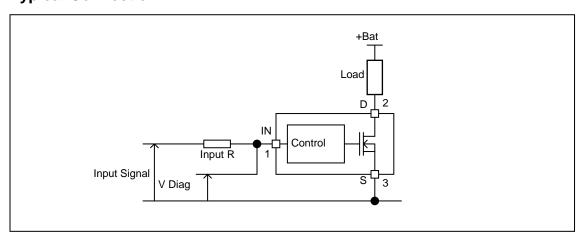






SOT223 AUIPS2041L

Typical Connection





Qualification Information[†]

<u> </u>	cation information				
		Automotive (per AEC-Q100 ^{††})			
Qualification Level			Comments: This family of ICs has passed an Automotive qualification. IR' Industrial and Consumer qualification level is granted by extension of the higher Automotive level.		
Moisture Sensitivity Level		DPAK-3L	MSL1, 260°C (per IPC/JEDEC J-STD-020)		
		SOT223-3L	MSL2, 260°C ^{†††} (per IPC/JEDEC J-STD-020)		
	Machine Model		Class M4 (+/-450V) (per AEC-Q100-003)		
ESD	Human Body Model		ss H3A (+/4500V) AEC-Q100-002)		
Charged Device Model			Class C4 (+/-900V) (per AEC-Q100-011)		
IC Latch-Up Test			ClassII, Level A (per AEC-Q100-004)		
RoHS C	ompliant		Yes		

[†] Qualification standards can be found at International Rectifier's web site http://www.irf.com/

^{††} Exceptions to AEC-Q100 requirements are noted in the qualification report.

^{†††} Higher MSL ratings may be available for the specific package types listed here. Please contact your International Rectifier sales representative for further information.



Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. (Tj= -40°C..150°C,

Vcc=6..50V unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vds	Maximum drain to source voltage	-0.3	60	V
Vin	Maximum input voltage	-0.3	6	V
Isd cont.	Max diode continuous current (limited by thermal dissipation) Rth=125°C/W	_	1.4	Α
Pd	Maximum power dissipation (internally limited by thermal protection)			W
Fu	Rth=125C°/W	_	1	VV
Tj max.	Max. storage & operating temperature junction temperature	-40	150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
Rth1	Thermal resistance junction to ambient IPS2041L	100	_	
Rth2	Thermal resistance junction to ambient with 1" square footprint	50	_	
Rth1	Thermal resistance junction to ambient IPS2041R D-Pak std. footprint	70	_	°C/W
Rth2	Thermal resistance junction to ambient IPS2041R D-Pak 1" sqr. footprint	50	_	
Rth3	Thermal resistance junction to case IPS2041R D-Pak	4	_	

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
VIH	High level input voltage	4	5.5	
VIL	Low level input voltage	0	0.5	
lds	Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V,Rth=100°C/W		1.4	Α
Rin	Recommended resistor in series with IN pin to generate a diagnostic	0.5	5	kΩ
Max L	Max recommended load inductance (including line inductance) (1)		10	mΗ
Max. t rise	Max. input rising time		1	μs

⁽¹⁾ Higher inductance is possible if maximum load current is limited - see figure 11



Static Electrical Characteristics

Tj= -40..150°C, Vcc=14V (unless otherwise specified), typical value are given for Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Rds(on)	ON state resistance Tj=25°C	_	100	130	mΩ	Vin=5V. lds=1A
	ON state resistance Tj=150°C(2)	_	180	240	1115.2	VIII=5V, IUS=TA
ldss1	Drain to source leakage current	_	0.01	1		Vcc=14V, Tj=25°C
ldss2	Drain to source leakage current	_	0.04	2	μA	Vcc=50V, Tj=25°C
V clamp1	Drain to source clamp voltage 1	63	68	_		Id=20mA See fig. 3 & 4
V clamp2	Drain to source clamp voltage 2	_	68	75	\/	Id=1A
Vin clamp	IN to source pin clamp voltage	5.5	6.2	7.5	V	lin=1mA
Vth	Input threshold voltage	1.1	2	2.8		Id=50mA
lin, on	ON state IN positive current	10	40	80		Vin=5V
lin, off	OFF state IN positive current	120	250	350	μΑ	
	(after protection latched)					

Switching Electrical Characteristics

Tj= -40..150°C, Vcc=14V, Resistive load=10Ω, Rinput=50Ω, Vin=5V, typical value are given for Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tdon	Turn-on delay time to 20%	0.25	1.3	4		
Tr	Rise time 20% to 80%	0.1	0.9	2		See figure 2
Tdoff	Turn-off delay time to 80%	1	3	8	μs	See ligure 2
Tf	Fall time 80% to 20%	0.1	0.6	3		
Eon + Eoff	Turn on and off energy	_	12	_	μJ	

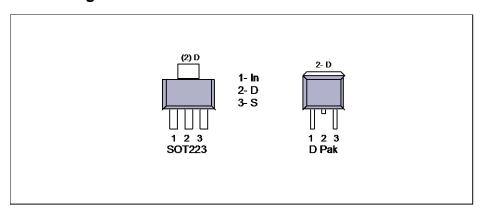
Protection Characteristics

Ti= -40..150°C. Vcc=14V (unless otherwise specified), typical value are given for Ti=25°C

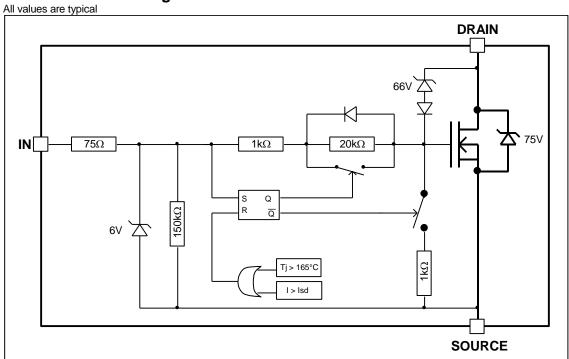
	, voo=14 v (dilicos otilici wioc opcollica), typica					
Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tsd	Over temperature threshold	150(2)	165	_	°C	See figure 1
Isd	Over current threshold	4	5.5	7.5	۸	Tj=25150°C see fig 1
		3.6	6	8	_ A	Tj= -40°C
Vreset	IN protection reset threshold	0.9	1.6	2	V	
Treset	Time to reset protection	15	50	500	μs	Vin=0V

⁽²⁾ Guaranteed by design

Lead Assignments



Functional Block Diagram





All curves are typical values. Operating in the shaded area is not recommended.

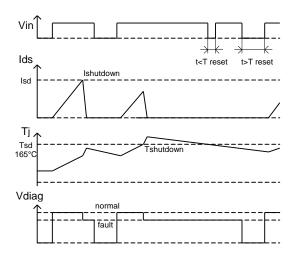


Figure 1 - Timing diagram

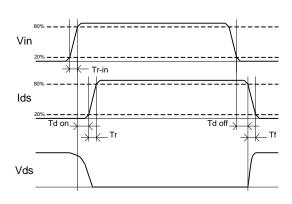


Figure 2 - IN rise time & switching definitions

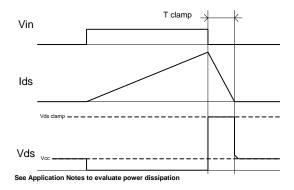


Figure 3 - Active clamp waveforms

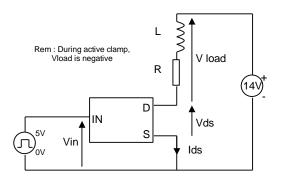


Figure 4 - Active clamp test circuit

6



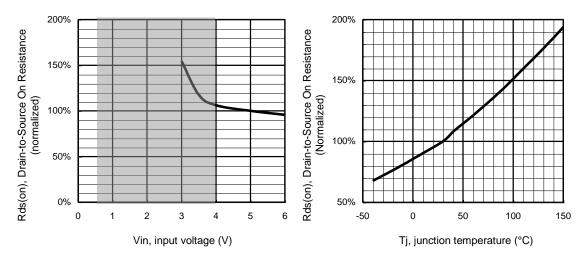


Figure 5 - Normalized Rdson (%) Vs Input voltage (V)

Figure 6 - Normalized Rds(on) (%) Vs Tj (°C)

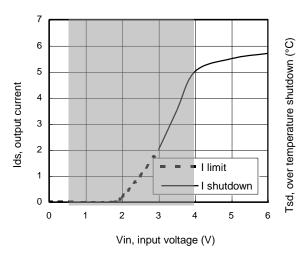


Figure 7 – Current limitation and current shutdown Vs Input voltage (V)

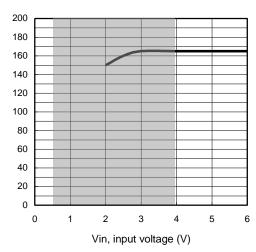
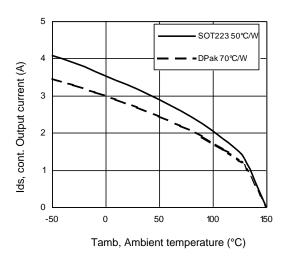


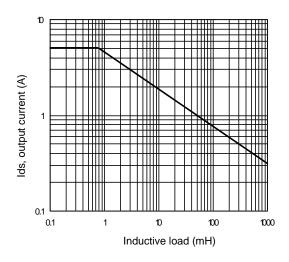
Figure 8 – Over temperature shutdown (°C)
Vs input voltage (V)



6
5
6
7
1
1
10
100
1000
Protection response time (s)

Figure 9 – Max. continuous output current (A) Vs Ambient temperature (°C)

Figure 10 – Ids (A) Vs over temperature protection response time (s)



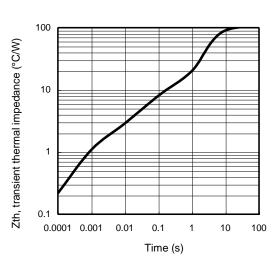


Figure 11 – Max. ouput current (A) Vs Inductive load (mH)

Figure 12 – Transient thermal impedance (°C/W) Vs time (s)

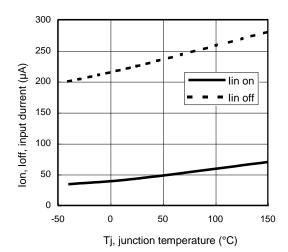
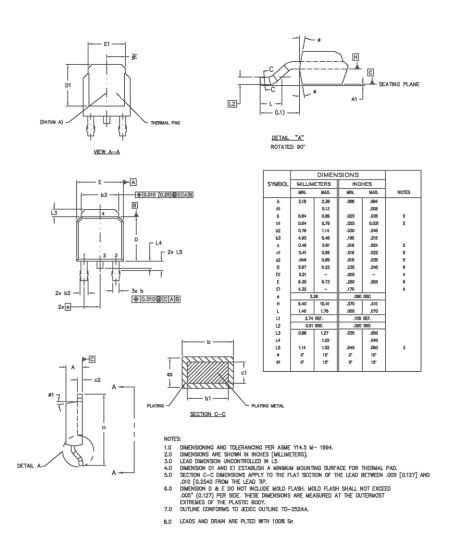


Figure 13 – Input current (μA) On and Off Vs junction temperature (°C)

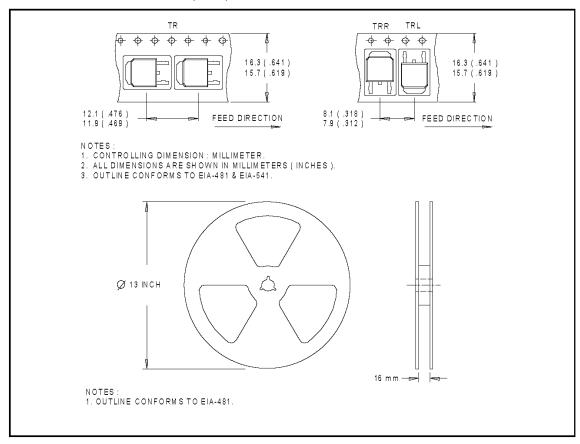


Case outline - Dpak - Automotive Q100 PbF MSL1 qualified



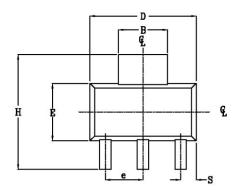
Tape & Reel - Dpak

Dimensions are shown in millimeters (inches)





Case Outline - SOT-223 - Automotive Q100 PbF MSL2 qualified

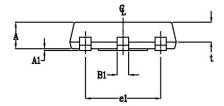


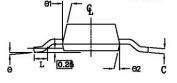
POS	MILLIM	ETERS	INC	HES	
1	MAX	MIN	MAX	MIN	
A	1.70	1.50	.067	.060	
A1	0.10	0.02	.004	.0008	
В	3.15	2.95	.124	.116	
B1	0.85	0.65	.033	.026	
C	0.35	0.25	.014	.010	
D	6.70	6.30	.264	.248	
е	2.30	NOM	.0905	05 NOM	
e1	4.60	NOM	.181	NOM	
E	3.70	3.30	.146	.130	
H	7.30	6.70	.287	.264	
S	1.05	0.85	.041	.033	
t	1.30	1.10	.051	.043	
Θ	10° k	(AX	10°	MAX	
Θ1	16*	10°	16*	10°	
Θ2	16*	10°	16*	10°	
L	0.75	MIN	0.02	95 MIN	

- NOTE:

 1. PACKAGE OUTLINE EXCLUSIVE OF ANY MOLD FLASHES DIMENSION.

 2. PACKAGE OUTLINE EXCLUSIVE OF BURR



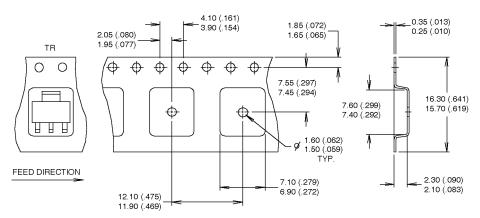


Leads and drain are plated with 100% Sn



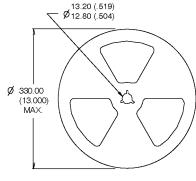
Tape & Reel - SOT-223

Dimensions are shown in milimeters (inches)



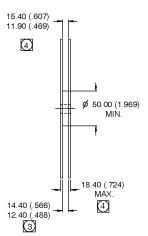
NOTES:

- 1. CONTROLLING DIMENSION: MILLIMETER.
- 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.
- 3. EACH \$\oldsymbol{\psi}330.00 (13.00) REEL CONTAINS 2,500 DEVICES.

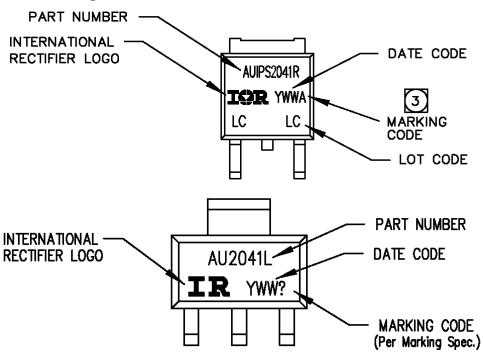




- 1. OUTLINE COMFORMS TO EIA-418-1.
- 2. CONTROLLING DIMENSION: MILLIMETER..
- DIMENSION MEASURED @ HUB.
- INCLUDES FLANGE DISTORTION @ OUTER EDGE.



Part Marking Information



Ordering Information

Base Part Number	Package Type	Standard Pack	Occupated a Boot Name to an	
		Form	Quantity	Complete Part Number
ALUBOOMA	D-Pak-5-Lead	Tube	75	AUIPS2041R
		Tape and reel	2000	AUIPS2041RTR
AUIPS2041L		Tape and reel left	3000	AUIPS2041RTRL
		Tape and reel right	3000	AUIPS2041RTRR
AUIPS2041L	SOT223	Tube	80	AUIPS2041L
AUIP32041L		Tape and reel	2500	AUIPS2041LTR



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WORLD HEADQUARTERS:

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Revision History

Revision	Date	Notes/Changes
С	26/02/2009	AU number update / OV removed
D	09/12/2010	Final release
Е	14/03/2011	Update part marking
F	November, 14 th 2011	Update T&R SOT223
G	May 9 th , 2012	Update the component number of the SOT223 tube

Mouser Electronics

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Infineon:

AUIPS2041LTR AUIPS2041R AUIPS2041RTRL