

AUIPS2031R

## INTELLIGENT POWER LOW SIDE SWITCH

#### **Features**

- Over temperature shutdown
- Over current shutdown
- Active clamp
- Low current & logic level input
- ESD protection
- Optimized Turn On/Off for EMI
- Diagnostic on the input current

#### **Description**

The AUIPS2031R is a three terminal Intelligent Power Switch (IPS) that features a low side MOSFET with overcurrent, over-temperature, ESD protection and drain to source active clamp. This device offers protections and the high reliability required in harsh environments. The switch provides efficient protection by turning OFF the power MOSFET when the temperature exceeds 165°C or when the drain current reaches 15A. The device restarts once the input is cycled. A serial resistance connected to the input provides the diagnostic. The avalanche capability is significantly enhanced by the active clamp and covers most inductive load demagnetizations.

### **Product Summary**

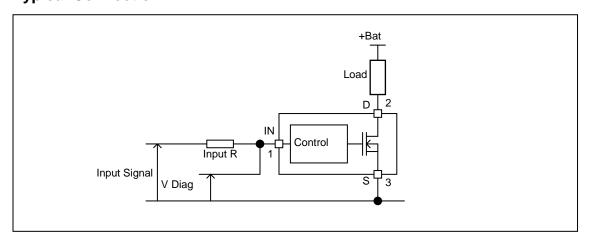
 $\begin{array}{ll} \text{Rds(on)} & 60\text{m}\Omega\,(\text{max.}) \\ \text{Vclamp} & 68\text{V} \\ \text{Ishutdown} & 10\text{A}\,\,(\text{min.}) \end{array}$ 

### **Packages**



DPak AUIPS2031R

## **Typical Connection**





### Qualification Information<sup>†</sup>

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		Automotive (per AEC-Q100 <sup>††</sup> )					
Qualifica	tion Level	Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.					
Moisture	Sensitivity Level	MSL1, 260°C DPAK-3L (per IPC/JEDEC J-STI 020)					
	Machine Model	Class M3 (+/-400V) (per AEC-Q100-003)					
ESD	Human Body Model	,	Class H1C (+/-2000V) (per AEC-Q100-002)				
Charged Device Model		Class C4 (+/-1000V) (per AEC-Q100-011)					
IC Latch-Up Test		Class II, Level A (per AEC-Q100-004)					
RoHS Compliant Yes							

- † Qualification standards can be found at International Rectifier's web site <a href="http://www.irf.com/">http://www.irf.com/</a>
- †† Exceptions to AEC-Q100 requirements are noted in the qualification report.
- ††† Higher MSL ratings may be available for the specific package types listed here. Please contact your International Rectifier sales representative for further information.

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## **Absolute Maximum Ratings**

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. (Tj= -40°C..150°C, Vcc=6..50V unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vds	Maximum drain to source voltage	-0.3	60	V
Vin	Maximum input voltage	-0.3	6	V
Isd cont.	Max diode continuous current (limited by thermal dissipation) Rth=50°C/W	_	2.5	Α
Pd	Maximum power dissipation (internally limited by thermal protection)  Rth=50C°/W	_	2.5	W
Ti max.	Maximum operating junction temperature	-40	150	°C
ijiiiax.	Maximum storage temperature	-55	150	

#### **Thermal Characteristics**

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Symbol	Parameter	Тур.	Max.	Units
Rth1	Thermal resistance junction to ambient IPS2031R D-Pak std. footprint	70	_	
Rth2	Thermal resistance junction to ambient IPS2031R D-Pak 1" sqr. footprint	50	_	°C/W
Rth3	Thermal resistance junction to case IPS2031R D-Pak	2.5	_	

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter		Min.	Max.	Units
VIH	High level input voltage			5.5	
VIL	Low level input voltage	ow level input voltage			
lds	Continuous drain current, Tambient=85°C, Tj=	ontinuous drain current, Tambient=85°C, Tj=125°C, Vin=5V,Rth=70°C/W			Α
Rin	Recommended resistor in series with IN pin	Recommended resistor in series with IN pin Input signal voltage=5V (1)			kΩ
	Input signal voltage=4V (1)			1	K22
Max. t rise	Max. input signal rising time (from 10% to 90%	%)(2)	_	0.5	μs

<sup>(1)</sup> Input signal of the pulse generator not the voltage on the IN pin of the device. Do not connect any other component on

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<sup>(2)</sup> Max. t rise is for the input signal of the pulse generator not on the IN pin voltage of the device



#### **Static Electrical Characteristics**

Tj=-40..150°C, Vcc=6..50V (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Rds(on)	ON state resistance Tj=25°C	_	45	60	m()	Vin=5V. Ids=5A
	ON state resistance Tj=150°C	_	80	110	mΩ	VIII=5V, IUS=5A
ldss1	Drain to source leakage current	_	0.1	1		Vcc=14V, Vin=0V, Tj=25°C
ldss2	Drain to source leakage current	_	0.15	2	μA	Vcc=50V, Vin=0V, Tj=25°C
V clamp1	Drain to source clamp voltage 1	63	68	_		Id=20mA See fig. 3 & 4
V clamp2	Drain to source clamp voltage 2	_	68	75	V	Id=1A
Vin clamp	IN to source pin clamp voltage	5.5	6.2	7.5	V	lin=1mA
Vth	Input threshold voltage	1.1	2	2.8		Id=200mA
lin, on	ON state IN positive current	10	40	80	Vin=5V	
lin, off	OFF state IN positive current (after protection latched)	120	250	350	μA	

## **Switching Electrical Characteristics**

Vcc=28V. Resistive load= $10\Omega$ . Rinput= $50\Omega$ . Vin=5V. Ti=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tdon	Turn-on delay time to 20%	0.5	2	5		
Tr	Rise time 20% to 80%	0.2	1.4	3		See figure 2
Tdoff	Turn-off delay time to 80%	3	8	12	μs	See ligure 2
Tf	Fall time 80% to 20%	0.2	1.4	3		
Eon + Eoff	Turn on and off energy	_	110	_	μJ	

#### **Protection Characteristics**

Tj=-40..150°C, Vcc=6..50V (unless otherwise specified)

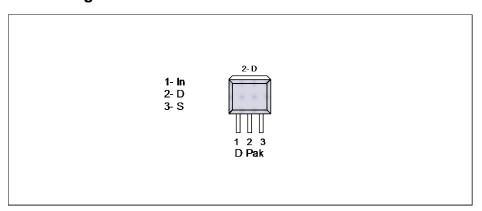
Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tsd	Over temperature threshold	150(2)	165	_	°C	See figure 1
Isd	Over current threshold	10	15	20	Α	See figure 1
Vreset	IN protection reset threshold	0.9	1.6	2	V	
Treset	Time to reset protection	15	50	500	μs	Vin=0V, Tj=25°C

<sup>(2)</sup> Guaranteed by design

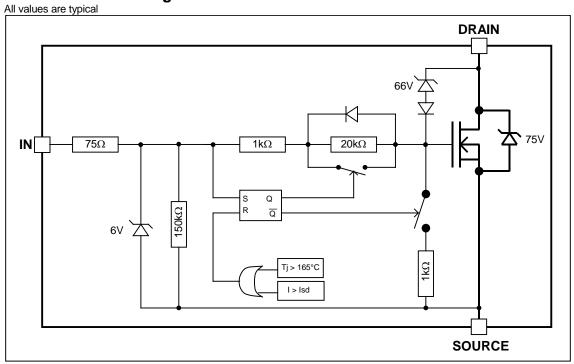
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## **Lead Assignments**



## **Functional Block Diagram**



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All curves are typical values. Operating in the shaded area is not recommended.

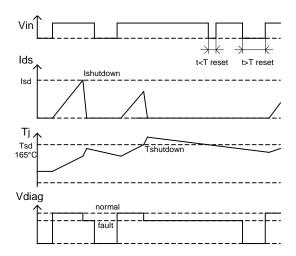


Figure 1 - Timing diagram

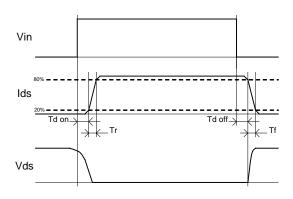


Figure 2 – IN rise time & switching definitions

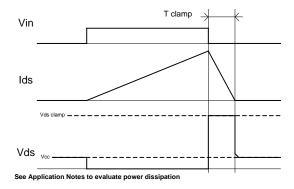


Figure 3 - Active clamp waveforms

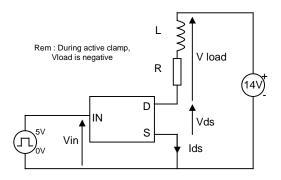


Figure 4 - Active clamp test circuit



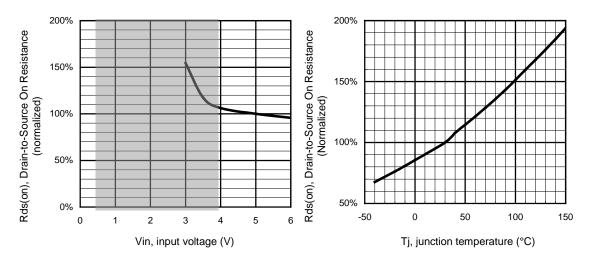


Figure 5 - Normalized Rdson (%) Vs Input voltage (V)



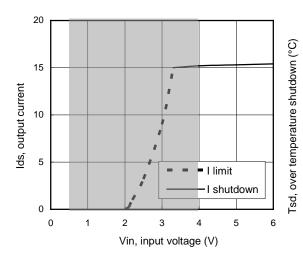


Figure 7 - Current limitation and current shutdown Vs Input voltage (V)

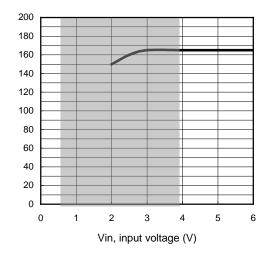
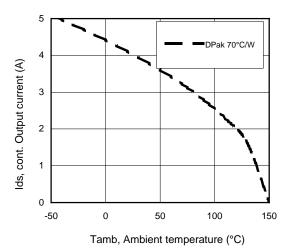


Figure 8 - Over temperature shutdown (°C) Vs input voltage (V)





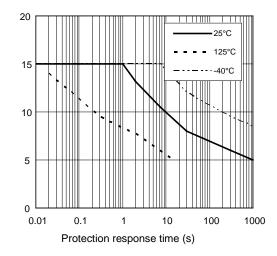


Figure 9 – Max. continuous output current (A) Vs Ambient temperature (°C)

Figure 10 – Ids (A) Vs over temperature protection response time (s)

ds, output current (A)

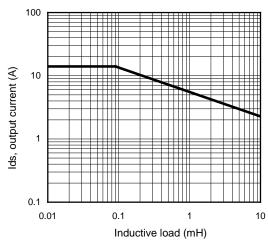
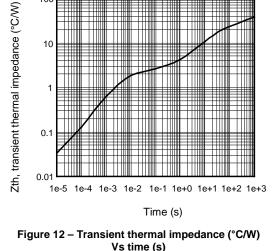


Figure 11 – Max. ouput current (A) Vs Inductive load (mH)



This is for single pulse when Tj=165°C and for repetitive pulses when Tj<115°C before turning off.



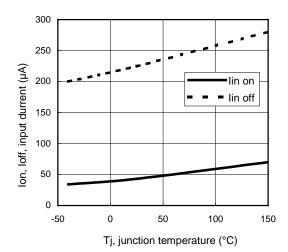
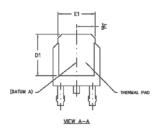
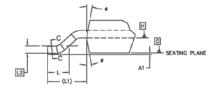


Figure 13 – Input current (μA) On and Off Vs junction temperature (°C)

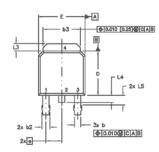


## Case outline - Dpak

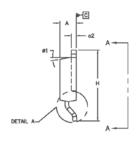




DETAIL "A"
ROTATED 90"



		DIMEN	SIONS		
SYMBOL	MILLIM	ETERS	INC	HES	
	MINL	MAX.	MINL	MAX	NOTES
A	2.18	2.30	.086	.004	
AI		0.13		.005	
b	0.64	0.89	.025	.035	5
ы	0.84	0.79	.025	0.031	5
b2	0.76	1.14	.030	.045	
b3	4.95	5.48	.195	.215	
c	0.48	0.61	.018	.024	5
cl	0.41	0.56	.016	.022	5
o2	.048	0.89	.018	.035	5
D	5.97	8.22	.235	.245	6
DI	5.21	- 1	.205	-	4
E	4.35	6.73	-250	.265	6
E	4.32	-	.170		4
•	2	22	.090	BSC	
н	8.40	10.41	.370	.410	
L	1.40	1.78	.055	.070	
ш	2.74	REF.	.108	REF.	
L2	0.51	BSC	.020 BSC		
L3	0.89	1.27	.035	.050	
L4		1.02		.040	
L5	1.14	1.02	.045	.080	3
	σ	10"	0	10*	
#1	a	15"	0	15"	



	g1
PLATING b1	NG META
SECTION C-C	

#### NOTES

- NOTES:

  10 DIMENSIONING AND TOLERANCING PER ASME Y14.5 N- 1994.

  20 DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].

  21 LEAD DIMENSION UNCONTROLLED IN IS.

  4.0 DIMENSION OF AND ET ESTABLISH A MINIMUM MOUNTING SURFACE FOR THERMAL PAD.

  5.0 SECTION C-C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 [0.127] AND

  .010 [0.2540 FROM THE LEAD TIP.

  6. DIMENSION 0 & ED DON TINCLIDE MOLD FLASH, MOLD FLASH SHALL NOT EXCEED

  .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST

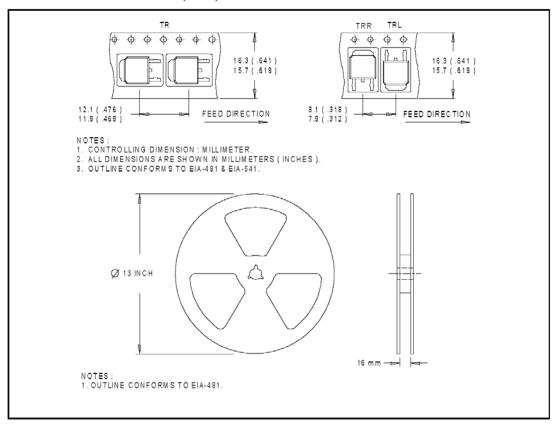
  EXTRIBUSE OF THE PLASTIC BODY.

  7.0 OUTLINE CONFORMS TO JEDEC OUTLINE TO-252AA.
- 8.0 LEADS AND DRAIN ARE PLTED WITH 100% Sn



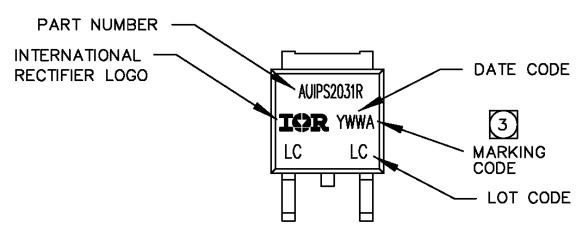
## Tape & Reel - Dpak

Dimensions are shown in millimeters (inches)





## **Part Marking Information**



## **Ordering Information**

Base Part Number	D	Standard Pack	Complete Port Number	
base i ait i dilibei	Package Type	Form	Quantity	Complete Part Number
	D Dala E Land	Tube	75	AUIPS2031R
ALUDC2024 D		Tape and reel	2000	AUIPS2031RTR
AUIPS2031R	D-Pak-5-Lead	Tape and reel left	3000	AUIPS2031RTRL
		Tape and reel right	3000	AUIPS2031RTRR



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#### WORLD HEADQUARTERS:

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**Revision History** 

Revision	Date	Notes/Changes
A4	March, 25 <sup>th</sup> , 2010	Add tri-temp ds
A5	May, 10 <sup>th</sup> 2010	Update before qual
A6	November, 17 <sup>th</sup> 2010	Final release
A7	December, 7 <sup>th</sup> 2010	Remove ESD section page3
A8	December, 9 <sup>th</sup> 2010	Update qual page
В	June, 21 <sup>st</sup> 2012	Update storage temperature
С	December, 3 <sup>rd</sup> 2012	Update switching losses
		Add a note figure 11
D	April, 18 <sup>th</sup> 2013	Update Recommended Input resistor

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## Infineon:

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