

### **Product Brief**

# OPTIGA<sup>™</sup> TPM

## Certified security solutions for Trusted Computing in PC and embedded applications

The Infineon OPTIGA<sup>™</sup> TPM (Trusted Platform Module) family is a standardized security controller family which provides a wide range of security functions for embedded platforms. As the leader of Trusted Computing solutions Infineon offers a broad range of products meeting your specific requirements.

#### Security and functionality

All OPTIGA™ TPM products are based on Infineon's advanced hardware security technology. The products are designed according to the Trusted Computing Group (TCG) specifications and are certified Common Criteria<sup>1)</sup> CC EAL4+. The security functions include system and data integrity, authentication, secured communication, secured data storage and secured updates.

#### Performance and power

Implemented on a 16-bit state-of-the-art security controller from Infineon, the products meet the latest Microsoft Windows boot time and performance criteria. Furthermore the products are supported in Linux OS and derivates.

A sleep current of down to 110 µA allows smooth integration into a broad range of devices requiring power efficient battery operated designs.

#### Extended temperature range and packages

The OPTIGA™ TPM family supports an improved commercial temperature range as standard (-20 °C to +85 °C) as well as an extended temperature range (-40 °C to +85 °C) for industrial applications. The OPTIGA<sup>™</sup> TPM is available in TSSOP-28 package or the small VQFN-32 package (5 x 5 mm<sup>2</sup>), which saves precious board space on your mobile platforms.

#### Migrate to TCG TPM 2.0 today

Leading the way into the future Infineon is the first provider offering a TPM 2.0 product according to the latest specification. With this early expertise we support your smooth transition to the new standard.

### Key features<sup>2)</sup>

- > Standardized security controller
- > TCG certified products
- > Products with TPM 1.2 and 2.0
- > Standard & extended temperature range (-40 °C to +85 °C)
- > Firmware upgrade capability
- > SPI, I<sup>2</sup>C & LPC interface
- > VQFN-32 & TSSOP-28 package
- > CC and FIPS certification

#### **Customer values**

- > Innovative security solutions provided by the market leader
- > High confidence level based on Common Criteria certification
- > Easy integration based on standardization

#### Applications

- > Notebooks/PCs/tablets/servers
- > Networking components
- > Industrial automation
- > Single board devices
- > Home automation
- > Automotive
- 1) The Trusted Computing Group (TCG) specifications for the standardized TPM v1.2 only consider LPC and SPI interfaces. The I<sup>2</sup>C interface is not part of the TCG defined specification. The SLB 9645 is built on the TCG compliant, EAL4+ certified TPM hardware and firmware, with the addition of I<sup>2</sup>C support.
- 2) Not all features apply to all product configurations please refer to product data book for further details.





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#### Fully certified & state-of-the-art security

For new products of the Infineon OPTIGA<sup>™</sup> TPM family the Common Criteria certification is a key focus. As the first and still leading the list, Infineon had its TPMs listed on the official TCG product list showing that the TCG TPM standard is fulfilled.

Infineon is a driver in the Trusted Computing Group (TCG), the standardization organization formed to develop open, vendor-neutral, global industry standards for hardware security. With an Infineon representative serving as the TCG president and a strong presence and chairs in various working groups, Infineon is working on future security standards and driving innovation.



#### Product summary<sup>2)</sup>

| Sales code    | TPM version   | Interface        | Temperature<br>range | Package  | Common Criteria        | Typical / recommended use                                   |
|---------------|---------------|------------------|----------------------|----------|------------------------|---|
| SLB 9645      |               |                  |                      |          |                        |   |
| SLB 9645TT1.2 | 1.2 rev. 116  | I <sup>2</sup> C | -20 +85              | TSSOP-28 |                        | Notebook, desktops, tablets, mobile computing on non x86    |
| SLB 9645XQ1.2 | 1.2 rev. 116  | I <sup>2</sup> C | -40 +85              | VQFN-32  |                        | Industrial embedded computing on non x86                    |
| SLB 9645XT1.2 | 1.2 rev. 116  | I <sup>2</sup> C | -40 +85              | TSSOP-28 |                        | Industrial embedded computing on non x86                    |
| SLB 9660      |               |                  |                      |          |                        |   |
| SLB 9660TT1.2 | 1.2 rev. 116  | LPC              | -20 +85              | TSSOP-28 | $\checkmark$           | Notebook, desktops, tablets on x86 & embedded computing     |
| SLB 9660VQ1.2 | 1.2 rev. 116  | LPC              | -20 +85              | VQFN-32  | $\checkmark$           | Notebook, desktops, tablets on x86 & embedded computing     |
| SLB 9660XT1.2 | 1.2 rev. 116  | LPC              | -40 +85              | TSSOP-28 | ✓                      | Industrial embedded computing on x86                        |
| SLB 9660XQ1.2 | 1.2 rev. 116  | LPC              | -40 +85              | VQFN-32  | $\checkmark$           | Industrial embedded computing on x86                        |
| SLB 9665      |               |                  |                      |          |                        |   |
| SLB 9665TT2.0 | 2.0 rev. 1.16 | LPC              | -20 +85              | TSSOP-28 | $\checkmark$           | Notebook, desktops, tablets on x86/x64 & embedded computing |
| SLB 9665VQ2.0 | 2.0 rev. 1.16 | LPC              | -20 +85              | VQFN-32  | $\checkmark$           | Notebook, desktops, tablets on x86/x64 & embedded computing |
| SLB 9665XT2.0 | 2.0 rev. 1.16 | LPC              | -40 +85              | TSSOP-28 | ✓                      | Industrial embedded computing on x86/x64                    |
| SLB 9665XQ2.0 | 2.0 rev. 1.16 | LPC              | -40 +85              | VQFN-32  | ✓                      | Industrial embedded computing on x86/x64                    |
| SLB 9670      |               |                  |                      |          |                        |   |
| SLB 9670VQ1.2 | 1.2 rev. 116  | SPI              | -20 +85              | VQFN-32  | $\checkmark$           | All architectures   |
| SLB 9670XQ1.2 | 1.2 rev. 116  | SPI              | -40 +85              | VQFN-32  | ✓                      | All architectures   |
| SLB 9670VQ2.0 | 2.0 rev. 1.16 | SPI              | -20 +85              | VQFN-32  | <b>√</b> <sup>3)</sup> | All architectures   |
| SLB 9670XQ2.0 | 2.0 rev. 1.16 | SPI              | -40 +85              | VQFN-32  | <b>√</b> <sup>3)</sup> | All architectures   |

2) Not all features apply to all product configurations – please refer to product data book for further details.

3) Certification ongoing

#### OPTIGA<sup>™</sup> product family

Infineon's OPTIGA<sup>™</sup> family consists of products and solutions for securing embedded systems. All products are based on secured hardware and software. In addition to the OPTIGA<sup>™</sup> TPM products,

the overall product family also includes the OPTIGA<sup>™</sup> Trust line of products with products and solutions for device authentication.

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