ITS4040D Evaluation Board Rev. 1.0

Schematics & Layout

Board Overview

Voltage Regulator
providing 5V logic level out of VEXT (optional usage)

Jumper
Enables/disable „V_S presence" LED

Jumper
To configure desired V_{IN} source for input signals

V_{EXT}
Supply for 5V voltage regulator

V_{IN}
Global input voltage from external source

V_S -Supply

Input Pin terminals
Individual V_{IN} for each channel from external source

Two alternative sets of terminals for V_{OUT}
Each channel has a LED option for active state.

STATUS Pin
Global Status Output

GND

GND

Jumper
Enables/disables STATUS Pin LED

DIP – Switch
Controls input voltage level High/Low per channel when global V_{IN} from external source is used

DIP – Switch
Enables/disables LED option per output channel

ITS4040D -EP-D

Schematics

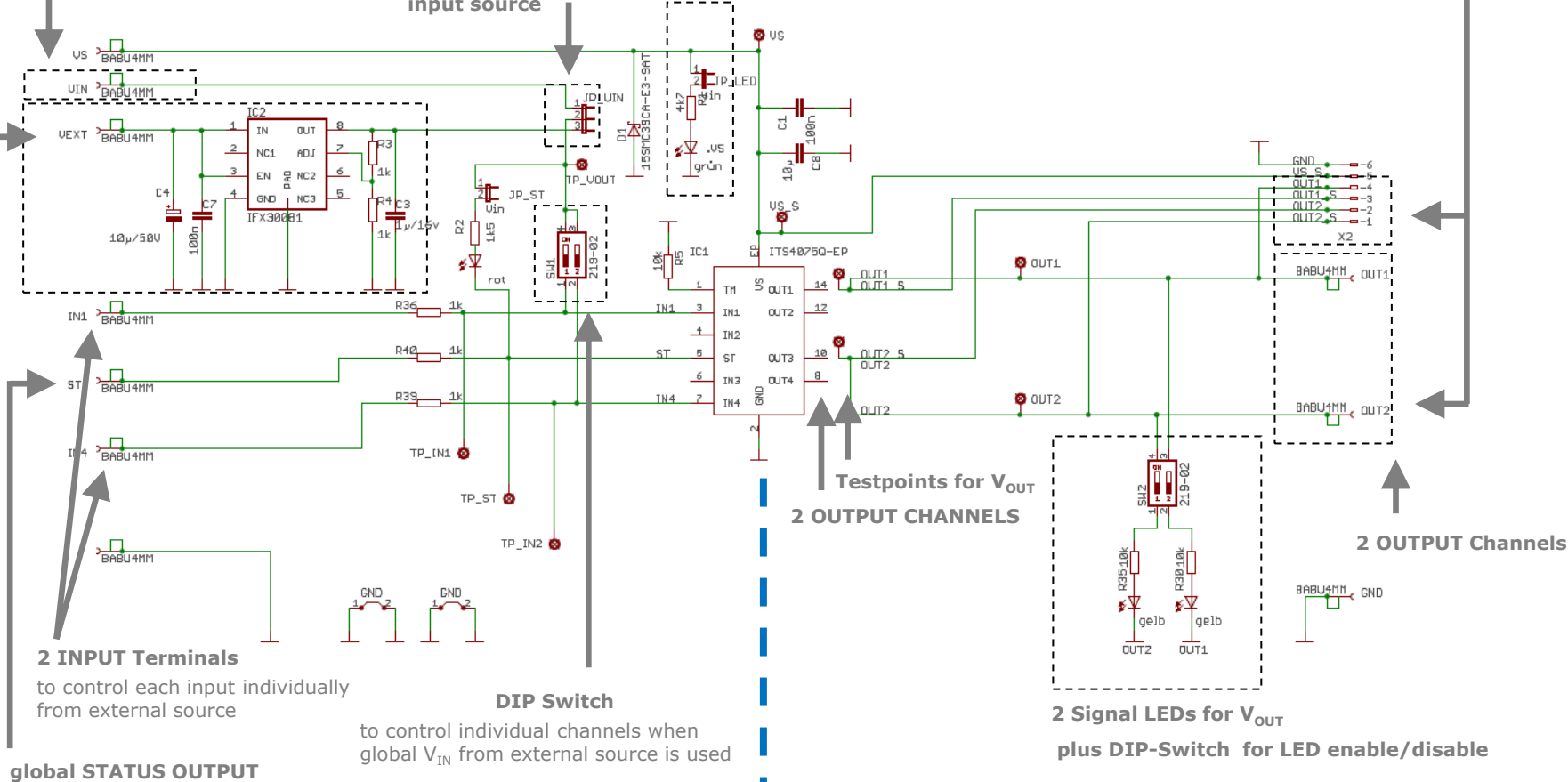**Voltage Regulator to generate 5V-logic levels for inputs**

V$_{IN}$ Terminal: for providing external levels globally to all input pins

„Power-Up" LED
Indicates whether V$_S$ is applied
Plus jumper to dsable LED

Jumper to select input source

Two alternative sets of terminals for V$_{OUT}$:
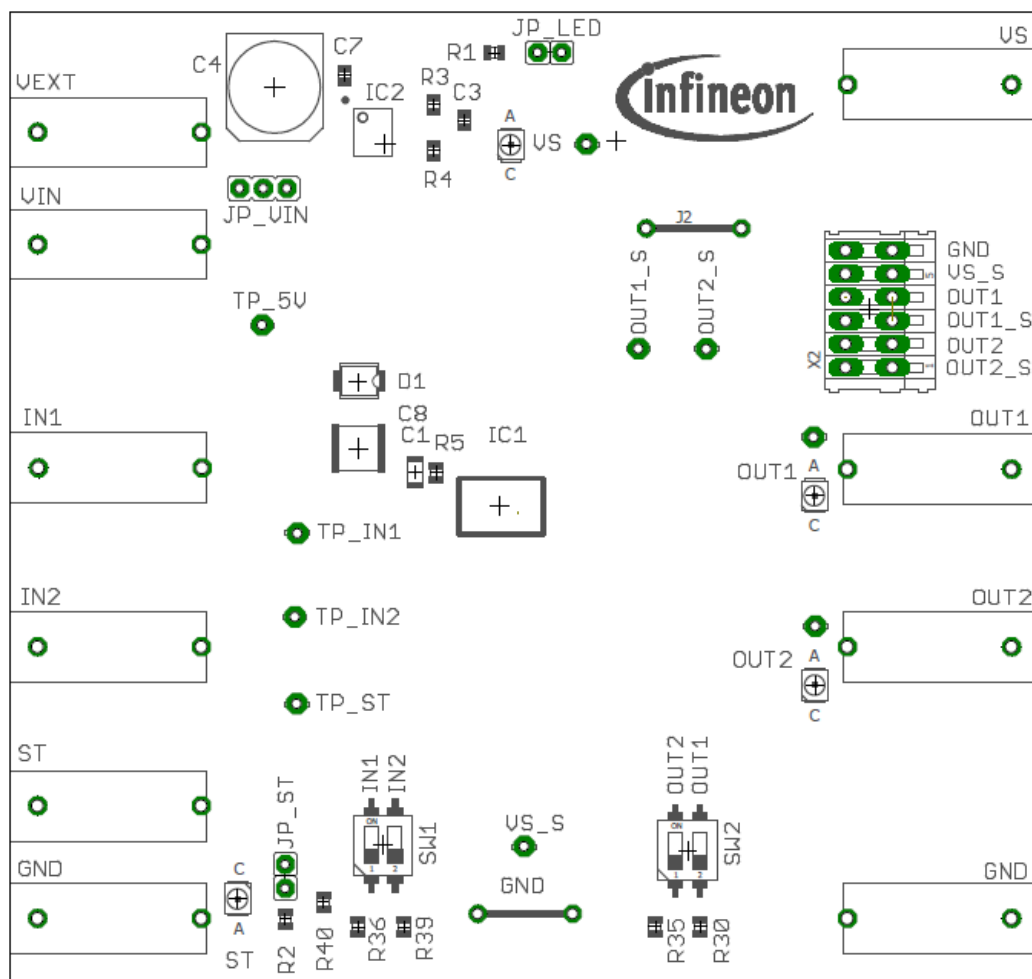- **Banana Plugs**
- **Bare wire Connector**

Testpoints for V$_{OUT}$

2 OUTPUT CHANNELS

2 OUTPUT Channels

2 INPUT Terminals
to control each input individually from external source

global STATUS OUTPUT

DIP Switch
to control individual channels when global V$_{IN}$ from external source is used

2 Signal LEDs for V$_{OUT}$
plus DIP-Switch for LED enable/disable

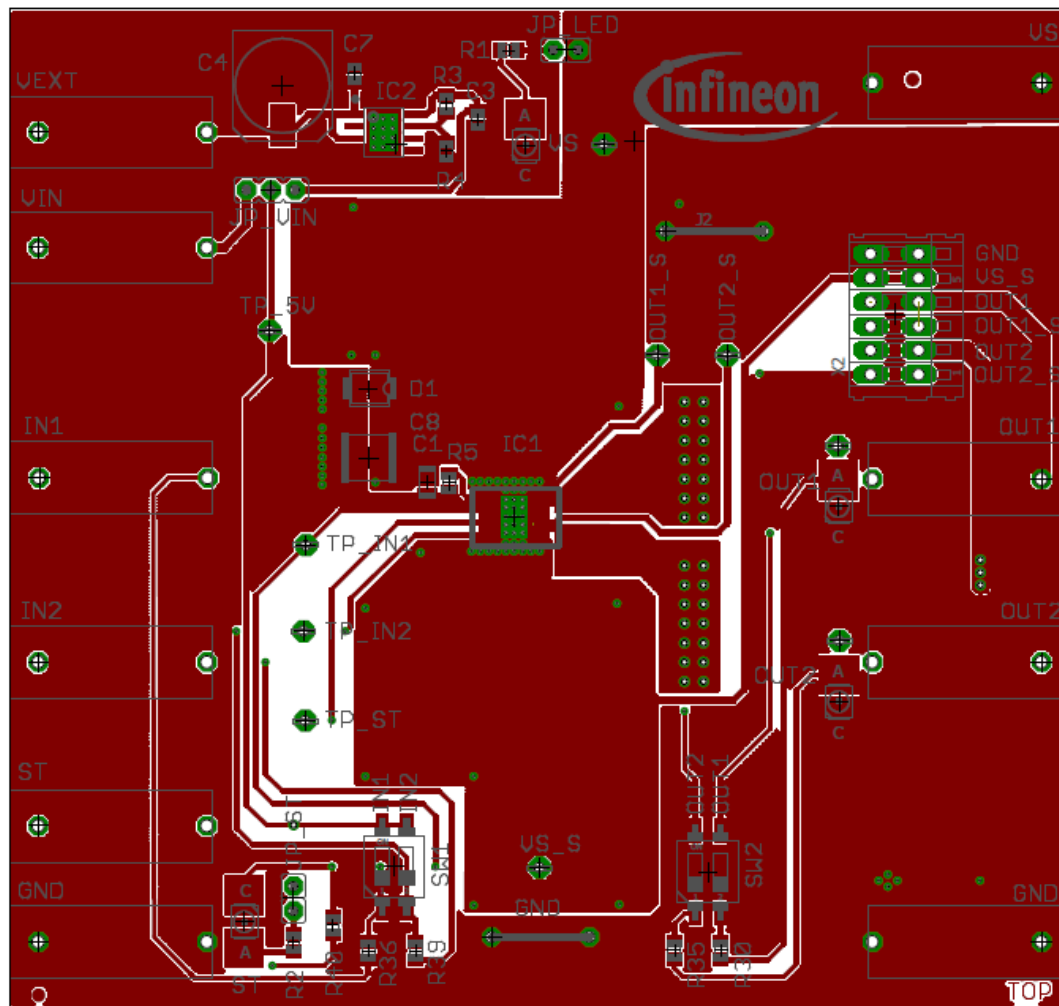**CONTROL/INPUT - Side**

POWER/OUTPUT - Side
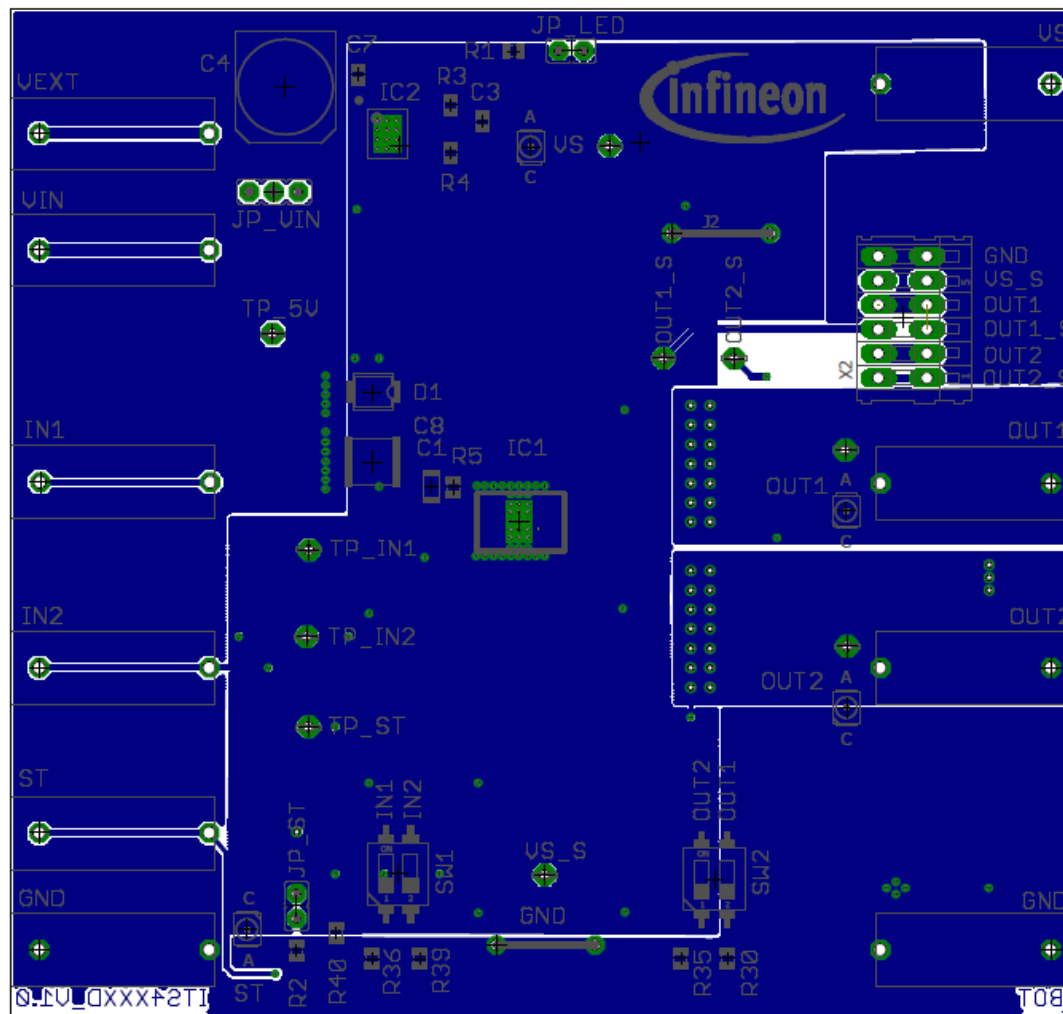
Top View

IC1 = ITS4040D 2-channel Industrial PROFET
IC2 = IFX30081 VREG as option to generate logic levels for input pins

Top Layer

Bottom Layer

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