

IRS10752LPBF

μΗVIC[™]

SOT-23 High-Side Gate Driver IC

Features

- Floating gate driver designed for bootstrap operation
- Fully operational to +100 V .
- Excellent dv/dt immunity •
- Excellent negative Vs transient immunity
- Wide V_{CC} range •
- UVLO on low-side and high-side
- Schmitt-trigger input with internal pull-down
- Output in phase with input
- Excellent latch immunity on all inputs & outputs
- **RoHS** compliant •
- 6-pin SOT-23 package

Applications

- High-side gate driver control •
- Pulse transformer replacement
- General purpose switched mode power electronics

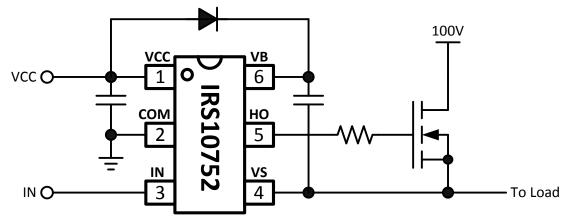
Description

The IRS10752 is a high-side, single-channel gate driver IC with 100V blocking and levelshifting capability. This allows for the gate driver to be connected directly to the gate of a high-side power MOSFET, while being controlled by the low-side, ground potential circuitry. The IRS10752 includes a wide V_{CC} supply range, UVLO protection, and excellent immunity to harsh dv/dt or -Vs switching environments. IR's HVIC technology allows for these functions and features to be realized in a 6-pin SOT-23 package.

Package Options



Typical Connection Diagram



Ordering Information

		Standar	d Pack		
Base Part Number	Package Type	Form	Quantity	Orderable Part Number	
IRS10752LPBF	SOT-23-6L	Tape and Reel	3000	IRS10752LTRPBF	



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Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM, all currents are defined positive into any pin. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

Symbol	Definition		Min	Мах	Units
V _B	High side floating absolute voltage		-0.3	120	
Vs	High side floating supply offset volt	age	V _B - 20	V _B + 0.3	
V _{HO}	High side floating gate drive output	voltage	V _S - 0.3	V _B + 0.3	
V _{cc}	Low side and logic fixed supply vol	tage	-0.3	20	- V
V _{IN}	Logic input voltage		COM - 0.3	V _{CC} + 0.3	
СОМ	Logic ground		V _{CC} - 20	V _{CC} + 0.3	
dVS/dt	High side floating supply offset volt	High side floating supply offset voltage slew rate		50	V/ns
R _{ØJA}	Thermal resistance, junction to ambient	6L-SOT-23		151	°C/W
TJ	Junction temperature		55	150	
Ts	Storage temperature		55	150	°C
TL	IC Pin temperature (soldering, 10 seconds)			300	

Recommended Operating Conditions

For proper operation the device should be used within the recommended conditions.

Symbol	Definition	Min	Max	Units
V _B	High side floating absolute voltage	V _s + 10	V _S + 18	
Vs	High side floating supply offset voltage	COM - 8 [†]	100	
V _{HO}	High side floating gate drive output voltage	Vs	V _B	V
V _{cc}	Low side and logic fixed supply voltage	10	18	
V _{IN}	Logic input voltage	СОМ	V _{cc}	
TJ	Junction temperature	-40	125	°C

[†] Logic operational for V_S of -8V to +100V. Logic state held for V_S of -8V to $-V_{BS}$.



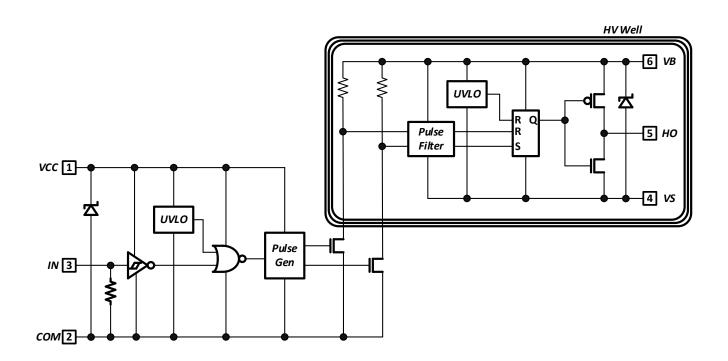
Electrical Characteristics

 $V_{CC}{=}15V,\,V_{BS}{=}15V,\,C_{L}{=}1000pF,\,and\,T_{A}{=}25~^{\circ}C$ unless otherwise specified.

Symbol	Definition	Min	Тур	Max	Units	Test Conditions
Low Side Ch	aracteristics	•			<u> </u>	
V _{CCUV+}	V _{CC} supply UVLO positive-going	8.0	9.0	10.0	V	
V _{CCUV-}	V _{CC} supply UVLO negative-going	7.0	8.0	9.0	V	
I _{QCC}	Quiescent V _{CC} supply current		100		μA	
V_{CC_CLAMP}	V _{CC} internal Zener clamp voltage		20.4			$I_{CC} = 5mA$
V _{IH}	Logic "1" input voltage			2.2	V	
VIL	Logic "0" input voltage	0.8				
I _{IN+}	Logic "1" input bias current		20	40		$V_{IN} = V_{CC}$
I _{IN-}	Logic "0" input bias current			5	μA	$V_{IN} = COM$
High Side Cl	haracteristics	•				
V _{BSUV+}	V _{BS} supply UVLO positive-going	8.0	9.0	10.0		
V _{BSUV-}	V _{BS} supply UVLO negative-going	7.0	8.0	9.0		
V_{BS_CLAMP}	V _{BS} internal Zener clamp voltage		20.4		V	I _{BS} = 5mA
V _{OH}	High level output voltage (V _B – HO)		0.8	1.4		$I_0 = 2mA$
V _{OL}	Low level output voltage (HO – V_S)		0.3	0.6		
I _{LK}	Offset supply leakage current			50		$V_B = V_S = 100V$
I _{QBS}	Quiescent V _{BS} supply current		80		μA	$V_{IN} = V_{CC}$ or COM
Gate Drive C	haracteristics					
t _{on}	Turn-on propagation delay		140			$V_{S} = 0V$
t _{OFF}	Turn-off propagation delay		215		n 2	$V_{\rm S} = 100 V$
t _{RISE}	Turn-on rise time		85		ns	
t _{FALL}	Turn-off fall time		40			$V_{S} = 0V$
I _{O+}	HO gate drive output source current		160		~ ^	
I _{O-}	HO gate drive output sink current		240		mA	
			•			

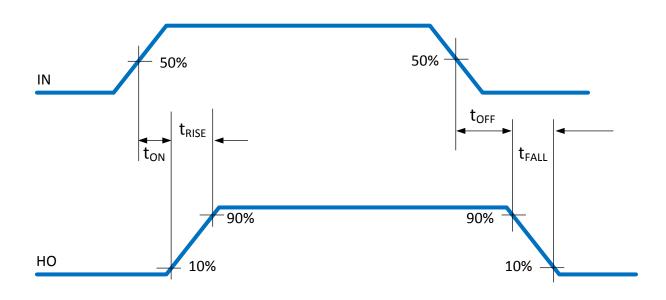


Functional Block Diagram





Timing Diagram

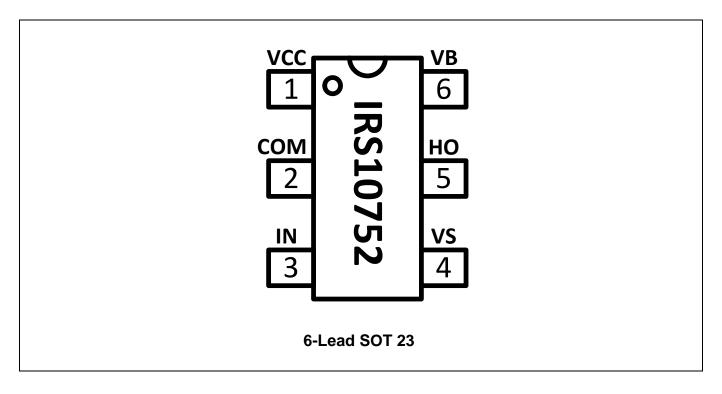




Pin Definitions

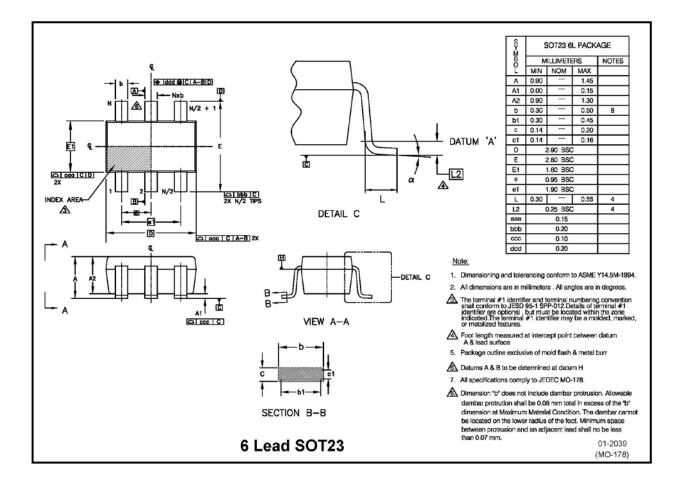
Pin	Symbol	Description	
1	VCC	IC supply voltage	
2	СОМ	IC power and signal ground	
3	IN	Logic input	
4	VS	High side floating supply offset voltage	
5	НО	High side gate driver output	
6	VB	High side floating supply voltage	

Pin Assignments





Package Details: 6L-SOT23





Tape and Reel Details: 6L-SOT23

CARRIER TAPE

WIDTH

8

12

16 24

32 44

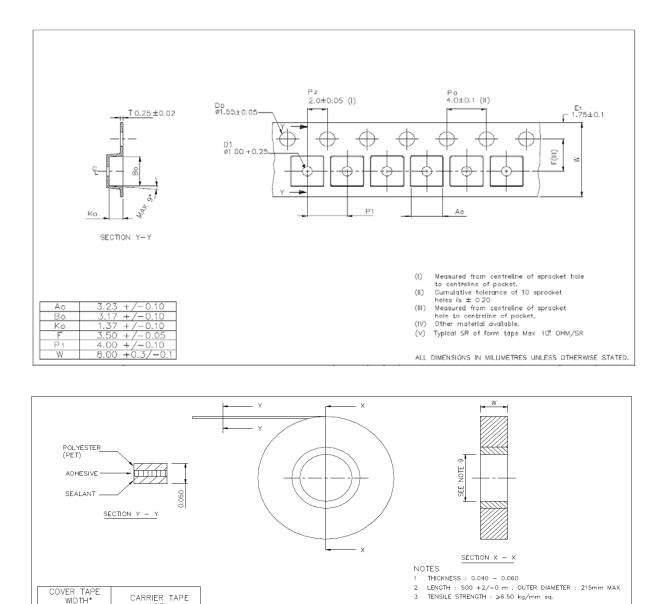
WIDTH* (W ±0.1)

9.2, 9.5

13.3, 13.5 21.0, 21.3

25.5. 26.8

37.5 49.5



3

4

5

6

ELONGATION : ≥80%

SURFACE RESISTIVITY : ≤10E11 OHMS/SQ (BOTH SIDES) (ANTH-STATIC) PEEL STRENGTH CONFORMS TO EIA SPEC.

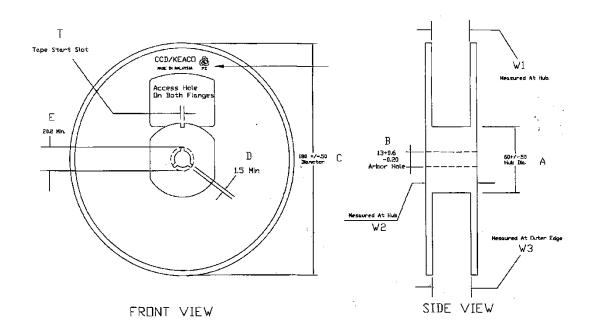
6 PFEL STRENGTH CONFORMS TO EIA SPEC. 7 RECOMMENDED SHELF LIFE : TWO YEAR FROM MANUFACTURING DATE 8 LUMINOUS TRANSMITTANCE : >80 % 9 3 INCH INTERNAL DIAMETER : @76.5±1.0 2 INCH INTERNAL DIAMETER : @50+1.7/-0 *10 OTHER COVER TAPE WIDTH REFER TO W4.08-04.

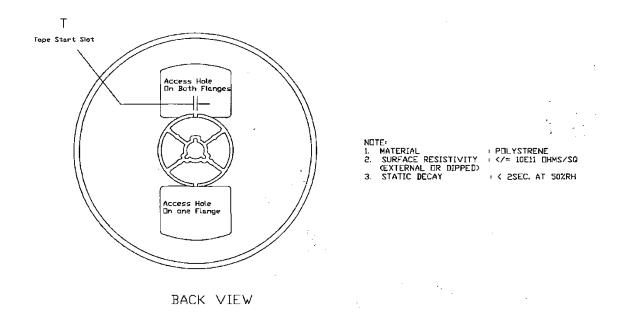
ALL DIMENSIONS IN MILLIMETRES UNLESS OTHERWISE STATED.



IRS10752LPBF

Tape and Reel Details: 6L-SOT23

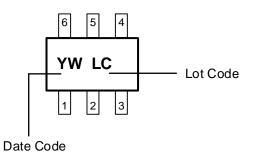




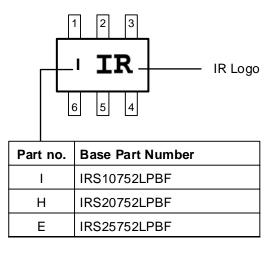


Part Marking Information: 6 Lead SOT23

Top Marking



Bottom Marking





Qualification Information[†]

Qualification Level		(pe	Industrial ^{††} (per JEDEC JESD 47E)			
		Comments: This family of ICs has passed JEDEC's				
			Industrial qualification. IR's Consumer qualification level is			
		granted by extension	granted by extension of the higher Industrial level.			
Moisturo Sonsitivity	Moisture Sensitivity Level		MSL1 ^{†††}			
woisture Sensitivity			(per IPC/JEDEC J-STD-020C)			
	Machine Model		Class B			
ESD		(per JEDEC s	(per JEDEC standard EIA/JESD22-A115-A)			
ESD	Human Body Model		Class 1B			
		(per EIA/JED	(per EIA/JEDEC standard JESD22-A114-B)			
IC Latch-Up Test			Class I, Level A			
			(per JESD78A)			
RoHS Compliant			Yes			

† Qualification standards can be found at International Rectifier's web site http://www.infineon.com/

- ++ Higher qualification ratings may be available should the user have such requirements. Please contact your International Rectifier sales representative for further information.
- +++ Higher MSL ratings may be available for the specific package types listed here. Please contact your International Rectifier sales representative for further information.

Revision History

Date	Comment
01/04/2017	 Updated "Infineon" logo –all pages. Added disclaimer on last page. Updated part marking information on page 11.



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