

# MOSFET

Metal Oxide Semiconductor Field Effect Transistor

## CoolMOS™ C7

600V CoolMOS™ C7 Power Transistor  
IPP60R120C7

## Data Sheet

Rev. 2.0  
Final

## 1 Description

CoolMOS™ C7 is a revolutionary technology for high voltage power MOSFETs, designed according to the superjunction (SJ) principle and pioneered by Infineon Technologies.

600V CoolMOS™ C7 series combines the experience of the leading SJ MOSFET supplier with high class innovation.

The 600V C7 is the first technology ever with  $R_{DS(on)} \cdot A$  below  $10\text{m}\Omega \cdot \text{mm}^2$ .

## Features

- Suitable for hard and soft switching (PFC and high performance LLC)
- Increased MOSFET  $dv/dt$  ruggedness to  $120\text{V/ns}$
- Increased efficiency due to best in class FOM  $R_{DS(on)} \cdot E_{oss}$  and  $R_{DS(on)} \cdot Q_g$
- Best in class  $R_{DS(on)}$  /package
- Qualified for industrial grade applications according to JEDEC (J-STD20 and JESD22)

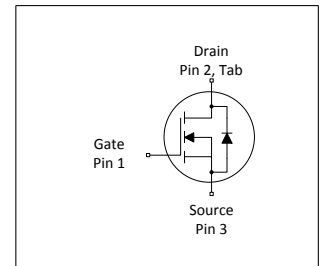
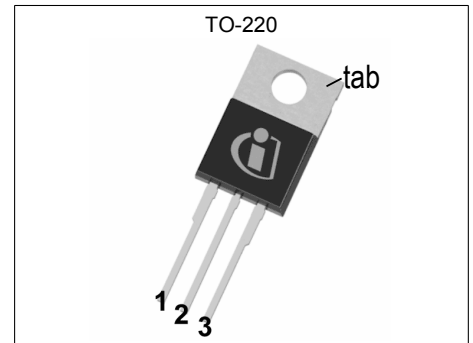
## Benefits

- Increased economies of scale by use in PFC and PWM topologies in the application
- Higher  $dv/dt$  limit enables faster switching leading to higher efficiency
- Enabling higher system efficiency by lower switching losses
- Increased power density solutions due to smaller packages
- Suitable for applications such as server, telecom and solar
- Higher switching frequencies possible without loss in efficiency due to low  $E_{oss}$  and  $Q_g$

## Applications

PFC stages and PWM stages (TTF, LLC) for high power/performance SMPS e.g. Computing, Server, Telecom, UPS and Solar.

*Please note: For MOSFET paralleling the use of ferrite beads on the gate or separate totem poles is generally recommended.*



**Table 1 Key Performance Parameters**

| Parameter                                    | Value | Unit                   |
|----------------------------------------------|-------|------------------------|
| $V_{DS} @ T_{j,max}$                         | 650   | V                      |
| $R_{DS(on),max}$                             | 120   | $\text{m}\Omega$       |
| $Q_{g,typ}$                                  | 34    | nC                     |
| $I_{D,pulse}$                                | 66    | A                      |
| $I_{D,continuous} @ T_j < 150^\circ\text{C}$ | 31    | A                      |
| $E_{oss}@400\text{V}$                        | 4     | $\mu\text{J}$          |
| Body diode $di/dt$                           | 360   | $\text{A}/\mu\text{s}$ |

| Type / Ordering Code | Package   | Marking | Related Links  |
|----------------------|-----------|---------|----------------|
| IPP60R120C7          | PG-TO 220 | 60C7120 | see Appendix A |

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## 2 Maximum ratings

at  $T_j = 25^\circ\text{C}$ , unless otherwise specified

**Table 2 Maximum ratings**

| Parameter                              | Symbol        | Values |      |          | Unit             | Note / Test Condition                                                                         |
|----------------------------------------|---------------|--------|------|----------|------------------|-----------------------------------------------------------------------------------------------|
|                                        |               | Min.   | Typ. | Max.     |                  |                                                                                               |
| Continuous drain current <sup>1)</sup> | $I_D$         | -      | -    | 19<br>12 | A                | $T_C=25^\circ\text{C}$<br>$T_C=100^\circ\text{C}$                                             |
| Pulsed drain current <sup>2)</sup>     | $I_{D,pulse}$ | -      | -    | 66       | A                | $T_C=25^\circ\text{C}$                                                                        |
| Avalanche energy, single pulse         | $E_{AS}$      | -      | -    | 78       | mJ               | $I_D=4.4\text{A}$ ; $V_{DD}=50\text{V}$ ; see table 10                                        |
| Avalanche energy, repetitive           | $E_{AR}$      | -      | -    | 0.39     | mJ               | $I_D=4.4\text{A}$ ; $V_{DD}=50\text{V}$ ; see table 10                                        |
| Avalanche current, single pulse        | $I_{AS}$      | -      | -    | 4.4      | A                | -                                                                                             |
| MOSFET dv/dt ruggedness                | dv/dt         | -      | -    | 120      | V/ns             | $V_{DS}=0\dots400\text{V}$                                                                    |
| Gate source voltage (static)           | $V_{GS}$      | -20    | -    | 20       | V                | static;                                                                                       |
| Gate source voltage (dynamic)          | $V_{GS}$      | -30    | -    | 30       | V                | AC ( $f>1\text{ Hz}$ )                                                                        |
| Power dissipation                      | $P_{tot}$     | -      | -    | 92       | W                | $T_C=25^\circ\text{C}$                                                                        |
| Storage temperature                    | $T_{stg}$     | -55    | -    | 150      | $^\circ\text{C}$ | -                                                                                             |
| Operating junction temperature         | $T_j$         | -55    | -    | 150      | $^\circ\text{C}$ | -                                                                                             |
| Mounting torque                        | -             | -      | -    | 60       | Ncm              | M3 and M3.5 screws                                                                            |
| Continuous diode forward current       | $I_S$         | -      | -    | 19       | A                | $T_C=25^\circ\text{C}$                                                                        |
| Diode pulse current <sup>2)</sup>      | $I_{S,pulse}$ | -      | -    | 66       | A                | $T_C=25^\circ\text{C}$                                                                        |
| Reverse diode dv/dt <sup>3)</sup>      | dv/dt         | -      | -    | 20       | V/ns             | $V_{DS}=0\dots400\text{V}$ , $I_{SD}\leq 6.7\text{A}$ , $T_j=25^\circ\text{C}$<br>see table 8 |
| Maximum diode commutation speed        | di/dt         | -      | -    | 360      | A/ $\mu\text{s}$ | $V_{DS}=0\dots400\text{V}$ , $I_{SD}\leq 6.7\text{A}$ , $T_j=25^\circ\text{C}$<br>see table 8 |
| Insulation withstand voltage           | $V_{ISO}$     | -      | -    | n.a.     | V                | $V_{rms}$ , $T_C=25^\circ\text{C}$ , $t=1\text{min}$                                          |

<sup>1)</sup> Limited by  $T_{j,max}$ .

<sup>2)</sup> Pulse width  $t_p$  limited by  $T_{j,max}$

<sup>3)</sup> Identical low side and high side switch

### 3 Thermal characteristics

**Table 3 Thermal characteristics**

| Parameter                                                  | Symbol     | Values |      |       | Unit | Note / Test Condition               |
|------------------------------------------------------------|------------|--------|------|-------|------|-------------------------------------|
|                                                            |            | Min.   | Typ. | Max.  |      |                                     |
| Thermal resistance, junction - case                        | $R_{thJC}$ | -      | -    | 1.357 | °C/W | -                                   |
| Thermal resistance, junction - ambient                     | $R_{thJA}$ | -      | -    | 62    | °C/W | leaded                              |
| Thermal resistance, junction - ambient for SMD version     | $R_{thJA}$ | -      | -    | -     | °C/W | n.a.                                |
| Soldering temperature, wavesoldering only allowed at leads | $T_{sold}$ | -      | -    | 260   | °C   | 1.6mm (0.063 in.) from case for 10s |

## 4 Electrical characteristics

at  $T_j=25^\circ\text{C}$ , unless otherwise specified

**Table 4 Static characteristics**

| Parameter                        | Symbol        | Values |                |            | Unit          | Note / Test Condition                                                                                                                 |
|----------------------------------|---------------|--------|----------------|------------|---------------|---------------------------------------------------------------------------------------------------------------------------------------|
|                                  |               | Min.   | Typ.           | Max.       |               |                                                                                                                                       |
| Drain-source breakdown voltage   | $V_{(BR)DSS}$ | 600    | -              | -          | V             | $V_{GS}=0\text{V}$ , $I_D=1\text{mA}$                                                                                                 |
| Gate threshold voltage           | $V_{(GS)th}$  | 3      | 3.5            | 4          | V             | $V_{DS}=V_{GS}$ , $I_D=0.39\text{mA}$                                                                                                 |
| Zero gate voltage drain current  | $I_{DSS}$     | -      | -              | 1          | $\mu\text{A}$ | $V_{DS}=600$ , $V_{GS}=0\text{V}$ , $T_j=25^\circ\text{C}$<br>$V_{DS}=600$ , $V_{GS}=0\text{V}$ , $T_j=150^\circ\text{C}$             |
| Gate-source leakage current      | $I_{GSS}$     | -      | -              | 100        | nA            | $V_{GS}=20\text{V}$ , $V_{DS}=0\text{V}$                                                                                              |
| Drain-source on-state resistance | $R_{DS(on)}$  | -      | 0.103<br>0.231 | 0.120<br>- | $\Omega$      | $V_{GS}=10\text{V}$ , $I_D=7.8\text{A}$ , $T_j=25^\circ\text{C}$<br>$V_{GS}=10\text{V}$ , $I_D=7.8\text{A}$ , $T_j=150^\circ\text{C}$ |
| Gate resistance                  | $R_G$         | -      | 0.83           | -          | $\Omega$      | $f=1\text{MHz}$ , open drain                                                                                                          |

**Table 5 Dynamic characteristics**

| Parameter                                                  | Symbol       | Values |      |      | Unit | Note / Test Condition                                                                             |
|------------------------------------------------------------|--------------|--------|------|------|------|---------------------------------------------------------------------------------------------------|
|                                                            |              | Min.   | Typ. | Max. |      |                                                                                                   |
| Input capacitance                                          | $C_{iss}$    | -      | 1500 | -    | pF   | $V_{GS}=0\text{V}$ , $V_{DS}=400\text{V}$ , $f=250\text{kHz}$                                     |
| Output capacitance                                         | $C_{oss}$    | -      | 27   | -    | pF   | $V_{GS}=0\text{V}$ , $V_{DS}=400\text{V}$ , $f=250\text{kHz}$                                     |
| Effective output capacitance, energy related <sup>1)</sup> | $C_{o(er)}$  | -      | 50   | -    | pF   | $V_{GS}=0\text{V}$ , $V_{DS}=0\dots400\text{V}$                                                   |
| Effective output capacitance, time related <sup>2)</sup>   | $C_{o(tr)}$  | -      | 515  | -    | pF   | $I_D=\text{constant}$ , $V_{GS}=0\text{V}$ , $V_{DS}=0\dots400\text{V}$                           |
| Turn-on delay time                                         | $t_{d(on)}$  | -      | 10.7 | -    | ns   | $V_{DD}=400\text{V}$ , $V_{GS}=13\text{V}$ , $I_D=7.8\text{A}$ ,<br>$R_G=5.3\Omega$ ; see table 9 |
| Rise time                                                  | $t_r$        | -      | 7    | -    | ns   | $V_{DD}=400\text{V}$ , $V_{GS}=13\text{V}$ , $I_D=7.8\text{A}$ ,<br>$R_G=5.3\Omega$ ; see table 9 |
| Turn-off delay time                                        | $t_{d(off)}$ | -      | 52   | -    | ns   | $V_{DD}=400\text{V}$ , $V_{GS}=13\text{V}$ , $I_D=7.8\text{A}$ ,<br>$R_G=5.3\Omega$ ; see table 9 |
| Fall time                                                  | $t_f$        | -      | 5    | -    | ns   | $V_{DD}=400\text{V}$ , $V_{GS}=13\text{V}$ , $I_D=7.8\text{A}$ ,<br>$R_G=5.3\Omega$ ; see table 9 |

**Table 6 Gate charge characteristics**

| Parameter             | Symbol               | Values |      |      | Unit | Note / Test Condition                                                 |
|-----------------------|----------------------|--------|------|------|------|-----------------------------------------------------------------------|
|                       |                      | Min.   | Typ. | Max. |      |                                                                       |
| Gate to source charge | $Q_{gs}$             | -      | 8    | -    | nC   | $V_{DD}=400\text{V}$ , $I_D=7.8\text{A}$ , $V_{GS}=0$ to $10\text{V}$ |
| Gate to drain charge  | $Q_{gd}$             | -      | 11   | -    | nC   | $V_{DD}=400\text{V}$ , $I_D=7.8\text{A}$ , $V_{GS}=0$ to $10\text{V}$ |
| Gate charge total     | $Q_g$                | -      | 34   | -    | nC   | $V_{DD}=400\text{V}$ , $I_D=7.8\text{A}$ , $V_{GS}=0$ to $10\text{V}$ |
| Gate plateau voltage  | $V_{\text{plateau}}$ | -      | 5.0  | -    | V    | $V_{DD}=400\text{V}$ , $I_D=7.8\text{A}$ , $V_{GS}=0$ to $10\text{V}$ |

<sup>1)</sup>  $C_{o(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 400V

<sup>2)</sup>  $C_{o(tr)}$  is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 400V

**Table 7 Reverse diode characteristics**

| Parameter                     | Symbol    | Values |      |      | Unit    | Note / Test Condition                                        |
|-------------------------------|-----------|--------|------|------|---------|--------------------------------------------------------------|
|                               |           | Min.   | Typ. | Max. |         |                                                              |
| Diode forward voltage         | $V_{SD}$  | -      | 0.9  | -    | V       | $V_{GS}=0V$ , $I_F=7.8A$ , $T_j=25^{\circ}C$                 |
| Reverse recovery time         | $t_{rr}$  | -      | 310  | -    | ns      | $V_R=400V$ , $I_F=7.8A$ , $di_F/dt=100A/\mu s$ ; see table 8 |
| Reverse recovery charge       | $Q_{rr}$  | -      | 3.5  | -    | $\mu C$ | $V_R=400V$ , $I_F=7.8A$ , $di_F/dt=100A/\mu s$ ; see table 8 |
| Peak reverse recovery current | $I_{rrm}$ | -      | 24   | -    | A       | $V_R=400V$ , $I_F=7.8A$ , $di_F/dt=100A/\mu s$ ; see table 8 |

## 5 Electrical characteristics diagrams

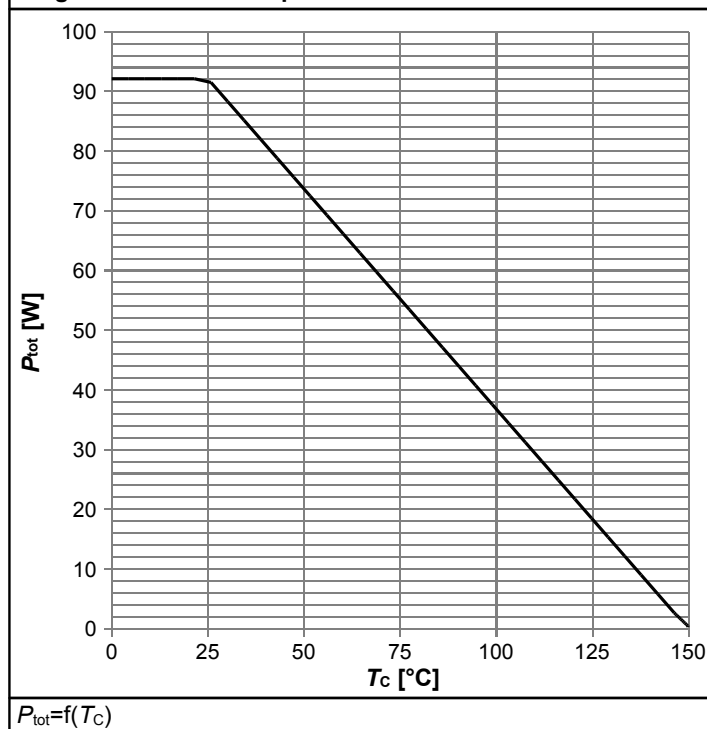
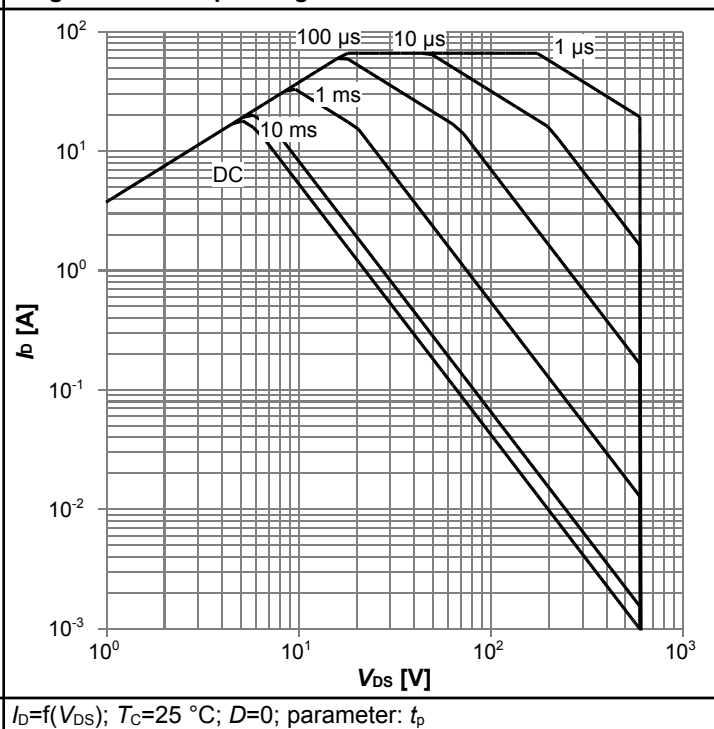
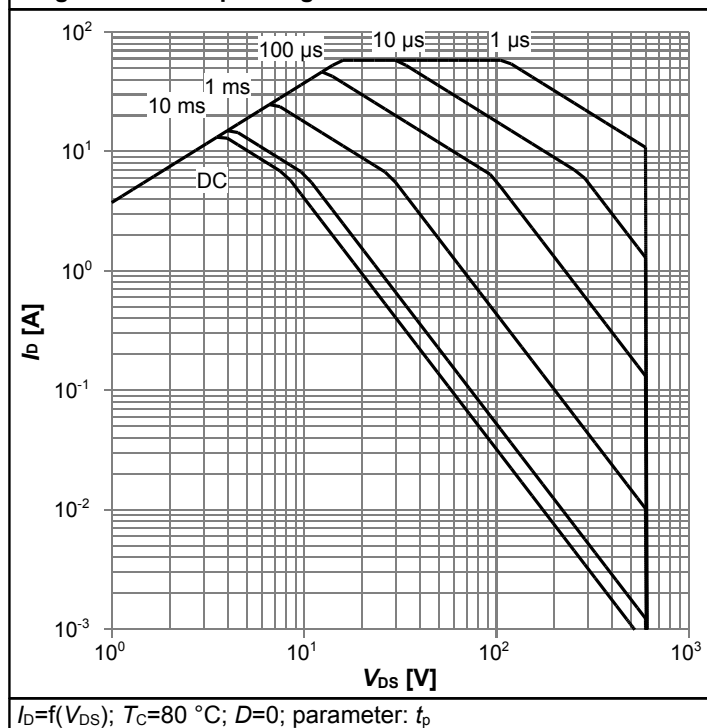
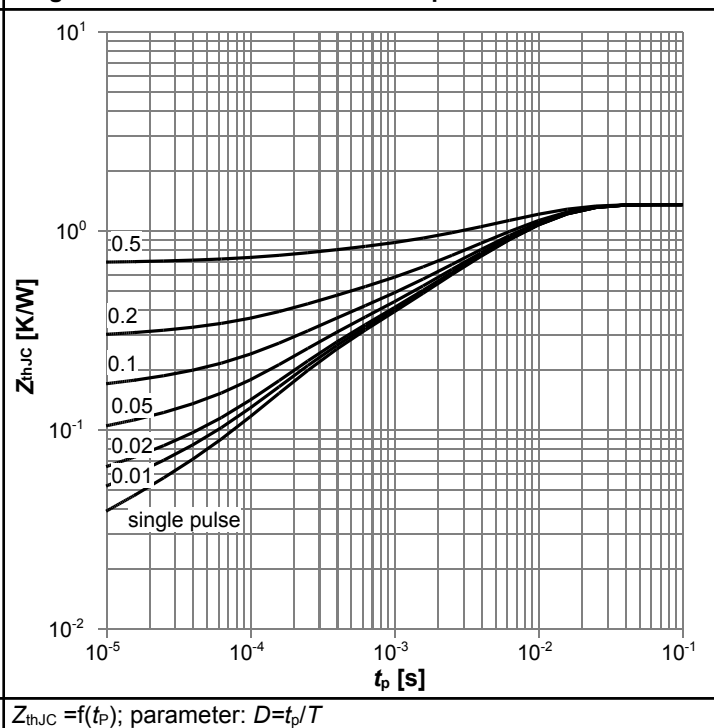
**Diagram 1: Power dissipation**

**Diagram 2: Safe operating area**

**Diagram 3: Safe operating area**

**Diagram 4: Max. transient thermal impedance**




Diagram 5: Typ. output characteristics

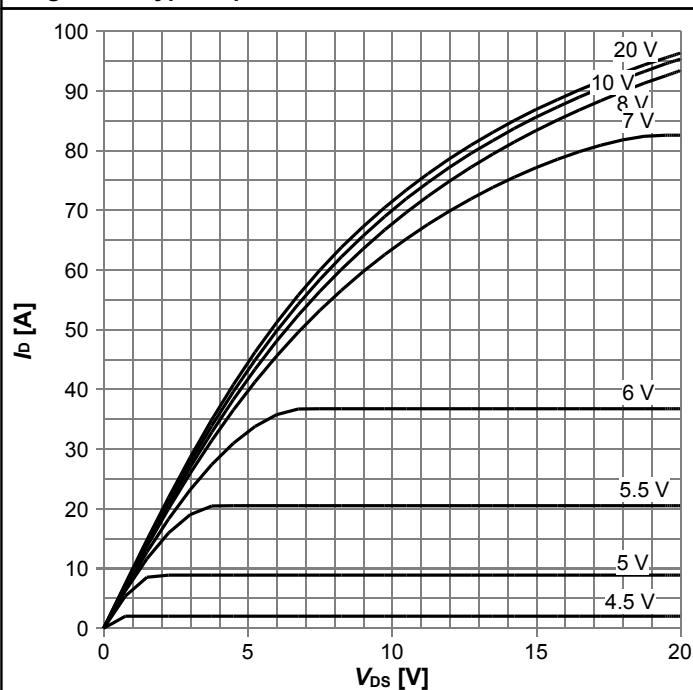

 $I_D = f(V_{DS}); T_J = 25^\circ\text{C}; \text{parameter: } V_{GS}$ 

Diagram 6: Typ. output characteristics

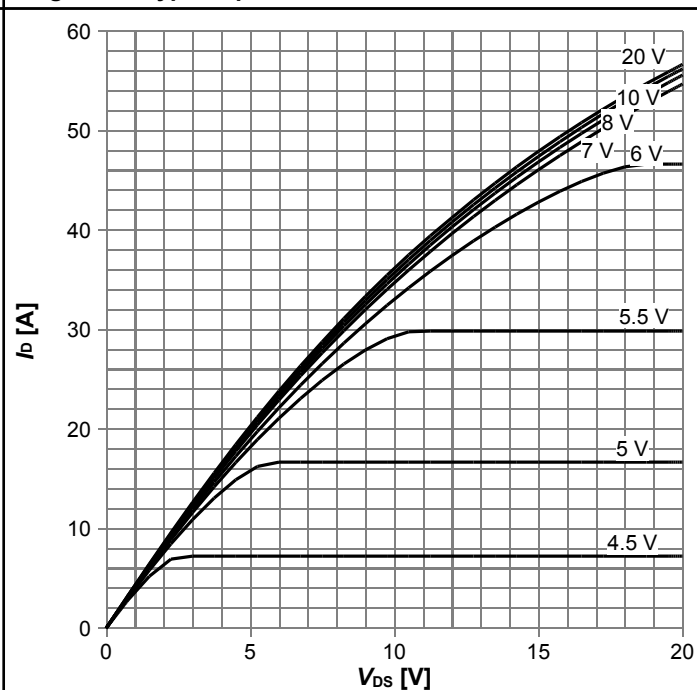

 $I_D = f(V_{DS}); T_J = 125^\circ\text{C}; \text{parameter: } V_{GS}$ 

Diagram 7: Typ. drain-source on-state resistance

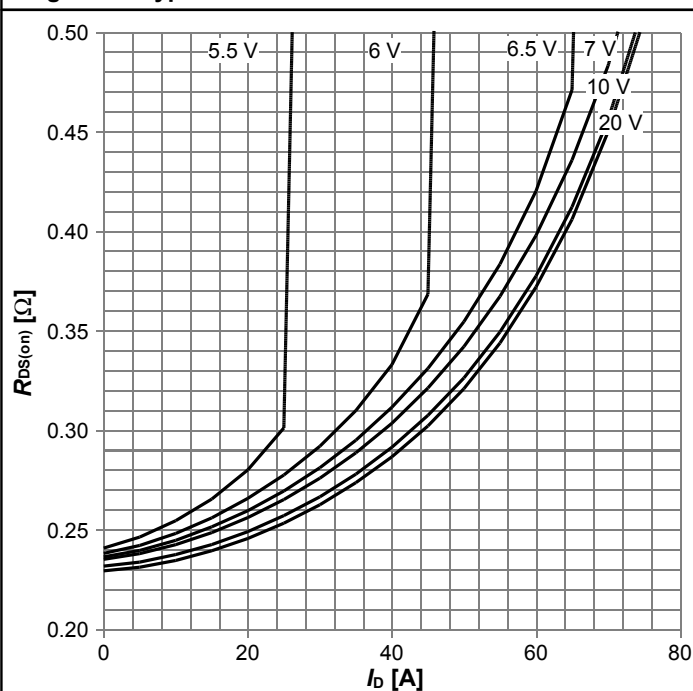

 $R_{DS(on)} = f(I_D); T_J = 125^\circ\text{C}; \text{parameter: } V_{GS}$ 

Diagram 8: Drain-source on-state resistance

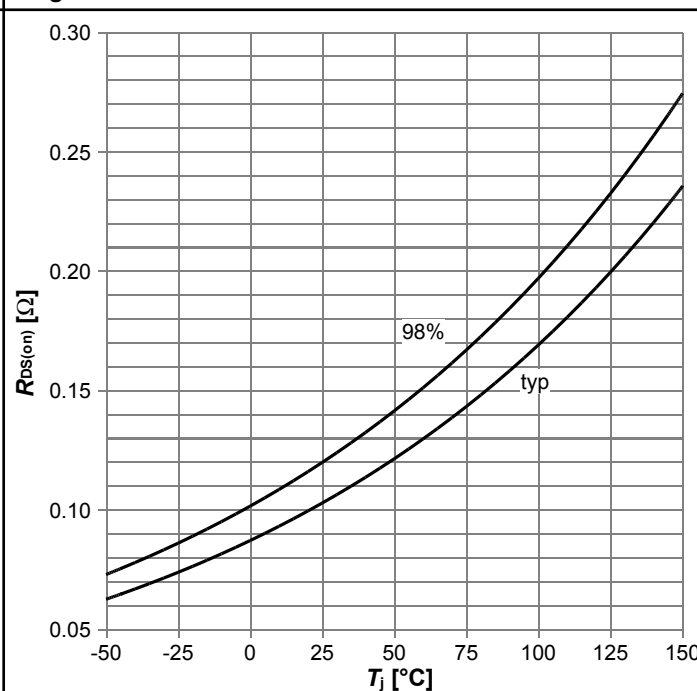

 $R_{DS(on)} = f(T_J); I_D = 7.8\text{ A}; V_{GS} = 10\text{ V}$

Diagram 9: Typ. transfer characteristics

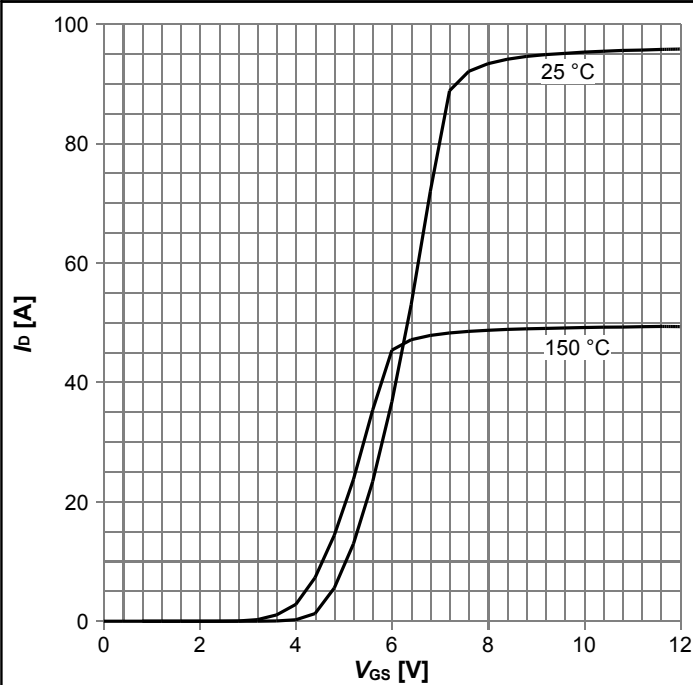

 $I_D = f(V_{GS}); V_{DS} = 20V; \text{parameter: } T_j$ 

Diagram 10: Typ. gate charge

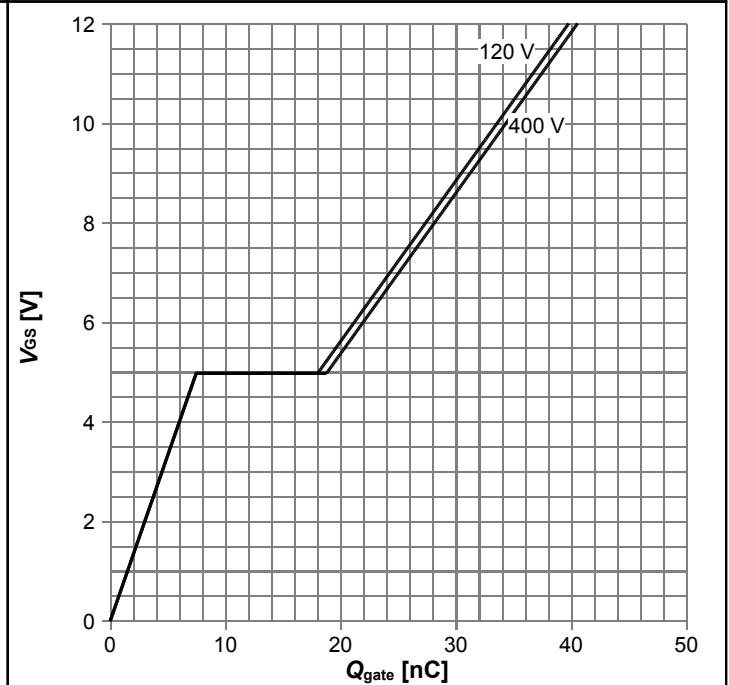

 $V_{GS} = f(Q_{gate}); I_D = 7.8 \text{ A pulsed}; \text{parameter: } V_{DD}$ 

Diagram 11: Forward characteristics of reverse diode

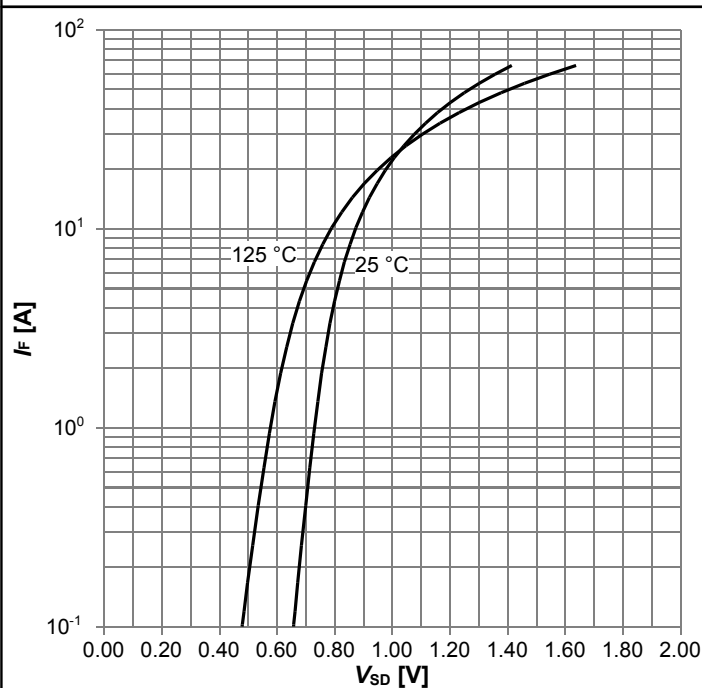

 $I_F = f(V_{SD}); \text{parameter: } T_j$ 

Diagram 12: Avalanche energy

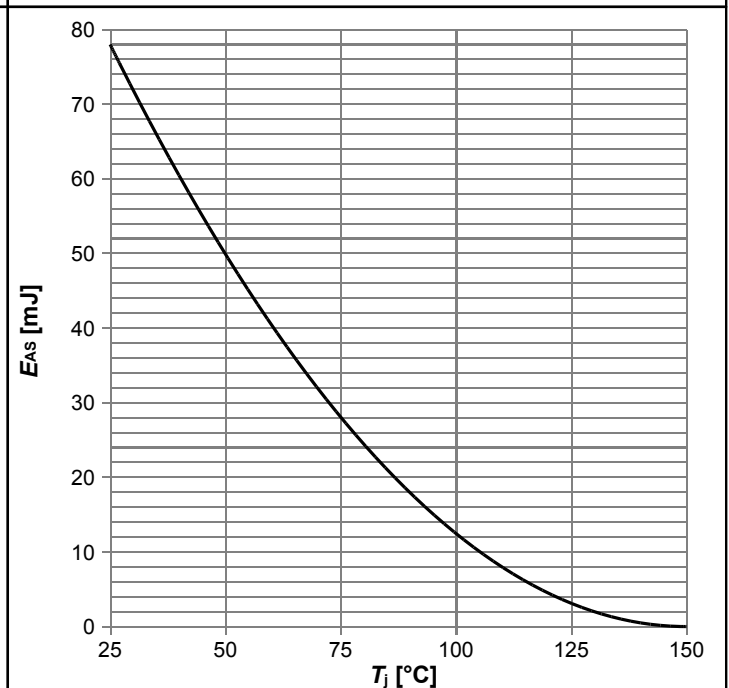
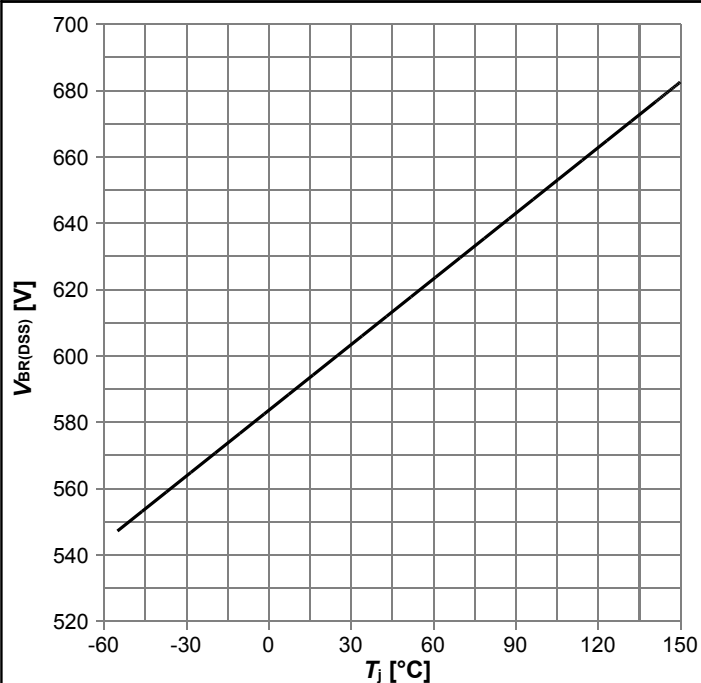
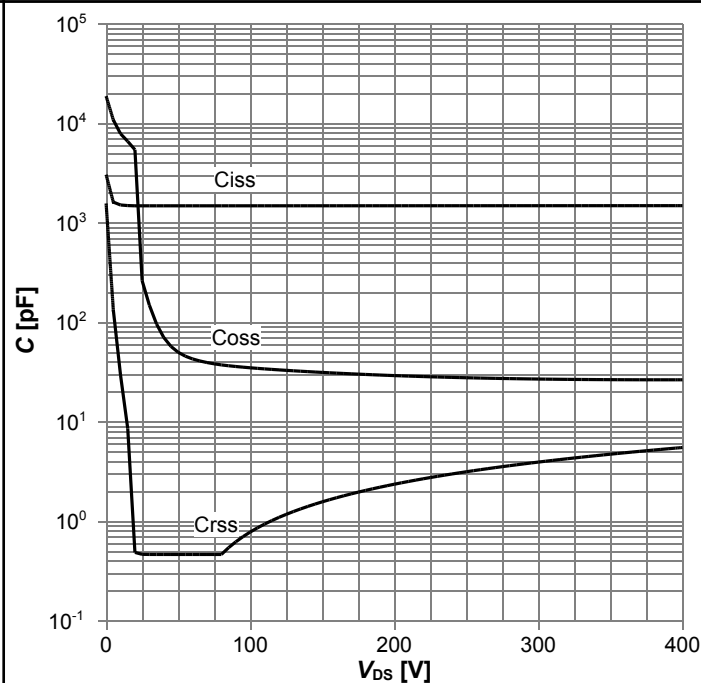

 $E_{AS} = f(T_j); I_D = 4.4 \text{ A}; V_{DD} = 50 \text{ V}$

Diagram 13: Drain-source breakdown voltage



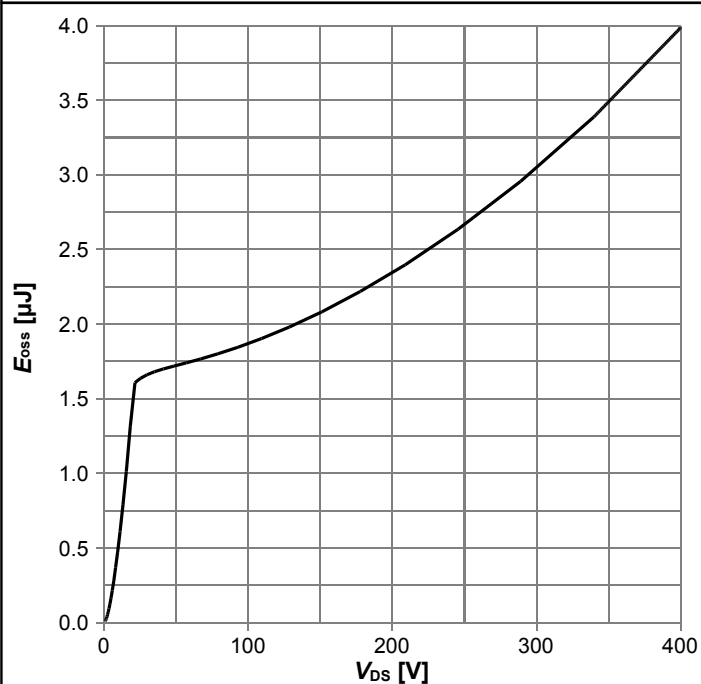
$$V_{BR(DSS)} = f(T_j); I_D = 1 \text{ mA}$$

Diagram 14: Typ. capacitances



$$C = f(V_{DS}); V_{GS} = 0 \text{ V}; f = 250 \text{ kHz}$$

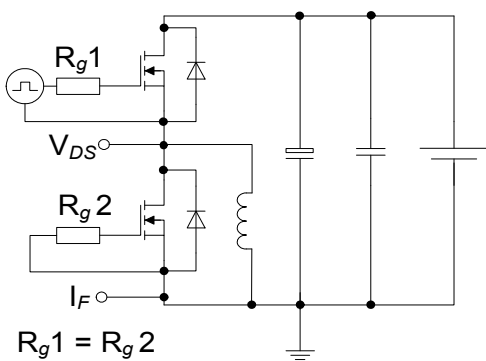
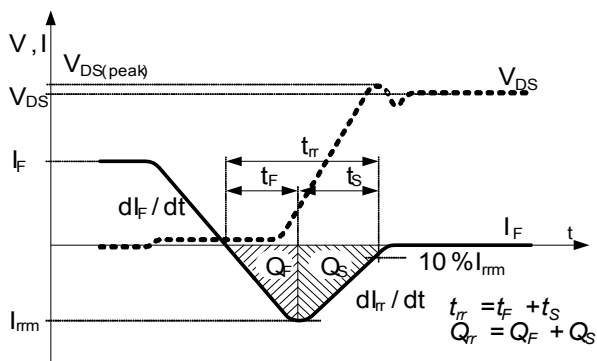
Diagram 15: Typ. Coss stored energy



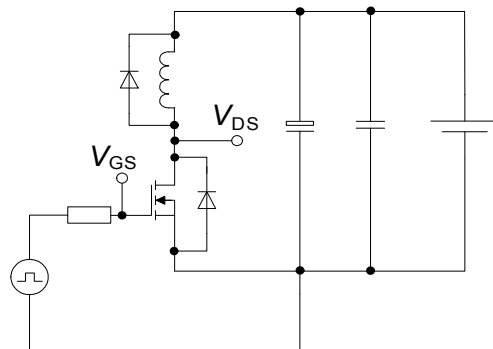
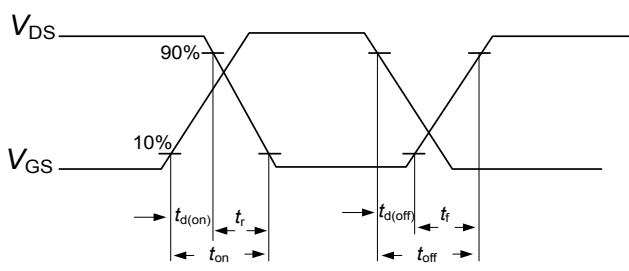
$$E_{oss} = f(V_{DS})$$

## 6 Test Circuits

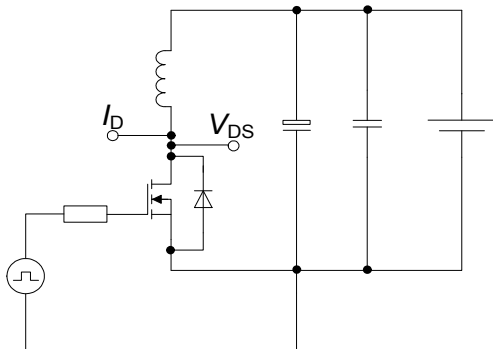
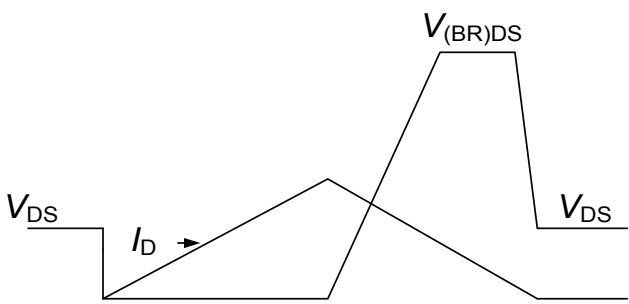
**Table 8 Diode characteristics**

| Test circuit for diode characteristics                                                                                | Diode recovery waveform                                                                                                                                        |
|-----------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------|
|  <p><math>R_{g1} = R_{g2}</math></p> |  <p><math>t_{rr} = t_F + t_S</math><br/> <math>Q_{rr} = Q_F + Q_S</math></p> |

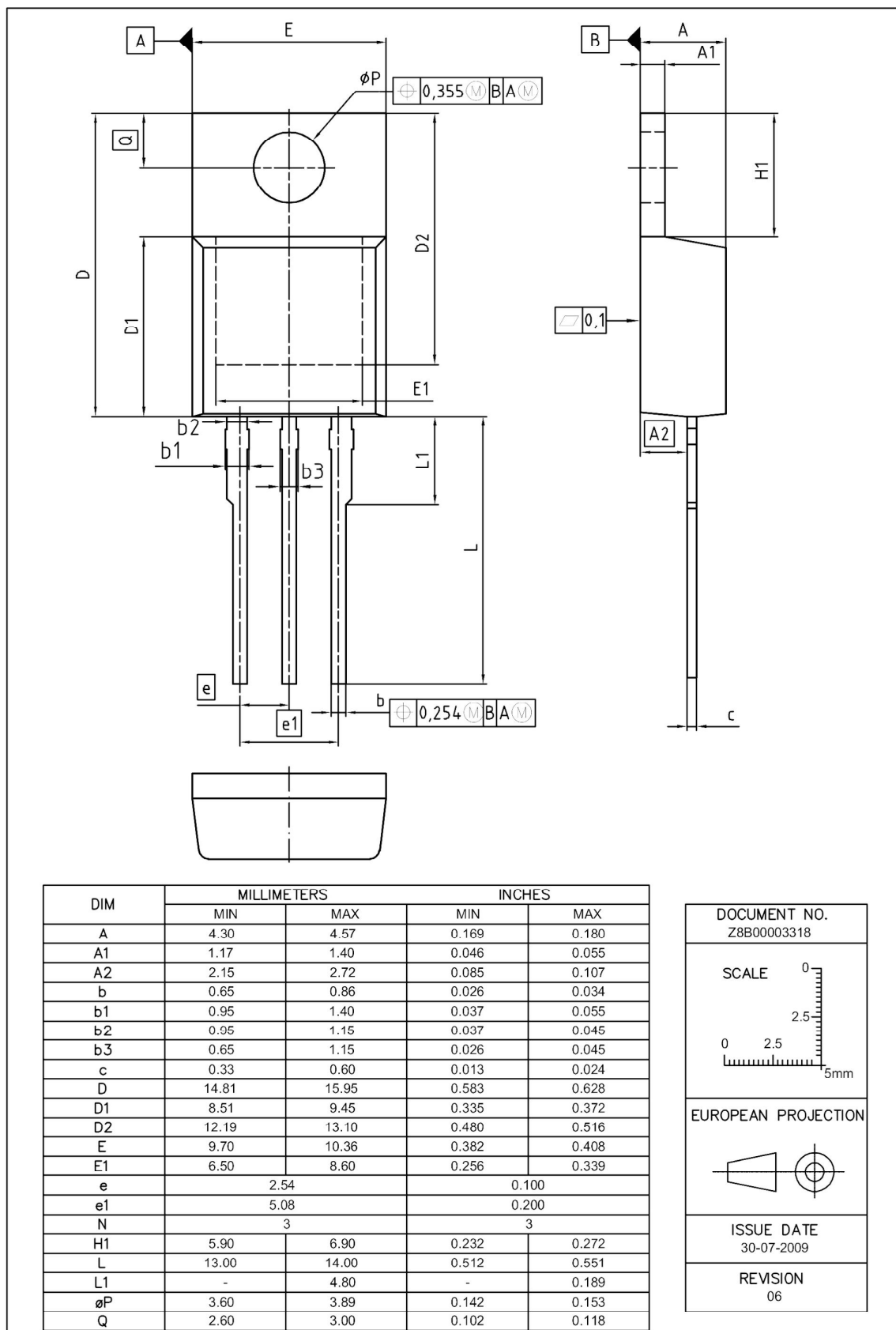
**Table 9 Switching times**

| Switching times test circuit for inductive load                                     | Switching times waveform                                                             |
|-------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------|
|  |  |

**Table 10 Unclamped inductive load**

| Unclamped inductive load test circuit                                               | Unclamped inductive waveform                                                         |
|-------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------|
|  |  |

## 7 Package Outlines



**Figure 1 Outline PG-TO 220, dimensions in mm/inches**

## 8 Appendix A

### Table 11 Related Links

- IFX CoolMOS™ C7 Webpage: [www.infineon.com](http://www.infineon.com)
- IFX CoolMOS™ C7 application note: [www.infineon.com](http://www.infineon.com)
- IFX CoolMOS™ C7 simulation model: [www.infineon.com](http://www.infineon.com)
- IFX Design tools: [www.infineon.com](http://www.infineon.com)

## Revision History

IPP60R120C7

**Revision: 2015-11-30, Rev. 2.0**

Previous Revision

| Revision | Date       | Subjects (major changes since last revision) |
|----------|------------|----------------------------------------------|
| 2.0      | 2015-11-30 | Release of final version                     |

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