00V 15A SiC Schottky MPS™ Diode



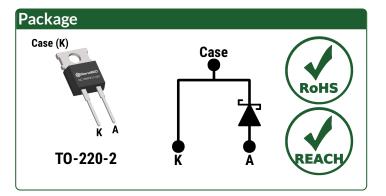
 V_{RRM} = 1200 V $I_{F(T_C = 156^{\circ}C)}$ = 15 A Q_C = 80 nC

Features

- Low V_F for High Temperature Operation
- Enhanced Surge and Avalanche Robustness
- Superior Figure of Merit Qc/IF

Silicon Carbide Schottky Diode

- Low Thermal Resistance
- Low Reverse Leakage Current
- Temperature Independent Fast Switching
- Positive Temperature Coefficient of V_F
- High dV/dt Ruggedness



Advantages

- Improved System Efficiency
- High System Reliability
- Optimal Price Performance
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- Easy to Parallel without Thermal Runaway
- Enables Extremely Fast Switching

Applications

- Power Factor Correction (PFC)
- Electric Vehicles and Battery Chargers
- Solar Inverters
- High Frequency Converters
- Switched Mode Power Supply (SMPS)
- Motor Drives
- Anti-Parallel / Free-Wheeling Diode
- LED and HID Lighting

Absolute Maximum Ratings (At T _C = 25°C Unless Otherwise Stated)								
Parameter	Symbol	Conditions	Values	Unit	Note			
Repetitive Peak Reverse Voltage	V_{RRM}		1200	٧				
	l _F	T _C = 100°C, D = 1	34					
Continuous Forward Current		$T_C = 135^{\circ}C$, D = 1	23	Α	Fig. 4			
		$T_C = 156^{\circ}C$, D = 1	15					
Non-Repetitive Peak Forward Surge Current, Half Sine Wave	I _{F,SM}	T_C = 25°C, t_P = 10 ms	150	٨				
		T_C = 150°C, t_P = 10 ms	120	Α				
Repetitive Peak Forward Surge Current, Half Sine Wave	I _{F,RM}	$T_C = 25^{\circ}C$, $t_P = 10 \text{ ms}$		Α				
		T_C = 150°C, t_P = 10 ms	63	А				
Non-Repetitive Peak Forward Surge Current	I _{F,MAX}	T_C = 25°C, t_P = 10 μ s	750	Α				
i ² t Value	∫i²dt	T_C = 25°C, t_P = 10 ms	112	A ² s				
Non-Repetitive Avalanche Energy	E _{AS}	L = 2.4 mH, I _{AS} = 15 A	270	mJ				
Diode Ruggedness	dV/dt	V _R = 0 ~ 960 V	200	V/ns				
Power Dissipation	Ртот	T _C = 25°C	223	W	Fig. 3			
Operating and Storage Temperature	T_j , T_{stg}		-55 to 175	°C				

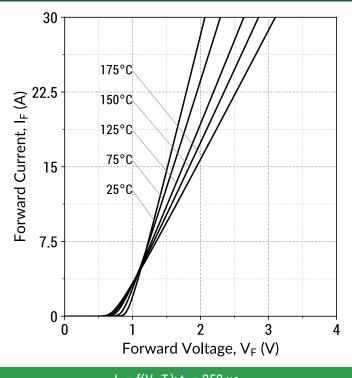


Electrical Characteristics								
Parameter	Symbol	Conditions -		Values			Unit	Note
	Зуший			Min.	Тур.	Max.	Ullit	Note
Diode Forward Voltage	V_F	I _F = 15 A, T _j = 25°C			1.5	1.8	٧	Fig. 1
	VF	I _F = 15 A, T _j = 175°C			1.9			
Reverse Current	l _n	V _R = 1200 V, T _j = 25°C			2	10	μΑ	Fig. 2
	IR	$V_R = 1200 \text{ V, } T_j = 175^{\circ}\text{C}$			17			
Total Capacitive Charge	Qc		$V_R = 400 V$		55		nC	Fig. 7
	QU	l _F ≤ I _{F,MAX} dI _F /dt = 200 A/μs	$V_{R} = 800 V$		80	IIC		Fig. 7
Switching Time	t _o		$V_{R} = 400 \text{ V}$		< 10		ns	
	ts		$V_{R} = 800 V$		\ 10		115	
Total Capacitance	С	V _R = 1 V, f = 1MHz			914		ьE	Fig. 6
		$V_R = 800 \text{ V, } f = 1 \text{MHz}$			53		pF	

Thermal/Package Characteristics								
Parameter	Symbol	Conditions	Values			Heit	Note	
		Conditions	Min.	Тур.	Max.	Unit	Note	
Thermal Resistance, Junction - Case	R_{thJC}			0.67		°C/W	Fig. 9	
Weight	W _T			2.0		g		
Mounting Torque	T _M	Screws to Heatsink			1.0	Nm		







 $I_F = f(V_F, T_j); t_P = 250 \mu s$

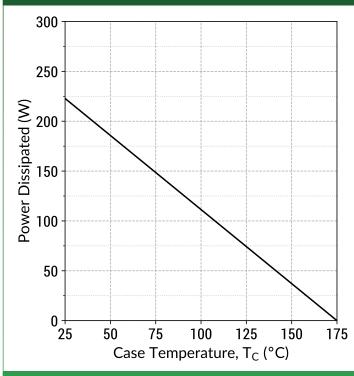
 10^{-4} 175°C 150°C 10^{-5} Reverse Current, I_R (A) 125°C 75°C 10^{-6} 25°C 10^{-7} 10^{-8} 10^{-9} 200 400 800 1000 600 1200

Figure 2: Typical Reverse Characteristics

 $I_R = f(V_R, T_i)$

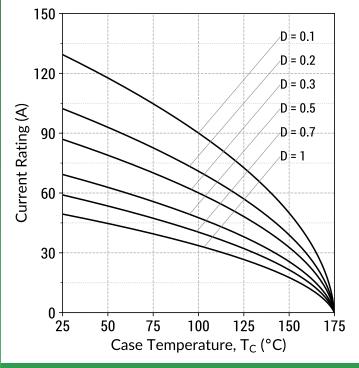
Reverse Voltage, V_R (V)

Figure 3: Power Derating Curves



 $P_{TOT} = f(T_C); T_j = 175^{\circ}C$

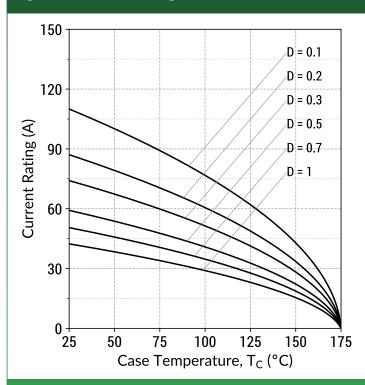
Figure 4: Current Derating Curves (Typical V_F)



 $I_F = f(T_C)$; D = t_P/T ; $T_j \le 175$ °C; $f_{SW} > 10$ kHz

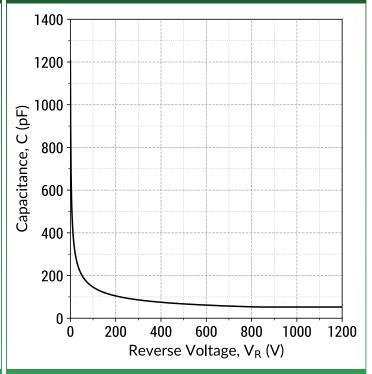


Figure 5: Current Derating Curves (Maximum V_F)



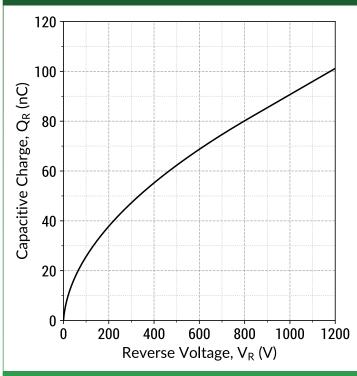
 $I_F = f(T_C); D = t_P/T; T_j \le 175$ °C; $f_{SW} > 10$ kHz

Figure 6: Typical Junction Capacitance vs Reverse Voltage Characteristics



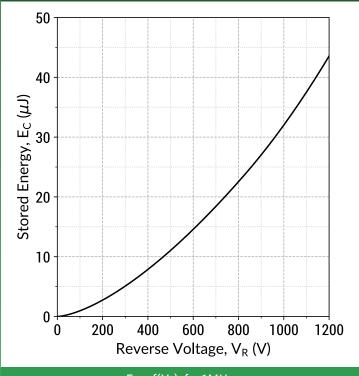
 $C = f(V_R)$; f = 1MHz

Figure 7: Typical Capacitive Charge vs Reverse Voltage Characteristics



 $Q_C = f(V_R)$; f = 1MHz

Figure 8: Typical Capacitive Energy vs Reverse Voltage Characteristics



 $E_C = f(V_R)$; f = 1MHz





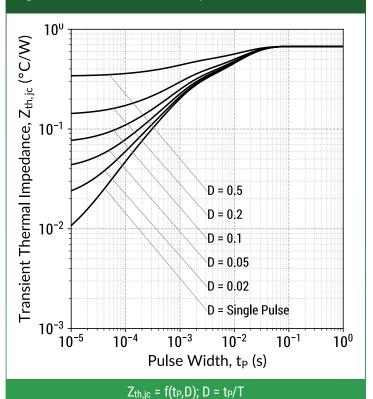
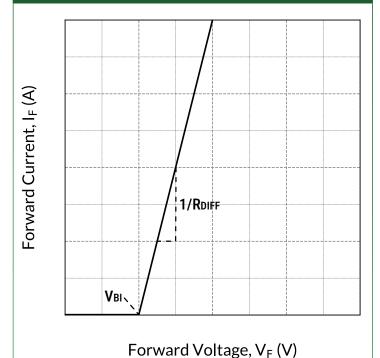


Figure 10: Forward Curve Model



 $I_F = f(V_F, T_j)$

Forward Curve Model Equation:

 $I_F = (V_F - V_{BI})/R_{DIFF} (A)$

Built-In Voltage (V_{BI}):

 $V_{BI}(T_j) = m \times T_j + n (V)$ $m = -0.00123 (V/^{\circ}C)$ n = 0.995 (V)

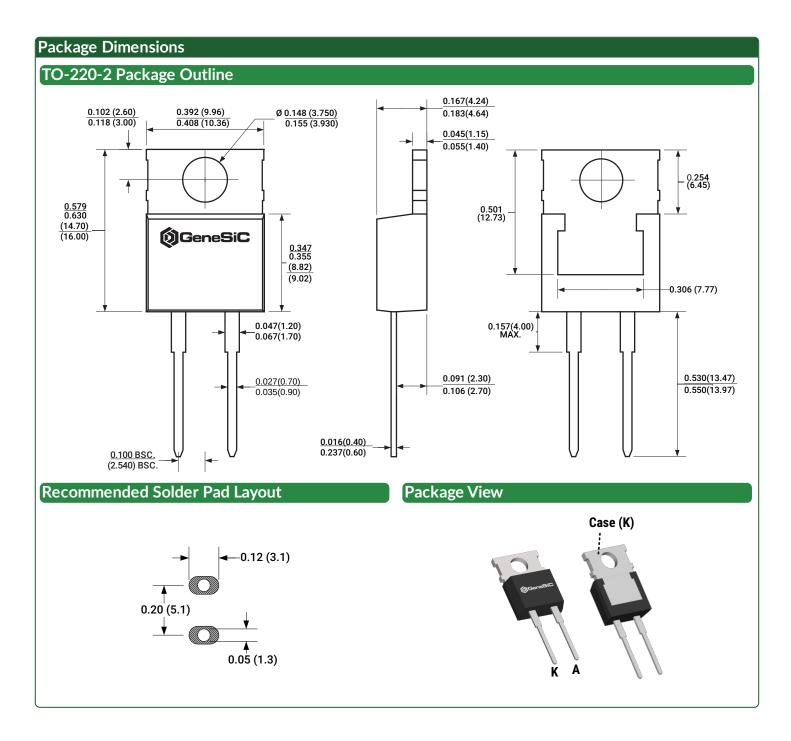
Differential Resistance (RDIFF):

 $R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c (\Omega)$ $a = 7.95e-07 (\Omega/^{\circ}C^2)$ $b = 0.000113 (\Omega/^{\circ}C)$ $c = 0.0334 (\Omega)$

Forward Power Loss Equation:

 $P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$





NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.





Compliance

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

Disclaimer

GeneSiC Semiconductor, Inc. reserves right to make changes to the product specifications and data in this document without notice. GeneSiC disclaims all and any warranty and liability arising out of use or application of any product. No license, express or implied to any intellectual property rights is granted by this document.

Unless otherwise expressly indicated, GeneSiC products are not designed, tested or authorized for use in life-saving, medical, aircraft navigation, communication, air traffic control and weapons systems, nor in applications where their failure may result in death, personal injury and/or property damage.

Related Links

SPICE Models: https://www.genesicsemi.com/sic-schottky-mps/GC15MPS12-220/GC15MPS12-220_SPICE.zip
 PLECS Models: https://www.genesicsemi.com/sic-schottky-mps/GC15MPS12-220/GC15MPS12-220_PLECS.zip
 CAD Models: https://www.genesicsemi.com/sic-schottky-mps/GC15MPS12-220/GC15MPS12-220_3D.zip

• Evaluation Boards: https://www.genesicsemi.com/technical-support

Reliability: https://www.genesicsemi.com/reliability
 Compliance: https://www.genesicsemi.com/compliance
 Quality Manual: https://www.genesicsemi.com/quality

Revision History

Rev 21/Jul: Updated with most recent test data
 Supersedes: Rev 19/Apr, Rev 20/Apr, Rev 20/Apr



www.genesicsemi.com/sic-schottky-mps/



Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

GeneSiC Semiconductor:
GC15MPS12-220