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## May 2003



## FDP6670AL/FDB6670AL

## N-Channel Logic Level PowerTrench<sup>o</sup> MOSFET

### **General Description**

This N-Channel Logic Level MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

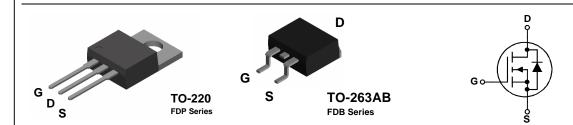
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable  $R_{\text{DS(ON)}}$  specifications.

The result is a MOSFET that is easy and safer to drive (even at very high frequencies), and DC/DC power supply designs with higher overall efficiency.

It has been optimized for low gate charge, low  $R_{\text{DS}(\text{ON})}$  and fast switching speed.

## Features

- 80 A, 30 V  $R_{DS(ON)} = 6.5 \text{ m}\Omega @ V_{GS} = 10 \text{ V}$  $R_{DS(ON)} = 8.5 \text{ m}\Omega @ V_{GS} = 4.5 \text{ V}$
- Critical DC electrical parameters specified at elevated temperature
- High performance trench technology for extremely low  $R_{\text{DS}(\text{ON})}$
- 175°C maximum junction temperature rating



## Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		30	V
V <sub>GSS</sub>	Gate-Source Voltage		± 20	V
ID	Drain Current – Continuous	(Note 1)	80	А
	- Pulsed	(Note 1)	240	
P <sub>D</sub>	Total Power Dissipation @ $T_c = 25^{\circ}C$		68	W
	Derate above 25°C		0.45	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range		-65 to +175	°C

## **Thermal Characteristics**

$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	2.2	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

## Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
FDB6670AL	FDB6670AL	13"	24mm	800 units
FDP6670AL	FDP6670AL	Tube	n/a	45

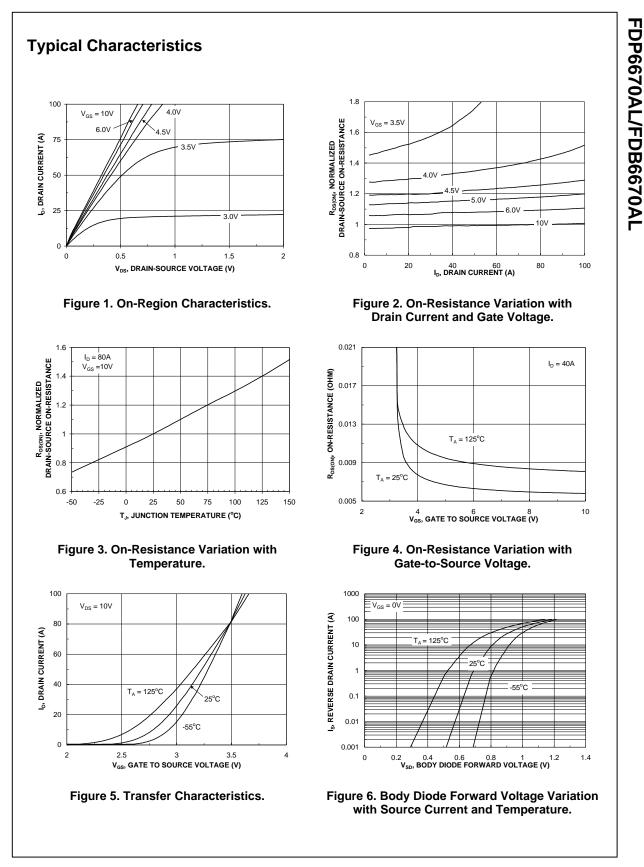
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Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Symbol	Falameter	Test conditions		тур	WIAX	Units
Drain-Sc	purce Avalanche Ratings (Note					
W <sub>DSS</sub>	Single Pulse Drain-Source Avalanche Energy	$V_{DD} = 15 \text{ V}, \qquad I_D = 80 \text{ A}$			114	mJ
AR	Maximum Drain-Source Avalanche Current				80	A
Off Char	acteristics					
BV <sub>DSS</sub>	Drain–Source Breakdown Voltage	$V_{GS}=0~V, \qquad I_D=250~\mu A$	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, Referenced to $25^{\circ}$ C		24		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 24 \text{ V}, \qquad V_{\text{GS}} = 0 \text{ V}$			1	μA
I <sub>GSS</sub>	Gate–Body Leakage	$V_{GS}=\pm~20~V,~~V_{DS}=0~V$			± 100	nA
On Char	acteristics (Note 2)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	1	1.9	3	V
$\Delta V_{GS(th)}$ $\Delta T_J$	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu\text{A}$ , Referenced to 25°C		-5		mV/°C
R <sub>DS(on)</sub>	Static Drain–Source On– Resistance			5.2 6.5 7.2	6.5 8.5 9.7	mΩ
I <sub>D(on)</sub>	On–State Drain Current	$V_{GS} = 10 \text{ V},  V_{DS} = 10 \text{ V}$	80		0.1	А
g <sub>FS</sub>	Forward Transconductance	$V_{DS} = 10V, I_D = 40 A$		115		S
-						-
	Characteristics			2440		pF
	Output Capacitance	$V_{DS} = 15 V$ , $V_{GS} = 0 V$ , f = 1.0 MHz		580		pr pF
C <sub>rss</sub>	Reverse Transfer Capacitance			250		pF
R <sub>G</sub>	Gate Resistance	$V_{GS} = 15 \text{ mV},  f = 1.0 \text{ MHz}$		1.4		Ω
Switchin	g Characteristics (Note 2)			I		1
t <sub>d(on)</sub>	Turn–On Delay Time	$V_{DD} = 10V,$ $I_{D} = 1 A,$		13	23	ns
t <sub>r</sub>	Turn–On Rise Time	$V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 6 \Omega$		13	23	ns
t <sub>d(off)</sub>	Turn–Off Delay Time			42	68	ns
t <sub>f</sub>	Turn–Off Fall Time			15	27	ns
Q <sub>g</sub>	Total Gate Charge	$V_{DS} = 15 V, I_D = 40 A,$		24	33	nC
Q <sub>gs</sub>	Gate-Source Charge	$V_{GS} = 5 V$		7		nC
Q <sub>gd</sub>	Gate-Drain Charge			9		nC
Drain-Se	ource Diode Characteristics	and Maximum Ratings				
Is	Maximum Continuous Drain-Source				80	Α
V <sub>SD</sub>	Drain–Source Diode Forward Voltage	$V_{GS} = 0 \ V,  I_S = 40 \ A \qquad (\text{Note 1})$		0.9	1.3	V
t <sub>rr</sub>	Diode Reverse Recovery Time	I <sub>F</sub> = 40 A,		34		nS
Q <sub>rr</sub>	Diode Reverse Recovery Charge	d <sub>iF</sub> /d <sub>t</sub> = 100 A/μs		24		nC

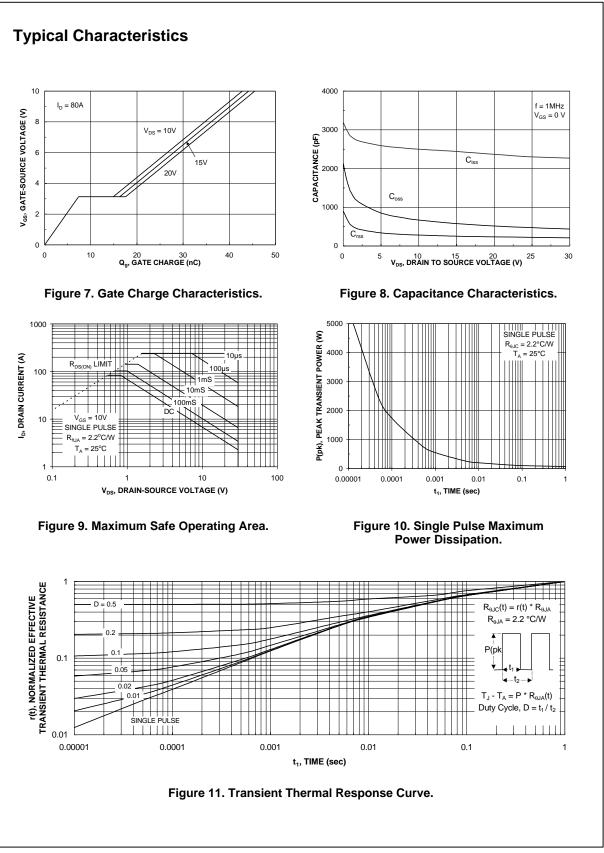
Notes:

1. Pulse Test: Pulse Width < 300 $\mu$ s, Duty Cycle < 2.0%

FDP6670AL/FDB6670AL



FDP6670AL/FDB6670AL Rev D(W)



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