AsahiKASEI MICRODEVICES

Clearance/Creepage 8mm CZ-375D
100A$_{rms}$ Accurate Coreless Current Sensor

1. General Description

CZ-375D is an open-type current sensor using Hall sensors, which outputs the analog voltage proportional to the AC/DC current. Group III-V semiconductor thin film is used as the Hall sensor, which enables the high-accuracy and high-speed current sensing. Coreless ultra-small surface mount package realizes the space-saving. Also, the low primary conductor resistance suppresses heat generation to achieve the 100A$_{rms}$ continuous current. Existing coreless current sensors have an accuracy disadvantage from degradations caused by a disturbed magnetic field. The CZ-375x series has a built-in stray magnetic field reduction function to suppress this effect. The CZ-375x series is also UL 61800-5-1 safety compliant, which is an excellent fit for industrial Charging pile, Commercial Air conditioners, etc. While using the same package as the CZ-370x series, the CZ-375x series can detect a wide range of current, from ±115Apeak (CZ-375A), to ±225Apeak (CZ-375D). This enables the designer to use the same board design across different products and helps the user to expand the options to different current ratings.

2. Features

- □ Compliant with safety standard of UL61800-5-1 (Clearance, Creepage distance ≥ 8.0mm)
- □ Certified with safety standards of UL-1577 and IEC/ UL62368-1
- □ Maximum Primary Current : 100A$_{rms}$
- □ High-accuracy : 0.5%F.S.(T_a=0 ~ 90°C Typ.)
- □ Quite small primary conductor resistance : 0.27mΩ Typ.
- □ Fast response time : 0.5μs Typ.
- □ Stray magnetic field reduction function
- □ Small-sized surface mount package（12.7mm×10.9mm×2.25mm）
- □ Differential output with VREF pin
- □ Isolation Voltage：4.2kV (AC50Hz,60s)
- □ Ratiometric output

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3. Applications

- □ Charging pile
- □ Commercial Air conditioners
- □ General Inverters
- □ UPS

Also, CZ-375D is suitable for other applications which are required isolation with small size and suppressing heat generation.

4. Table of Contents

5. Block Diagram and Functions

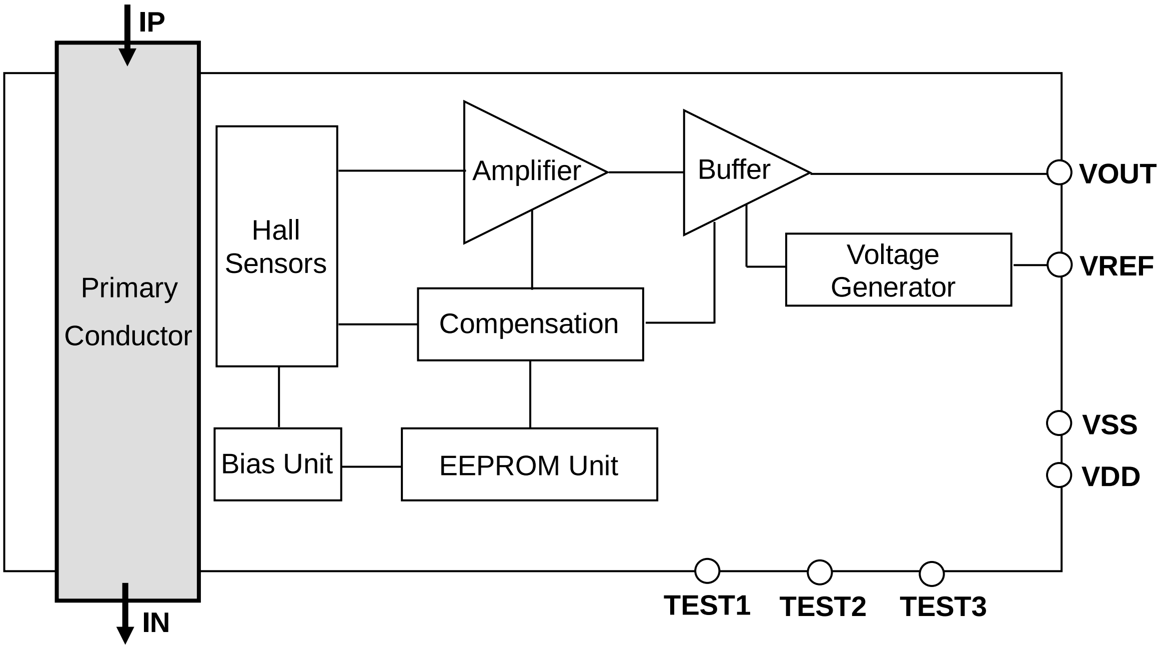

Figure 1. Block diagram of CZ-375D

Table 1. Explanation of circuit blocks

Circuit Block	Function
Primary Conductor	A device has the primary conductor built-in.
Hall Sensors	Hall elements which detect magnetic flux density generated from the measured current.
Amplifier	Amplifier of Hall elements' output.
Buffer	Output buffer with gain. This block outputs the voltage (V_{OUT}) proportional to the current applied to the primary conductor.
Compensation	Compensation circuit which adjusts the temperature drifts of sensitivity and zero-current voltage.
Bias Unit	Drive circuit for Hall elements.
EEPROM Unit	Non-volatile memory for setting adjustment parameters.
Voltage Generator	Reference voltage generating circuit of V_{OUT}.

6. Pin Configurations and Functions

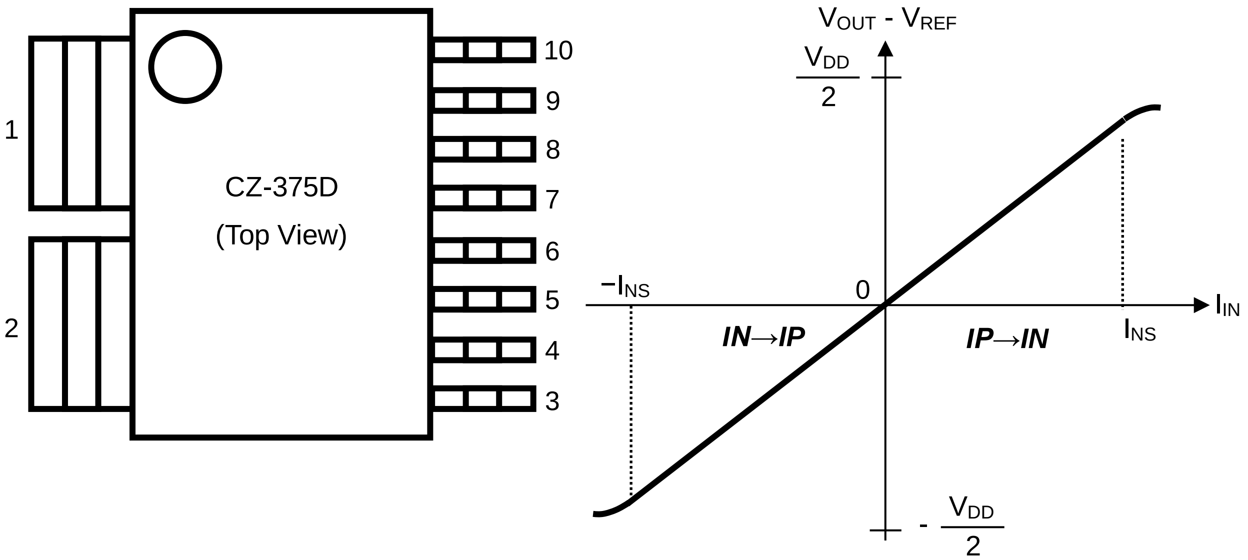

Figure 2. Pin configurations and typical output characteristics of CZ-375D

Table 2. Pin configuration and functions of CZ-375D

Pin No.	Pin Name	I/O	Type	Function
1	IP	I	—	Primary conductor pin (+)
2	IN	I	—	Primary conductor pin (-)
3	VSS	GND	Power	Ground pin (GND)
4	TEST1	—	—	Test pin (Recommended external connection : GND)
5	VREF	O	Analog	Reference output pin
6	VOUT	O	Analog	Sensor output pin
7	VDD	PWR	Power	Power supply pin (5V)
8	TEST2	—	—	Test pin (Recommended external connection : OPEN)
9	TEST3	—	—	Test pin (Recommended external connection : OPEN)
10	VSS	GND	Power	Ground pin (GND)

7. Absolute Maximum Ratings

Table 3. Absolute maximum ratings

Parameter	Symbol	Min.	Max.	Units	Notes
Supply Voltage	V_{DD}	-0.3	6.5	V	VDD pin
Analog Output Current	I_{OUT}	-10	10	mA	VOUT pin, VREF pin
Junction Temperature	T_j	-40	150	°C	
Storage Temperature	T_{STG}	-40	150	°C	

WARNING:
Operation at or beyond these limits may result in permanent damage to the device. Normal operation is not guaranteed at these extremes.

8. Recommended Operating Conditions

Table 4. Recommended operating conditions

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
Supply Voltage	V_{DD}	4.5	5.0	5.5	V	VDD pin
Sensor Output Load Capacitance 1	C_{LVOUT}			1000	pF	Between VOUT pin and VSS pin
Reference Output Load Capacitance 2	C_{LVREF}			1000	pF	Between VREF pin and VSS pin
Sensor Output Load Resistance 1	R_{LVOUT}	3			kΩ	Between VOUT pin and VSS pin Between VOUT pin and VDD pin
Reference Output Load Resistance 2	R_{LVREF}	3			kΩ	Between VREF pin and VSS pin Between VREF pin and VDD pin
Operating Ambient Temperature	T_a	-40		105	°C	
Case Temperature Note 1)	T_c	-40		130	°C	Compliant with safety standard of UL61800-5-1
Maximum Primary Current (RMS)	I_{RMSmax}			100	A_{rms}	Continuous DC value or RMS value which can be applied to primary conductor

WARNING:

Electrical characteristics are not guaranteed when operated at or beyond these conditions.

Note1. Continuous 100A_{rms} current can be flowed through this IC, and even a larger current can be flowed transiently. Using as your system complied with safety standard of UL61800-5-1, the case temperature of this IC should be less than 130°C.

9. Electrical Characteristics

Table 5. Electrical Characteristics

Conditions(unless otherwise specified) : T_a=25°C, V_{DD}=5V

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Current Consumption	I_{DD}	I_{IN}=0A, No loads		16.6	21.6	mA
Sensitivity Note 2)	V_h	I_{IN}=±30A ≤5A 1ms, >5A 500µs V_h=(V_{OUT}-V_{REF})/1A	9.9	10.0	10.1	mV/A
Zero-Current Output Note 2)	V_{of}	I_{IN}=0A, V_{OUT}-V_{REF}	-0.02		0.02	V
Reference Output Voltage Note 6)	V_{REF}		0.5×V_{DD} -0.02	0.5×V_{DD}	0.5×V_{DD} +0.02	V
Linear Sensing Range Note 3)	I_{NS}		-225		225	A
Output Saturation Voltage H Note 4)	V_{satH}	R_{LVOUT}=3kΩ	V_{DD}-0.15			V
Output Saturation Voltage L Note4)	V_{satL}	R_{LVOUT}=3kΩ			0.15	V
Linearity Error Note 5) Note 6)	ρ	T_a=-40 ~ 105°C F.S.=V_{satH}-V_{satL}		±0.15	±0.23	%F.S.
Rise Response Time Note 4)	t_r	I_{IN} 90% to V_{OUT} 90%, C_{LVOUT}= C_{LVREF}=1000pF		0.5		µs
Fall Response Time Note 4)	t_f	I_{IN} 10% to V_{OUT} 10%, C_{LVOUT}= C_{LVREF}=1000pF		0.5		µs
Input Current Equivalent Noise	I_{Nrms}	I_{IN}=0A ,DC ~ 400kHz		100		mA_{rms}
Ratiometric Error of Sensitivity	V_{h-R}	V_{DD}=4.5V ~ 5.5V	-1.0		1.0	%
Ratiometric Error of Zero-Current Output	V_{of-R}	V_{DD}=4.5V ~ 5.5V I_{IN}=0A	-0.3		0.3	%F.S.
Stray Magnetic Field Reduction	E_{bc}	Equivalent to Zero-Current output drift -10mT < Stray Magnetic Field <10mT		0.01		A/mT
dV/dt Settling Time Note 4) Note 7)	t_{dVdt}	200V/µs 200V		2		µs
Primary Conductor Resistance	R_P			0.27		mΩ
Isolation Voltage Note 8)	V_{INS}	AC50Hz, 60s	4.2			kV_{rms}
Isolation Resistance Note 3)	R_{INS}	DC1kV	500			MΩ
Retention Time of EEPROM data	EEP_{RT}	T_j =105°C	10			Year
		T_j =100°C	15			

Table 6. Temperature drift characteristics

Conditions(unless otherwise specified) : V_{DD}=5V

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Temperature Drift of Sensitivity Note 5) Note 6) Note 9)	V_{h-d}	T_a=0 ~ 90°C	-0.7	0.4	0.9	%
		T_a=0 ~ 105°C	-0.7	0.8	1.4	
		T_a=-40 ~ 105°C	-1.9	1.0	1.4	
Temperature Drift of Zero-current Output Note 5) Note 6) Note 9)	V_{of-d}	T_a=0 ~ 90°C I_{IN}=0A		±1.0	±2.7	mV
		T_a=0 ~ 105°C I_{IN}=0A		±1.3	±3.5	
		T_a=-40 ~ 105°C I_{IN}=0A		±1.3	±3.5	
Temperature Drift of Reference Output Note 6) Note 9)	V_{REF-d}	T_a=-40 ~ 105°C		±1		mV
Total Accuracy Note 5) Note 6)	E_{total}	T_a=0 ~ 90°C F.S.=V_{satH}-V_{satL}	-0.9	±0.5	0.7	%F.S.
		T_a=0 ~ 105°C F.S.=V_{satH}-V_{satL}	-0.9	±0.5	0.9	
		T_a=-40 ~ 105°C F.S.=V_{satH}-V_{satL}	-1.2	±0.7	0.9	

Note 2) These values can be drifted by long-term use or reflow process. Please '13.Reliability Tests' for the reference of drift values.

Note 3) These parameters are guaranteed by design.

Note 4) These parameters are tested in wafer condition.

Note 5) The Typical value is defined as the "average value ± 1σ"of the actual measurement result in a certain lot. The minimum value and the maximum value are defined as "average value ± 3σ"of the same condition.

Note 6) These values can be drifted by long-term use or reflow process.

Note 7) The threshold level of the dV/dt settling time is the convergence value ±1mV.

Note 8) This parameter is tested for 1second at 5.1kV_{rms} in mass-production line for all devices.

Note 9) These parameters are defined as the drift from the values at T_a=25°C.

10. Characteristic Definitions

10.1. Sensitivity(V_h), Zero-Current Output (V_{of}), and Linearity Error (ρ) are defined as below:

Sensitivity(V_h) is defined as the slope of the approximate straight line calculated by the least square method, using the data of output voltage (V_{OUT} -V_{REF}) when the primary current (I_{IN}) is swept within the range of linear sensing range (I_{NS}).
The output voltage (V_{OUT} -V_{REF}) when the primary current (I_{IN}) is 0A is the Zero-Current Output (V_{of}).

Linearity Error (ρ) is defined as the ratio of the maximum error voltage (V_d) to the full scale (F.S.), where V_d is the maximum difference between the output voltage (V_{OUT} -V_{REF}) and the approximate straight line.
Definition formula is shown as below:

$$\rho = V_d / F.S. \times 100$$

Full scale (F.S.) is defined by $V_{satHmin}$ - $V_{satLmax}$.

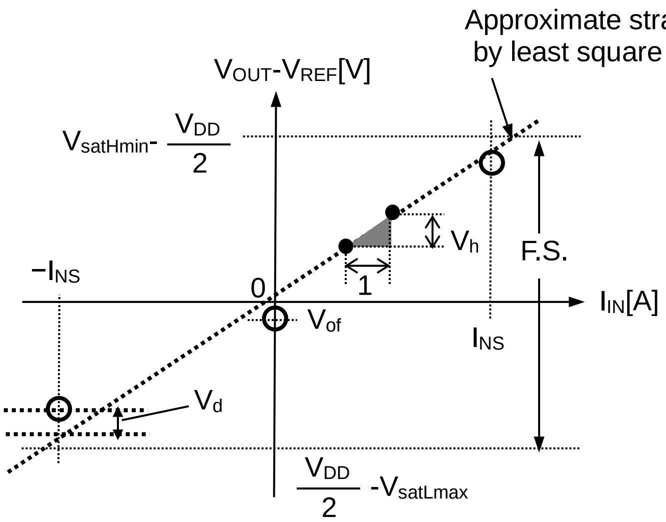

Figure 3. Characteristic definitions of CZ-375D

10.2. Ratiometric Error of Sensitivity is defined as below:

$$V_{h-R} = 100 \times \frac{\left\{\frac{V_h(V_{DD})}{V_h(5V)} - \frac{V_{DD}}{5}\right\}}{\frac{V_{DD}}{5}}$$

10.3. Ratiometric Error of Zero-Current Output is defined as below:

$$V_{of-R} = 100 \times \frac{\left(V_{of}(V_{DD}) - \frac{V_{of}(5V) \times V_{DD}}{5}\right)}{F.S.}$$

10.4. Total Accuracy E_{total} [%F.S.] is defined as below:

$$E_{total} = 100 \times \frac{V_{err}}{F.S.}$$

$$V_{err} = (V_{h-meas} - V_h) \times I_{NS} + V_{of-d} + \rho_{meas} \times F.S.$$

V_{h-meas} : Measured Sensitivity value [mV/A]

V_h : Sensitivity (Typ.) [mV/A]

V_{of-d} : Measured Temperature Drift of Zero-Current Output [mV]

ρ_{meas} : Measured Linearity Error [%F.S.]

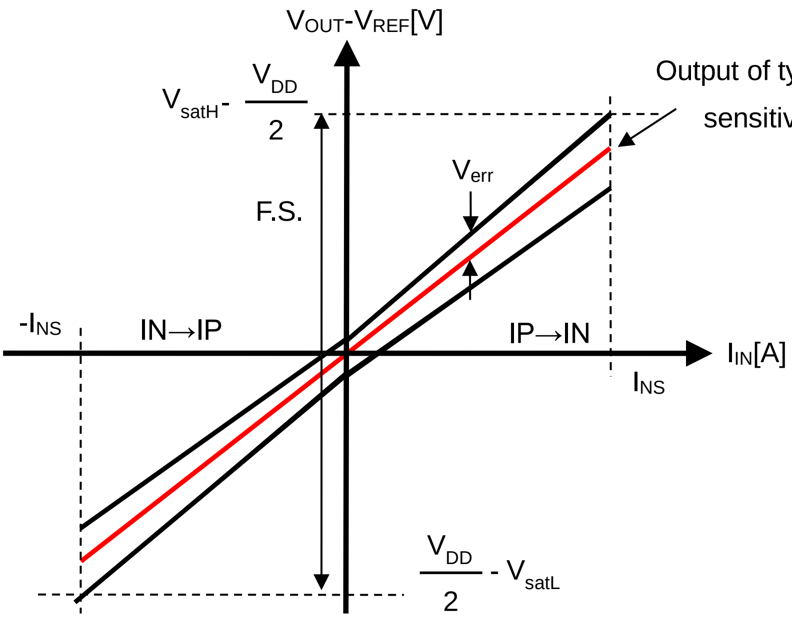

Figure 4. Total Accuracy of CZ-375D

10.5. Rise Response Time t_r [μs] and Fall Response Time t_f [μs]

Rise response time (or fall response time) is defined as the time delay from the 90% (or 10%) of input primary current (I_{IN}) to the 90% (or 10%) of the output voltage (V_{OUT}-V_{REF}) under the pulse input of primary current (Figure 5).

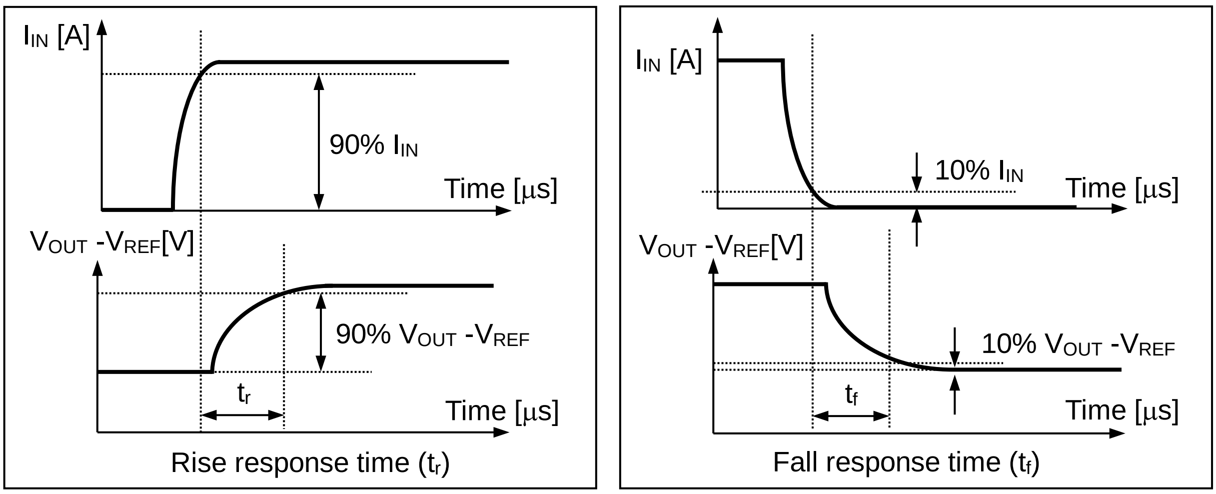

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Figure 5. Definition of response time

11. External Circuits Example

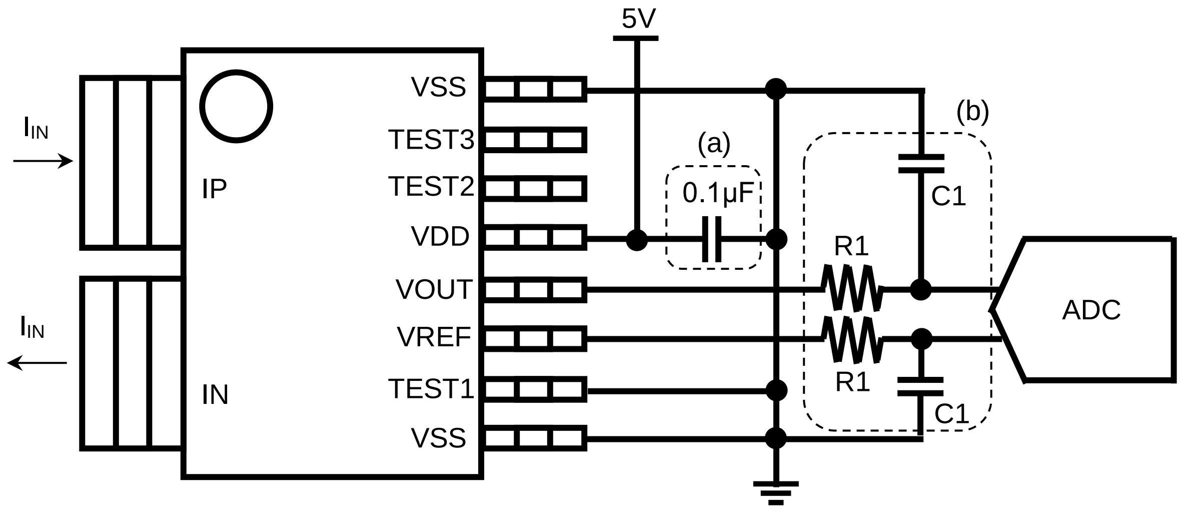


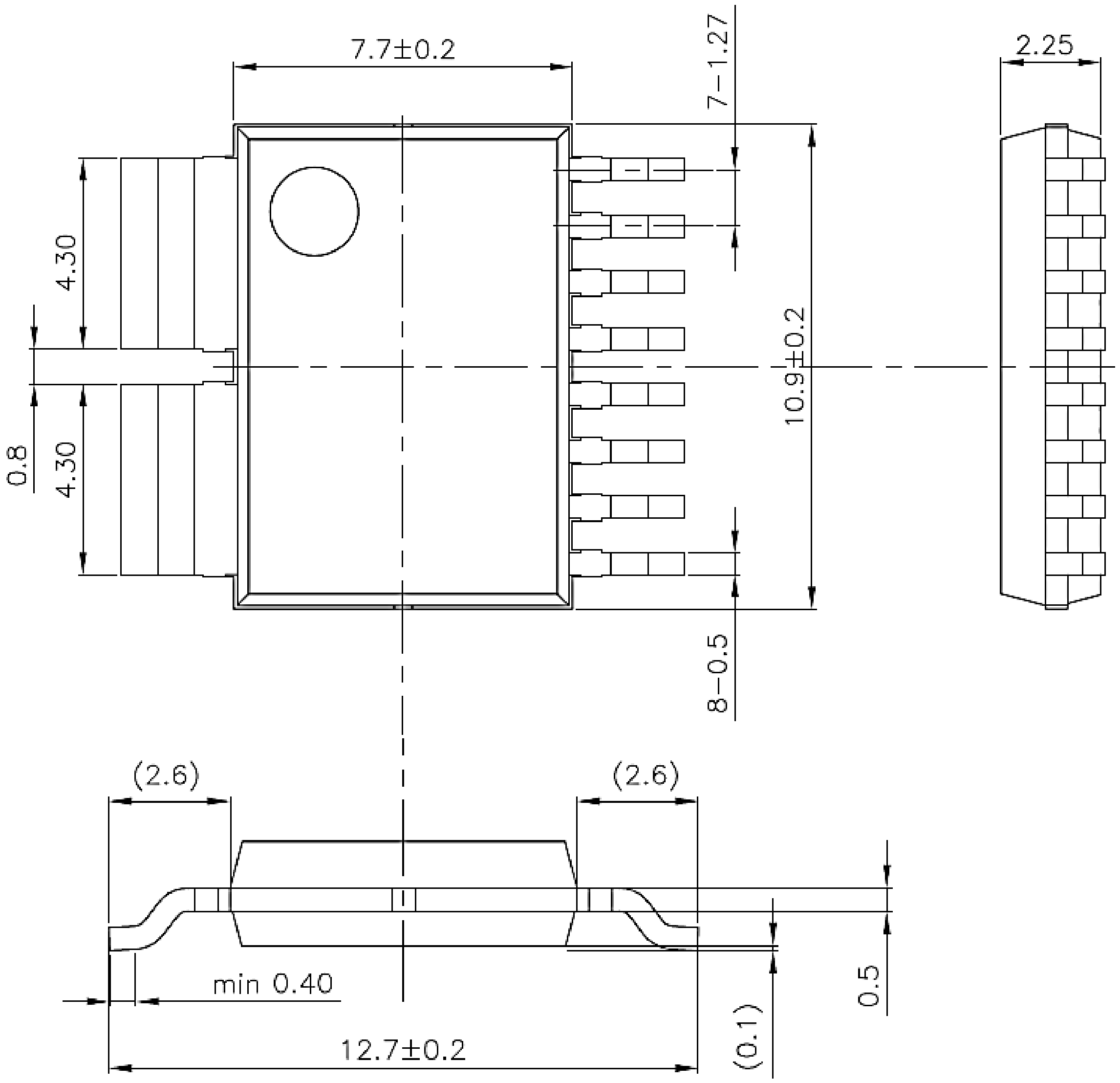
Figure 6. External circuits example

(a) 0.1μF bypass capacitor should be placed close to CZ-375D

(b) Add a low-pass filter if it is necessary. The C1 values should be fixed in consideration of load conditions.

12. Package

12.1. Outline Dimensions



Unit : mm

The tolerances of dimensions without any mention are ±0.1mm.

( ) is a reference values.

Figure 7. Outline dimensions of CZ-375D

Terminals : Cu

Plating for Terminals : Sn-Bi

Package material : RoHS compliant, halogen-free

Table 7. Isolation characteristics of CZ-375x

Parameter	Symbol	Min.	Typ.	Max.	Units
Creepage distance	Cr	8.0			mm
Clearance distance	Cl	8.0			mm

*Flammability standard is V0. (According to UL94)

*Comparative tracking index (CTI) is 400V. Material Group is Ⅱ.

12.2. Standards

・IEC／UL 62368-1, 2nd Ed, 2014-12-01 (Audio/video, information and communication technology equipment Part 1: Safety requirements)

・CAN/CSA C22.2 No. 62368-1-14, 2nd Ed-(Audio/video, information and communication technology equipment Part 1: Safety requirements)

・UL1577ーOptical IsolatorsーEdition 5.(File No. E499004)

・CSA Component Acceptance Service No. 5A－Component Acceptance Service for Optocouplers and Related Devices (File No. E499004)

12.3. Recommended Pad Dimensions

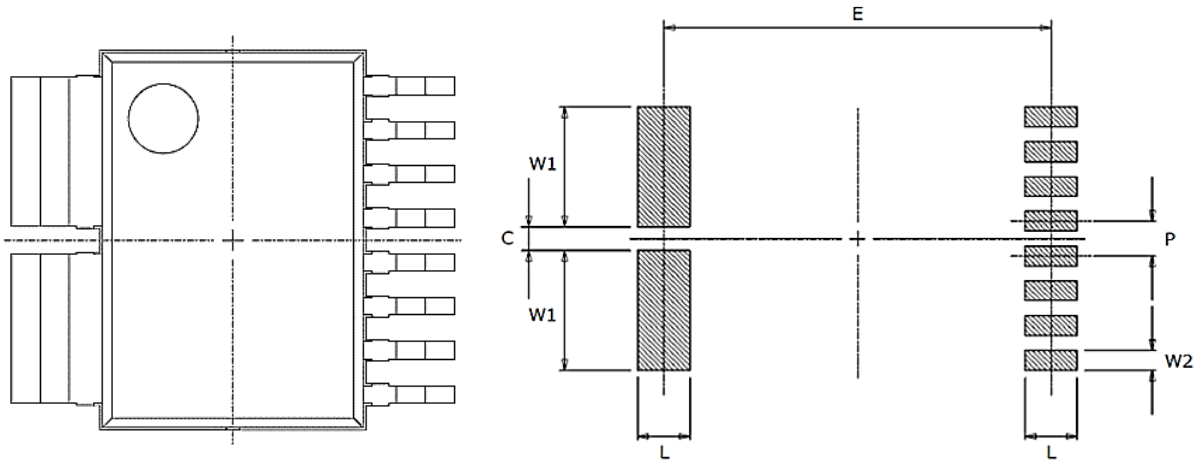


Figure 8. Recommended pad pattern

Table 8. Recommended pad dimensions

L	1.59
E	11.79
W1	4.44
W2	0.64
C	0.66
P	1.27

Unit:mm

If two or more trace layers are used as the current paths, please make enough number of through-holes to flow current between the trace layers. In order to make heat dissipation better, it is recommended that Pad on Via should be provided on the pad of the primary conductor.

12.4 Marking

Production information is printed on the package surface by laser marking. Markings consist of 11 characters excluding AKM logo.

A K M
C Z 3 7 5 D ← Production Code (CZ-375D)
Y M D L_2 L_1 ← Production Date (Y/M/D/L_2/L_1)

Figure 9. Markings of CZ-375D

Table 9. Production date code table

Year(Y)		Month(M)		Day(D)		Lot number		
Character	Year	Character	Month	Character	Day	Character (L_2)	Character (L_1)	Lot number
D	2023	C	January	1	1	0	1	01
E	2024	D	February	2	2	0	2	02
F	2025	E	March	3	3	0	3	03
G	2026	F	April	4	4	0	4	04
H	2027	G	May	5	5	0	5	05
J	2028	H	June	6	6	⋮	⋮	⋮
K	2029	J	July	7	7	6	7	67
L	2030	K	August	8	8	6	8	68
N	2031	L	September	9	9	6	9	69
P	2032	M	October	0	10	7	0	70
R	2033	N	November	A	11	7	1	71
S	2034	P	December	B	12	⋮	⋮	⋮
T	2035			C	13			
U	2036			D	14			
V	2037			E	15			
W	2038			F	16			
X	2039			G	17			
0	2040			H	18			
1	2041			J	19			
2	2042			K	20			
3	2043			L	21			
4	2044			N	22			
5	2045			P	23			
6	2046			R	24			
7	2047			S	25			
8	2048			T	26			
9	2049			U	27			
A	2050			V	28			
B	2051			W	29			
C	2052			X	30			
				Y	31			

13. Reliability Tests

Table 10. Test parameters and conditions of reliability tests

No.	Test Parameter	Test Conditions	n	Test Time
1	Temperature Humidity Bias Test	【JEITA EIAJ ED-4701 102】 T_a=85°C, 85%RH, continuous operation	22	500h
2	High Temperature Bias Test	【JEITA EIAJ ED-4701 101】 T_a=150°C, continuous operation	22	500h
3	High Temperature Storage Test	【JEITA EIAJ ED-4701 201】 T_a=150°C	22	500h
4	Low Temperature Operating Test	T_a=−40°C, continuous operation	22	500h
5	Heat Cycle Test	【JEITA EIAJ ED-4701 105】 −65°C⇔ +150°C 30min.⇔ 30min. Tested in vapor phase	22	500 Cycles

Tested samples are pretreated as below before each reliability test:
Desiccation: 125°C/24h → Moisture Absorption: 60°C/60%RH/168h
→ Reflow: 3 times (JEDEC Level2a)

Criteria：

Products whose drifts between before pretreated and after the reliability tests do not exceed the values below are considered to be in spec.

Sensitivity V_h (T_a=25°C)	: Within ±1.5%
Zero-Current Output V_{of} (T_a=25°C)	: Within ±25mV
Linearity Error ρ (T_a=25°C)	: Within ±0.5%F.S.
EEPROM data	: Unchanged

14. Precautions

<Storage Environment>

Products should be stored at an appropriate temperature, and at as low humidity as possible by using desiccator (5 to 35°C). It is recommended to use the products within 4 weeks since packing was opened. Keep products away from chlorine and corrosive gas. When stored in an inappropriate environment, it can affect the product properties.

<Long-term Storage>

Long-term storage may result in poor lead solderability and degraded electrical performance even under proper conditions. For those parts, which stored long-term should be checked as for solderability before it is used.
For storage longer than 1 year, it is recommended to store in nitrogen atmosphere. Oxygen of atmosphere oxidizes leads of products, and lead solderability get worse.

<Other Precautions>

1) This product should not be used under the environment with corrosive gas including chlorine or sulfur.
2) This product is lead (Pb) free. All leads are plated with Sn-Bi. Do not store this product alone in high temperature and high humidity environment. Moreover, this product should be mounted on substrate within six months after delivery.
3) This product is damaged when it is used on the following conditions:
- Supply voltage is applied in the opposite way.
- Overvoltage which is larger than the value indicated in the specification.
4) This product will be damaged if it is used for a long time with the current (effective current) which exceeds the current rating. Careful attention must be paid so that maximum effective current is smaller than current rating.
5) The characteristics can be changed by the influences of nearby current and magnetic field and electric field. Please make sure of the mounting position.

As this product contains gallium arsenide, observe the following procedures for safety.
1) Do not alter the form of this product into a gas, powder, liquid, through burning, crushing, or chemical processing.
2) Observe laws and company regulations when discarding this product.

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