

### LOW-JITTER SAW OSCILLATOR (SPSO)

**OUTPUT: LV-PECL, LVDS** 

## **XG-2121CA XG-2102CA**

 Frequency range 100 MHz to 700 MHz 2.5 V --- XG-2121CA 3.3 V --- XG-2102CA LV-PECL or LVDS Supply voltage Output

Output enable (OE) Function External dimensions :  $7.0 \times 5.0 \times 1.2 \,\text{mm}$ Low jitter and low phase noise by SAW unit.





**Product Number** 

XG-2121CA P: X1M000311xxxx00 XG-2121CA L: X1M000351xxxx00 XG-2102CA P: X1M000301xxxx00

XG-2102CA L: X1M000341xxxx00





#### Specifications (characteristics)

Opecifications	(Cital at						
Item	O. mala al	LV-PECL		LVI	DS	Odifferent December	
	Symbol	XG-2121CA P	XG-2102CA P	XG-2121CA L	XG-2102CA L	Conditions / Remarks	
Output frequency range	fo	100 MHz to 700 MHz			Please contact us about available frequencies.		
Supply voltage	Vcc	2.5 V ± 0.125 V   3.3 V ± 0.33 V   2.5 V ± 0.125 V   3.3 V ± 0.33 V					
Storage temperature	T stg	-55 C to		0+125 C		Storage as single product.	
Operating temperature	T use	P: 0 C to	+70 C, R: -5 C	to +85 C, S: -20 C			
Frequency tolerance	f tol	G: ± 50 × 10 <sup>-8</sup> , H: ±100 × 10 <sup>-8</sup>					
Current consumption	Icc	60 mA Max.		30 mA Max.		OE=V <sub>CC</sub> , L ECL=50 Ω or l	LVDS=100 Ω
Disable current	I_dis	2 mA Max.		15 mA Max.		OE=GND	
Symmetry	SYM	45 % to 55 %		At outputs crossing point			
Output voltage (LV-PECL)	VoH	1.55 V Typ.	2.35 V Typ.	_	-		
		Vcc-1.025 V to Vcc-0.88 V		ı		DC characteristics	
	VoL	0.80 V Typ. 1.60 V Typ.		-			
	VOL	V <sub>cc</sub> -1.81 V to	V <sub>CC</sub> -1.81 V to V <sub>CC</sub> -1.62 V –				
Output voltage (LVDS)	Vod	-		350 mV Typ, 24	7 mV to 454 mV	V <sub>OD1</sub> , V <sub>OD2</sub>	
	dVop	-		50 mV Max.		$dV_{OD} =  V_{OD1}-V_{OD2} $	DC characteristics
	Vos	_		1.25 V Typ, 1.125 V to 1.375 V		Vos1, Vos2	
	dVos	_		150 mV Max.		dVos =   Vos1-Vos2	
Output load condition	L_ECL	50 Ω		_		Terminated to V <sub>CC</sub> -2.0 V	
(ECL) / (LVDS)	L LVDS	-		100 Ω		Connected between OUT to OUT	
Input voltage	VIH	70 % V <sub>CC</sub> Min.			OE terminal		
	V <sub>IL</sub>	30 % V <sub>CC</sub> Max.					
Rise time / Fall time	tr / tf	400 ps Max.			Between 20 % and 80 % o		
					Between 20 % and 80 %of Differential Output Peak to Peak voltage		
Start-up time	t_str	2.22	10 ms Max.		Time at minimum supply voltage to be 0 s		
Phase Jitter	tғл	0.23 ps Max. 0.22 ps Max. 0.21 ps Max. 0.18 ps Max. 0.16 ps Max.		0.27 ps Max.		100 MHz ≤ fo < 150 MHz	Offset frequency: -12 kHz to 20 MHz
				0.24 ps Max. 0.23 ps Max. 0.19 ps Max.		150 MHz ≤ fo < 200 MHz	
						200 MHz ≤ fo < 300 MHz	
						300 MHz ≤ fo < 400 MHz	
					s Max.	400 MHz ≤ fo < 500 MHz	1
		0.14 ps Max.		0.14 ps Max.		500 MHz ≤ fo < 600 MHz	4
		0.10 ps Max.		0.10 ps Max.		600 MHz ≤ fo ≤ 700 MHz	l .
Frequency aging	f age	± 10 × 10 <sup>-6</sup> / year Max.			+25 C, First year, Vcc=2.5	5 V, 3.3 V	

Product Name

XG-2121 CA 212.500000MHz P H P A (\$67: GRA, GSA are not available)

(Standard form)

1 2 3 4567

①Model

④Output (P:LV-PECL, L:LVDS)

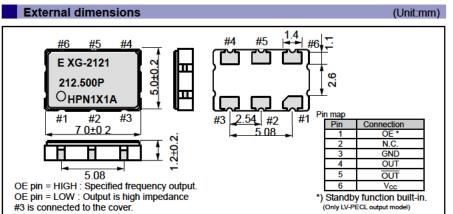
⑤Frequency tolerance ⑥Operating temperature

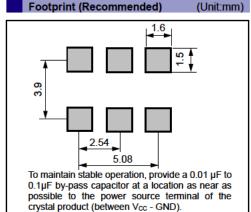
Trequency aging (A\*1: Frequency tolerance include aging, N\*2: Frequency tolerance exclude aging)

⑤Fr	(6)(	
G	±50 × 10 <sup>-8</sup>	Р
) 1	±100 × 10 <sup>-6</sup>	R
н	±100 x 10°	S

⑥Operating temp.			
Р	0 °C to +70 °C		
R	-5 °C to +85 °C		
S	-20 °C to +70 °C		

- This includes initial frequency tolerance, temperature variation, supply voltage change, reflow drift, and aging (+25 C,10 years).
- This includes initial frequency tolerance, temperature variation, supply voltage change, and reflow drift (except aging).





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Explanation of the mark that are using it for the catalog



►Pb free.



► Complies with EU RoHS directive.

\*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.





▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



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