

### SEIKO EPSON CORPORATION

**CRYSTAL OSCILLATOR (SPXO) OUTPUT : CMOS** 

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Product Number (please contact us) SG2016CAN: X1G004801xxxx00 SG-210STF: X1G004171xxxx00 SG3225CAN: X1G005961xxxx15 SG5032CAN: X1G004451xxxx00 SG7050CAN: X1G004481xxxx00

# SG2016 / 3225 / 5032 / 7050CAN SG-210STF

- Frequency
- Supply voltage
- Function
- : 1.8 V to 3.3 V Typ. Standby( $\overline{sT}$ ) :
- Operating temperature : -40 °C to +105 °C

20 standard frequencies





(3.2 x 2.5 mm)





(2.5 x 2.0 mm) (2.0 x 1.6 mm)

SG5032CAN (5.0 x 3.2 mm)

SG7050CAN (7.0 x 5.0 mm)

### Specifications (characteristics)

Item	Symbol	Specifications				Conditions / Remarks						
Output frequency	fo	14.7456 MHz 16 25 MHz 26	MHz 6 MHz 6 MHz 6 MHz 0 MHz	10 MHz 20 MHz 27 MHz 48 MHz	12 MHz 24 MHz 32 MHz 50 MHz	z 24.576 MH z 33.33 MH	lz z					
Supply voltage	Vcc	1.60 V to 3.63 V 1.71 V to 3.63 V 2.25 V to 3.63 V						$f_0 = 72 \text{ MHz}$ T $\mu_{C0} = 195 ^{\circ}\text{C}$ Max				Refer to Figure 1
Storage temperature	T_stg	-55 °C to +125 °C -40 °C to +125 °C						SG2016CAN All others				
Operating temperature	T_use	-20 °C to +70	°C, -40	°C to +85 °C	C, -40 °C	to +105 °C		See of fig	ure *1			
Frequency tolerance	f_tol	±25 × 10 <sup>-6</sup> ±50 × 10 <sup>-6</sup>					-20 °C to +70 °C -40 °C to +85 °C, -40 °C to +105 °C					
Current consumption	V <sub>CC</sub> = 1.8 V ± 10 %         V <sub>CC</sub> = 2.5 V ± 10 %         V <sub>CC</sub> = 3.3 V ± 10           1.5 mA Max.         1.6 mA Max.         1.8 mA Max.           1.8 mA Max.         2.0 mA Max.         2.2 mA Max.           2.1 mA Max.         2.4 mA Max.         2.8 mA Max.           2.4 mA Max.         2.8 mA Max.         3.0 mA Max.			$\begin{tabular}{lllllllllllllllllllllllllllllllllll$								
Stand-by current	I_std	2.1 µA Max.	2	2.5 µA Max.	2.7 µA Max.			ST =GND				
Symmetry	SYM						level, L_CMO	S ≤ 15 pF				
Output voltage	VOH VOL VOH-2 VOL-2	90 % V <sub>CC</sub> Min. 10 % V <sub>CC</sub> Max. V <sub>CC</sub> - 0.4 V Min. 0.4 V Max.						I <sub>OH</sub> I <sub>OL</sub>	1.8 V ± 10 % -1.5 mA 1.5 mA 1.8 V±10 % -3 mA 3 mA	2.5 V ± 10 % -3 mA 3 mA 2.5 V±10 % -4 mA 4 mA	3.3 V ± -4 n 4 m 3.3 V± -6 n 6 m	nA IA 10 % nA
Output load condition (CMOS)	L_CMOS	15 pF Max.						IOL	3 1114	4 111A	011	
Input voltage	Vih Vil	80 % V <sub>CC</sub> Min. 20 % V <sub>CC</sub> Max.						- ST terminal				
Rise time and Fall time	tr / tf	3 ns Max. 3.5 ns Max. (@1.8 V±10 %)						20 % V <sub>CC</sub> to 80 % V <sub>CC</sub> level, L_CMOS = 15 pF				
Start-up time	t_str	3 ms Max.						T = 0 at 90 % V <sub>CC</sub>				
Frequency aging	f_age		±3 ×	10 <sup>-6</sup> / year N	lax.			+25 °C, F	irst year			

#### [Model: SG2016/3225/5032/7050CAN]

Product name SG2016 C AN 25.00000MHz T J H A (Standard form) 1 3 (4)(5)(6)(7)(2) ①Model ②Output(C: CMOS) ③Frequency ④Supply voltage (5) Frequency tolerance (6) Operating temperature range ⑦Internal identification code("A" is default) ④Supply v

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pply voltage *See Figure 1	⑤Fre	5 Frequency tolerance / 6 Operating temperature range			
1.8 V to 3.3 V Typ.	DB	±25 × 10 <sup>-6</sup> / -20 °C to +70 °C			
2.5 V to 3.3 V Typ.	JG	±50 × 10 <sup>-6</sup> / -40 °C to +85 °C			
	JH	±50 × 10 <sup>-6</sup> / -40 °C to +105 °C			

[Model : SG-210STF]

T 1.8

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Product name SG-210 S T F 25.00000MHz Y 23 (Standard form) 1 **(4**) (5) ①Model ②Function(S:Standby) ③Supply voltage ④Frequency ⑤Frequency tolerance ③Supply voltage \*See Figure 1 T 1.8 V to 3.3 V Typ.

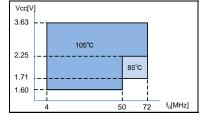
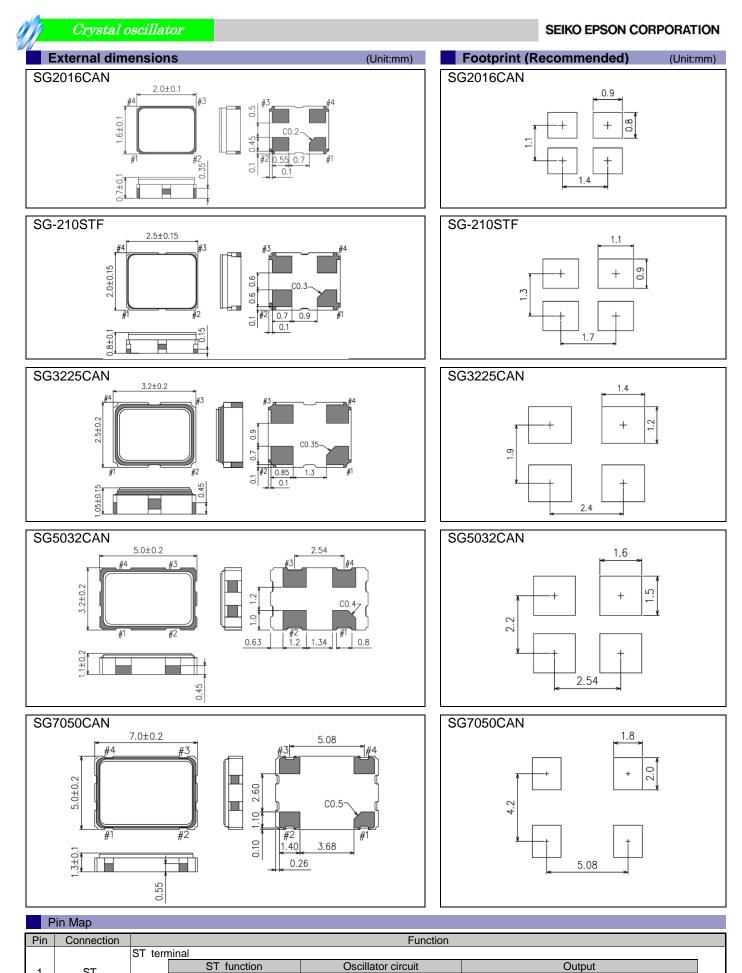


Figure 1 : The upper limit of Operating temperature and the related conditions

Please note that Supply voltage range  $(V_{\text{CC}})$ depends on Output frequency (fo) and upper limit of Operationg temperature (T\_use Max.).

(	⑤Frequency tolerance						
	S	±25 × 10 <sup>-6</sup> / -20 °C to +70 °C					
	L	±50 × 10 <sup>-6</sup> / -40 °C to +85 °C					
	Y	±50 × 10 <sup>-6</sup> / -40 °C to +105 °C					



1	51		HIGH or "open"	Oscillation	Specified frequency: Enable	
			LOW	Oscillation stop	High impedance: Disable	
2	GND	Ground				
3	OUT	Clock ou	utput			
4	V <sub>cc</sub>	Power s	upply			
■Note	s: To maintain	stable op	eration, provide a 0.01uF to	0.1uF by-pass capacitor at a loca	ation as near as possible to the power source tern	ninal o

of the crystal product (between Vcc - GND).

# PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

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