

SEIKO EPSON CORPORATION

REAL TIME CLOCK MODULE (I²C-Bus) **Built-in EEPROM and Unique ID-ROM**

RX-8731LC

- •Built in frequency adjusted 32.768 kHz crystal unit.
- •Interface Type
- : I²C-Bus interface (400 kHz)

CLOCK and CALENDAR

TIMER REGISTER

ALARM REGISTER

REGISTER

CONTROLLER

CONTROL

and

SYSTEM

EEPROM 10 Byte (80 bit)

ID-ROM 6 Byte (48 bit)

- •Operating voltage range : 1.7 V to 5.5 V
- •Wide voltage for Timekeeping
- : 1.3 V to 5.5 V
- : 0.35 µA / 3 V (Typ.) Low backup current

32.768 kHz

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osc

•32.768 kHz frequency output function : C-MOS output With Control Pin

INTERRUPTS

CONTROLLER

I/O Port

I²C-Bus

CIRCUIT

INTERFACE

FOUT CONTROLLER

- •The various functions include full calendar, alarm, timer.
- * The I²C-Bus is a trademark of NXP Semiconductors

Block diagram

FOE

/ IRO

P00 - P03

A 0

SDA

SCL

FOUT



Product Number



RX-8731LC : Q418731C2000100

Overview

RoHS

Complian

- Built in 10 Byte (80 bit) EEPROM
- Programmable I/O ports
 - 4 Programmable I/O ports

• Interface Type

- FOE pin enables output on/off control.
 Output frequency is selectable.

- Update interrupt function

Pin F	unction		Terminal connection / External dimensions (Unit:mm)
Signal Name	Input / Output	Function	RX – 8731 LC
SCL	Input	Serial Clock input pin.	
SDA	Bi-directional	Data input and output pin.	2. P00 G P 11. P01
A 0	Input	Device address A0 input pin.	3. SDA
FOUT	Output	FOUT pin is 32.768 kHz clock output pin (C-MOS) that output control is possible.	4. SCL $\rightarrow 24 \rightarrow 9$ P03
FOE	Input	FOE pin control the frequency output from FOUT pin with FSEL1-bit and FSEL0-bit.	5. A0
/ IRQ	Output	Interrupt output pin. (N-ch open drain)	
P00 P01 P02 P03	Bi-directional	Programmable I/O ports.	6. GND → 28 → 7. FOUT VSOJ – 12pin *Stop using the glue
Vdd	-	Connected to a positive power supply.	Any glue must never use it after soldering LC-package to a circuit board. This product has glass on the back side of a package. When glue invasions between circuit board
GND	_	Connected to a ground.	side and glass side, then glass cracks by thermal expansion of glue. In this case a crystal oscillation stops. Consider glue abolition or glue do not touch to LC-package

Specifications (characteristics)

Recommended Operating Conditions						
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Power voltage	Vdd	-	1.7	3.0	5.5	V
Clock voltage	VCLK	_	1.3	3.0	5.5	V
Operating temperature	Topr		-40	+25	+85	°C

Frequency characteristics

Item	Symbol	Conditions	Rating	Unit
Frequency tolerance	∆f/f	Ta = +25 °C VDD = 3.0 V	B: 5 ± 23 *	× 10 ⁻⁶
Oscillation	4	Ta = +25 °C VDD = 1.6 V	1 Max.	s
Start-up time	time t _{STA}	Ta = -40 °C to +85 °C VDD = 1.6 V	3 Max.	s
*Equivalent to ±1 r	ninute of mo	nthly deviation		

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit	
	Івк	fSCL = 0 Hz / IRQ = OFF	VDD = 5 V	-	0.45	1.5	μA
Current		FOUT : output OFF (Hi - z)	VDD = 3 V	-	0.35	1.4	
Consumption	132k	fSCL = 0 Hz / IRQ = OFF	VDD = 5 V	-	8.0	16.0	μA
		FOUT : 32.768 kHz output CL = 30 pF	VDD = 3 V	-	5.0	10.0	

- Built in EEPROM and ID-ROM
 - Built in 6 Byte (48 bit) ID-ROM

- · I²C-Bus high-speed bus specifications. (400 kHz)

• 32.768 kHz frequency output function • FOUT pin output (C-MOS output), CL=30 pF

- < 32.768 kHz, 1024 Hz, 1 Hz >
- The various interrupt function
 - Alarm interrupt function
 Timer interrupt function

* Refer to application manual for details.

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