

MMDT5551

160V DUAL NPN HIGH-VOLTAGE TRANSISTOR IN SOT363

Features

- Epitaxial Planar Die Construction
- Complementary PNP Type MMDT5401
- Ideal for Medium Power Amplification and Switching
- Ultra-Small Surface-Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part.
 A listing can be found at

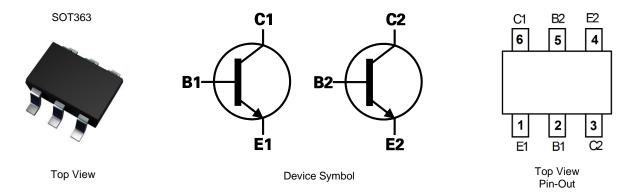
https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOT363
- Package Material: Molded Plastic, "Green" Molding Compound,
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Finish.
 Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.006 grams (Approximate)



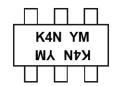
Ordering Information (Note 4)

Part Number	ımber Package Marking Reel Size (inches)		Tape Width (mm)	Packing		
Fart Number	Package	Ivial Killy	Reel Size (Iliches)	rape width (mm)	Qty.	Carrier
MMDT5551-7-F	SOT363	K4N	7	8	3000	Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



K4N = Product Type Marking Code YM = Date Code Marking Y = Year (ex: L = 2024) M = Month (ex: 4 = April)

Date Code Key

Year	2012	-	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	Z	•	L	М	N	Р	R	S	Т	U	V	W
	_			_					I _		1	_
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Absolute Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	180	V
Collector-Emitter Voltage	VCEO	160	V
Emitter-Base Voltage	V _{EBO}	6	V
Continuous Collector Current	Ic	200	mA

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Dower Dissipation	(Note 5)	D-	200	mW	
Power Dissipation	(Notes 6 & 7)	P _D	320		
The moral Desistance I have then to Ambient	(Note 5)	-	625	°C/W	
Thermal Resistance, Junction to Ambient	(Notes 6 & 7)	Reja	390		
Thermal Resistance, Junction to Case	(Note 8)	Rejc	140		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

Notes:

- 5. For a device mounted on minimum recommended pad layout 1oz weight copper that is on a single-sided FR4 PCB; device is measured under still air conditions whilst operating in a steady state.
- 6. Same as Note 5, except the device is mounted 25mm x 25mm 2oz copper.
- 7. Maximum combined dissipation.
- 8. Thermal resistance from junction to the top of package.

Thermal Characteristics and Derating Information

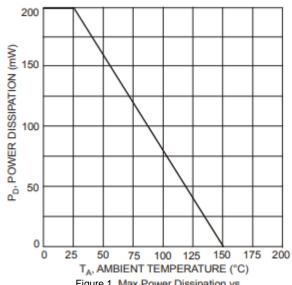


Figure 1. Max Power Dissipation vs. Ambient Temperature



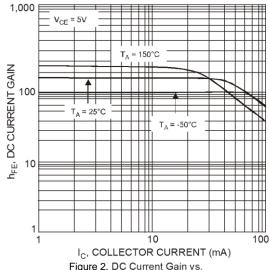
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS								
Collector-Base Breakdown Voltage	ВУсво	180	—	_	V	$I_C = 100\mu A, I_E = 0$		
Collector-Emitter Breakdown Voltage (Note 9)	BVceo	160	_	_	V	Ic = 1mA, I _B = 0		
Emitter-Base Breakdown Voltage	BVEBO	6	_	_	V	IE = 10μA, IC = 0		
Collector-Base Cutoff Current	lone		_	50	nA	V _{CB} = 120V, I _E = 0		
Collector-Base Cuton Current	Ісво		_	50	μA	$V_{CB} = 120V, I_E = 0, T_A = +100^{\circ}C$		
Base-Emitter Cutoff Current	IEBO	_	_	50	nA	V _{EB} = 4V, I _C = 0		
ON CHARACTERISTICS (Note 9)								
		80		_		Ic = 1mA, VcE = 5.0V		
DC Current Gain	hfE	80	_	250		Ic = 10mA, VcE = 5.0V		
		30		_		I _C = 50mA, V _{CE} = 5.0V		
Collector-Emitter Saturation Voltage	\/·			0.15	V	Ic = 10mA, I _B = 1.0mA		
Collector-Emitter Saturation Voltage	V _{CE(sat)}	_	_	0.20	V	Ic = 50mA, I _B = 5.0mA		
Page Emitter Seturation Voltage	\/·		_	1.0	V	Ic = 10mA, I _B = 1.0mA		
Base-Emitter Saturation Voltage	V _{BE(sat)}					Ic = 50mA, I _B = 5.0mA		
SMALL SIGNAL CHARACTERISTICS								
Output Capacitance	Cobo	-	_	6.0	pF	VcB = 10V, f = 1.0MHz, IE = 0		
Small Signal Current Gain	h _{fe}	50	_	250	_	Ic = 1mA, VcE = 10V, f = 1.0MHz		
Current Gain-Bandwidth Product	f _T	100	_	300	MHz	I _C = 10mA, V _{CE} = 10V, f = 100MHz		
Noise Figure	NF	_	_	8.0	dB	$V_{CE} = 5.0V$, $I_C = 200\mu A$, $R_S = 1k\Omega$, $f = 1.0kHz$		

Note: 9. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%.



Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)





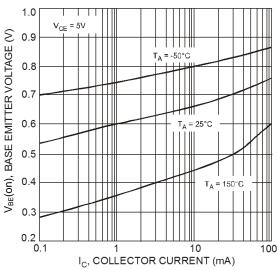


Figure 4. Base Emitter Voltage vs. Collector Current

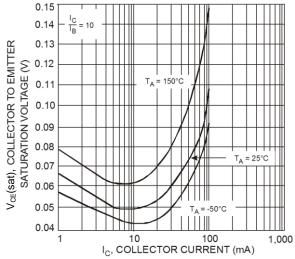


Figure 3. Collector Emitter Saturation Voltage vs. Collector Current

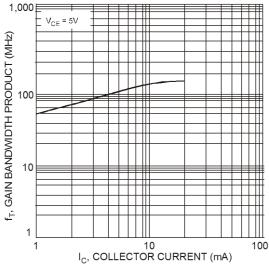


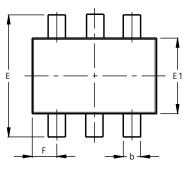
Figure 5. Gain Bandwidth Product vs. Collector Current

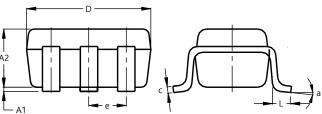


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



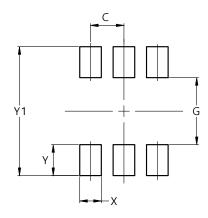


SOT363							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.10	0.30	0.25				
С	0.10	0.22	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	0.650 BSC						
F	0.40	0.45	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

 $\label{prop:lease} Please see \ http://www.diodes.com/package-outlines.html for the latest version.$

SOT363



Dimensions	Value
Dimensions	(in mm)
С	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.



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