



DMTH10H4M6SPS

100V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	Rds(on) Max	Ι _D T _C = +25°C
100V	4.6mΩ @ V _{GS} = 10V	118.8 A

Description and Applications

This new generation N-channel enhancement mode MOSFET is designed to minimize $R_{DS(ON)}$ yet maintain superior switching performance. This device is ideal for use in notebook battery power management and load switch.

- Motor controls
- DC-DC converters
- Power managements

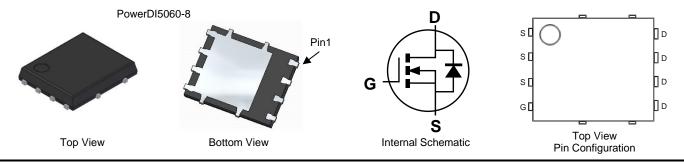
Features and Benefits

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low RDS(ON) Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
 For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)



Ordering Information (Note 4)

Part Number	Package	Packing		
	Fackage	Qty.	Carrier	
DMTH10H4M6SPS-13	PowerDI5060-8	2,500	Tape & Reel	

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



);;; = Manufacturer's Marking T10H4M6SS = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 23 = 2023) WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	100	V		
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current, V_{GS} = 10V (Note 5)	Steady State	$T_A = +25^{\circ}C$ $T_A = +100^{\circ}C$	ID	17 12	А
Continuous Drain Current, V _{GS} = 10V (Note 6)	Steady State	Tc = +25°C T _C = +100°C	ID	118.8 84	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	IDМ	475	A		
Maximum Continuous Body Diode Forward Current (No	ls	118.8	А		
Pulsed Body Diode Forward Current (10µs Pulse, T _C =	I _{SM}	475	A		
Avalanche Current (Note 7) L = 0.3mH	las	41	A		
Avalanche Energy (Note 7) L = 0.3mH	Eas	252	mJ		

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5) $T_A = +25^{\circ}C$		PD	2.7	W
Thermal Resistance, Junction to Ambient (Note 5)	Reja	54	°C/W	
Total Power Dissipation (Note 6)	PD	136	W	
Thermal Resistance, Junction to Case (Note 6)	Rejc	1.1	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +175	°C	

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Cumula al	Min	Turn	Max	Unit	Test Condition	
	Symbol	Min	Тур	Max	Unit	lest Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	100	—		V	$V_{GS} = 0V$, $I_D = 10mA$	
Zero Gate Voltage Drain Current	IDSS	_	—	1	μA	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	lgss		_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(th)	2	_	4	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	RDS(ON)	_	3	4.6	mΩ	VGS = 10V, ID = 30A	
Diode Forward Voltage	V _{SD}	_	0.8	1.2	V	$V_{GS} = 0V, I_{S} = 30A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	4327	_		V _{DS} = 50V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss	_	1335	_	pF		
Reverse Transfer Capacitance	Crss		39	_			
Gate Resistance	Rg	-	2.1	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	66	_			
Gate-Source Charge	Qgs	_	18	_	nC	V _{DD} = 50V, I _D = 30A, V _{GS} = 10V	
Gate-Drain Charge	Q _{gd}	_	17	_			
Turn-On Delay Time	tD(ON)	_	15.2	_			
Turn-On Rise Time	t _R	_	26.4	_	ns	$\begin{split} V_{DD} &= 50V, V_{GS} = 10V, \\ I_D &= 30A, R_G = 4.7\Omega, R_L = 1.1\Omega \end{split}$	
Turn-Off Delay Time	tD(OFF)	_	44.9				
Turn-Off Fall Time	tF		28.2				
Reverse Recovery Time	t _{RR}		63		ns		
Reverse Recovery Charge	QRR	_	136		nC	I⊧ = 22.5A, di/dt = 100A/µs	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.

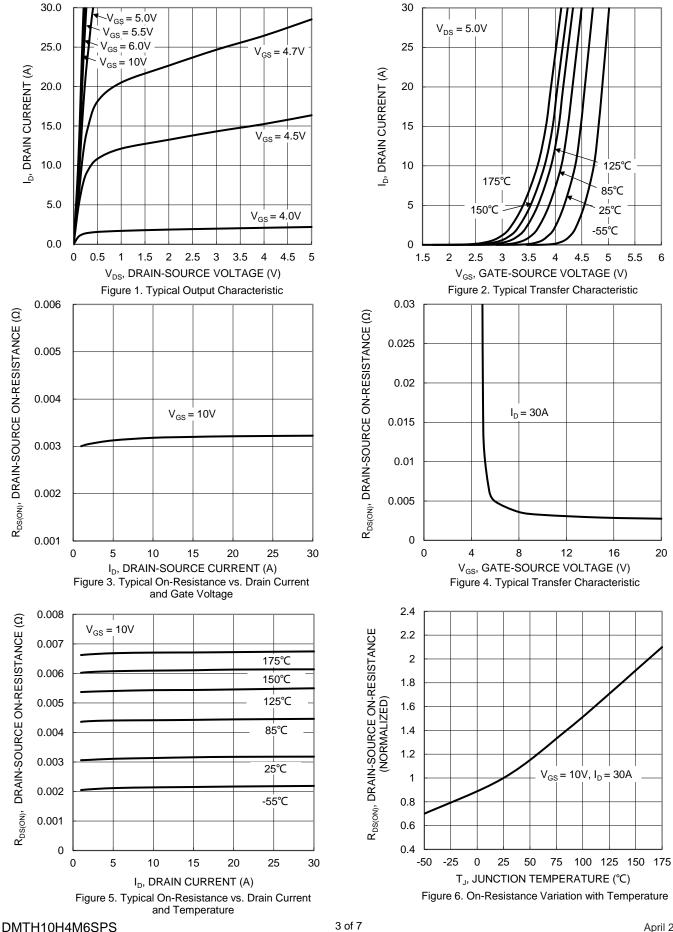
6. Thermal resistance from junction to soldering point (on the exposed drain pad).

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.



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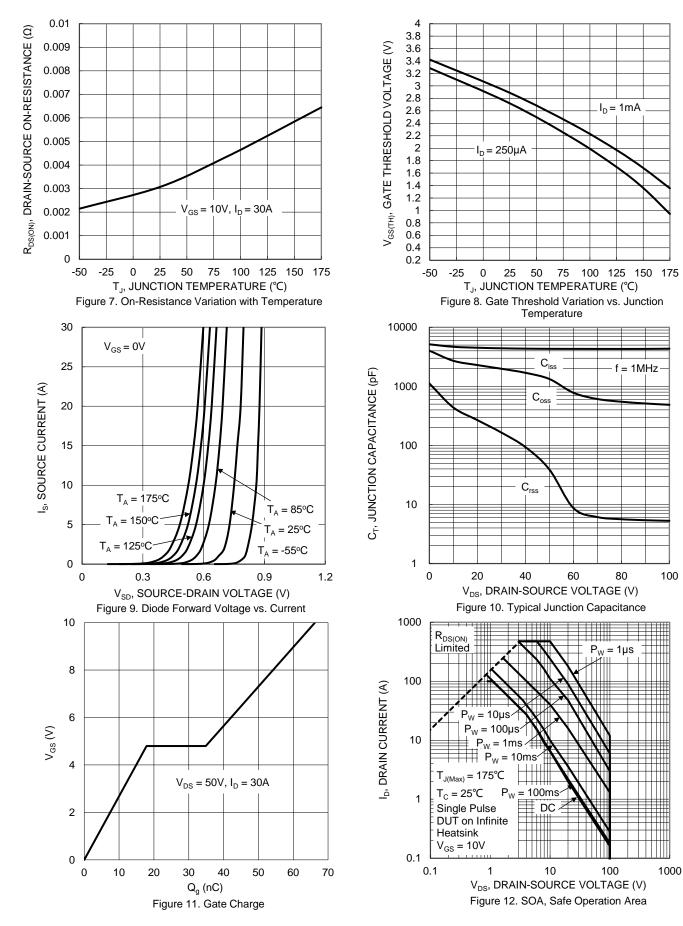


Document number: DS41950 Rev. 3 - 2

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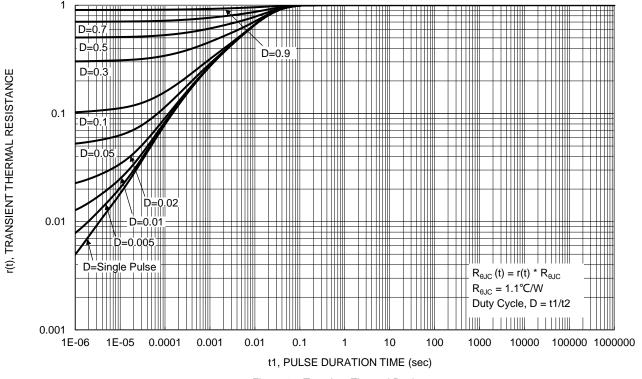


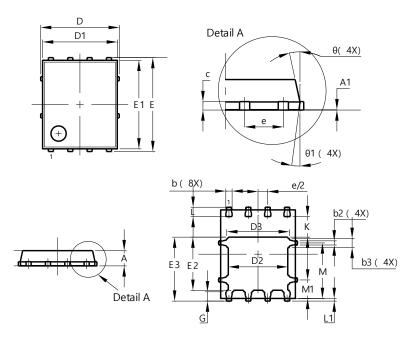
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

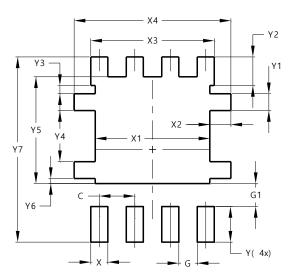


PowerDI5060-8						
Dim	Min	Тур				
Α	0.90	1.10	1.00			
A1	0.00	0.05	-			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
c	0.230	0.330	0.277			
D		5.15 BSC				
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	4.30	4.10			
E		6.15 BSC	;			
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е		1.27 BSC	;			
G	0.51	0.71	0.61			
ĸ	0.51 –		-			
L	0.51 0.71		0.61			
L1	0.100	0.200	0.175			
М	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
Θ	10°	12°	11°			
Θ1	6°	8°	7°			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610



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