



DMT31M7LSS

#### **Product Summary**

BV <sub>DSS</sub>	Rds(on) Max	I⊳ Max Tc = +25°C
	$2.7 m\Omega @ V_{GS} = 10V$	78A
30V	$4m\Omega @ V_{GS} = 4.5V$	64A

#### **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (RDs(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- Power Management Functions
- DC-DC Converters

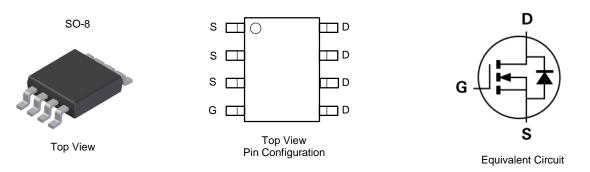
#### N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Features and Benefits**

- Low RDS(ON) Minimizes On-State Losses
- Excellent Q<sub>gd</sub> x R<sub>DS(ON)</sub> Product (FOM)
- Advanced Technology for DC-DC Converters
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

#### **Mechanical Data**

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections Indicator: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Lead Frame.
  Solderable per MIL-STD-202, Method 208 3
- Weight: 0.074 grams (Approximate)



#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMT31M7LSS-13	SO-8	2,500/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

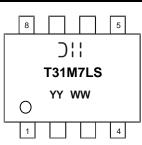
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

#### **Marking Information**

Notes:



)'' = Manufacturer's Marking T31M7LS = Product Type Marking Code YYWW = Date Code Marking YY or  $\overline{YY}$  = Year (ex: 21 = 2021) WW or <u>WW</u>= Week (01 to 53)



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V <sub>DSS</sub>	30	V
Gate-Source Voltage		Vgss	±20	V
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 6)	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	25 20	А
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 7)	Tc = +25°C T <sub>C</sub> = +70°C	ID	78 63	А
Maximum Continuous Body Diode Forward Current (Note	: 6)	ls	3.3	А
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)		Ідм	150	А
Pulsed Body Diode Forward Current (380µs Pulse, Duty Cycle = 1%)		lsм	150	А
Avalanche Current, L=0.1mH (Note 8)		las	59	А
Avalanche Energy, L=0.1mH (Note 8)		E <sub>AS</sub>	176	mJ

#### **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	1.7	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	73	°C/W
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	PD	2.2	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	RθJA	56	°C/W
Thermal Resistance, Junction to Case (Note 7)	T <sub>C</sub> = +25°C	R <sub>0JC</sub>	5.9	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

#### Electrical Characteristics (T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)	Oymbol		iyp	Max	Unit	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30		_	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	IDSS	_		1	μA	$V_{DS} = 24V, V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 9)						
Gate Threshold Voltage	Vgs(th)	1.0	_	3.0	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$
Static Drain-Source On-Resistance		_	2.1	2.7		VGS = 10V, ID = 20A
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	2.6	4	mΩ	VGS = 4.5V, ID = 20A
Diode Forward Voltage	V <sub>SD</sub>	_	0.7	1.0	V	$V_{GS} = 0V, I_S = 2A$
DYNAMIC CHARACTERISTICS (Note 10)						•
Input Capacitance	Ciss	_	5492	_		V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	Coss		2261	—	pF	
Reverse Transfer Capacitance	Crss	_	317	_		
Gate Resistance	Rg	_	1.6	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	84	_		
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	43	_	nC	$V_{DD} = 15V, I_D = 20A$
Gate-Source Charge	Q <sub>gs</sub>	_	13	_	nc	
Gate-Drain Charge	Q <sub>gd</sub>	_	12	_		
Turn-On Delay Time	tD(ON)	_	15	_		
Turn-On Rise Time	tR	_	7	_		$\label{eq:VDD} \begin{array}{l} V_{DD} = 15V, \ V_{GS} = 10V, \\ R_g = 3\Omega, \ I_D = 20A \end{array}$
Turn-Off Delay Time	tD(OFF)	_	55	_	ns	
Turn-Off Fall Time	tF	_	41	—		
Reverse Recovery Time	trr	_	33	_	ns	I <sub>F</sub> = 15A, dI/dt = 500A/µs
Reverse Recovery Charge	Q <sub>RR</sub>	_	55	_	nC	I <sub>F</sub> = 15A, dl/dt = 500A/µs

Notes:

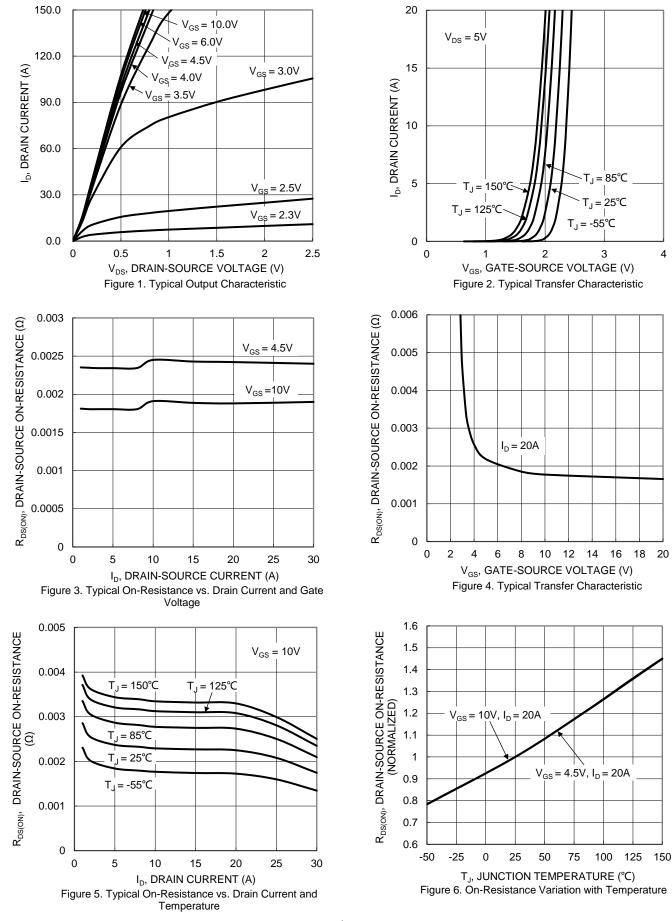
Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad).

8. I<sub>AS</sub> and E<sub>AS</sub> ratings are based on low frequency and duty cycles to keep  $T_J$  = +25°C.

9. Short duration pulse test used to minimize self-heating effect. 10. Guaranteed by design. Not subject to product testing.



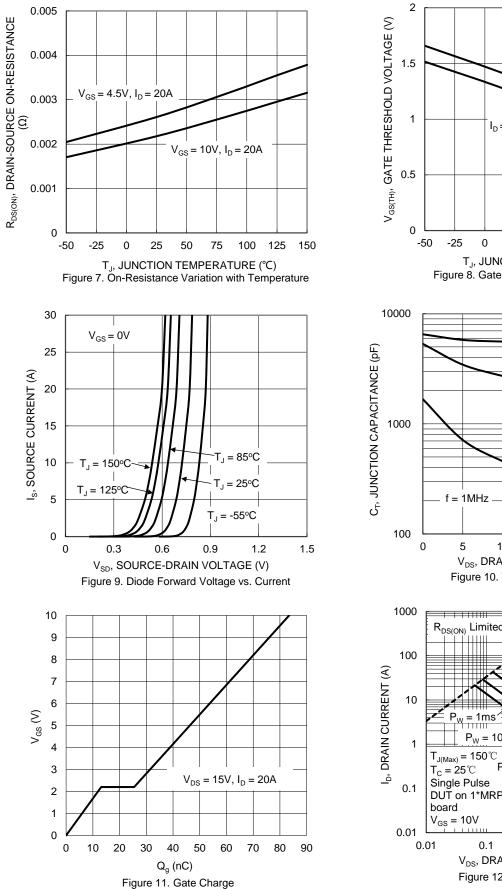
#### DMT31M7LSS

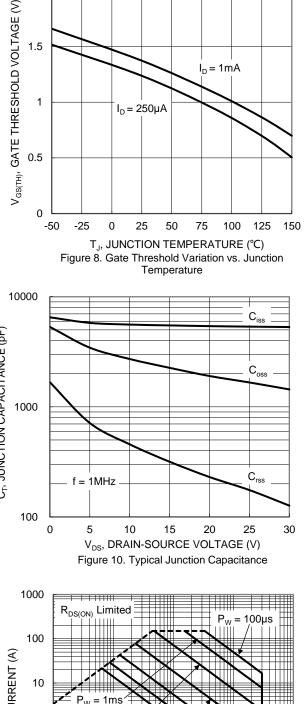


DMT31M7LSS Document number: DS42094 Rev. 5 - 2



## DMT31M7LSS





ттт  $P_W = 10ms$ 

0.1

 $\mathsf{P}_{\mathsf{W}}$ 

= 100ms

Ρ = 1s

1

V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V)

Figure 12. SOA, Safe Operation Area

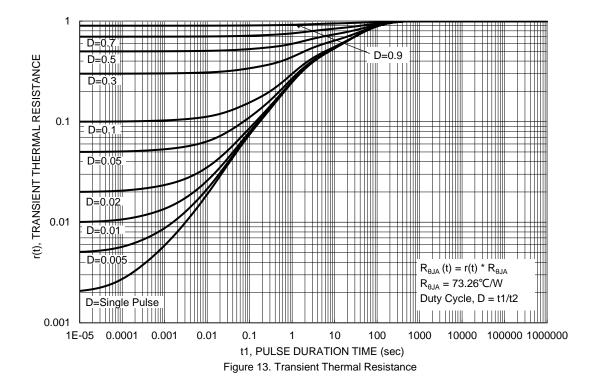
 $P_W = 10s$ 

₽ DC

10

100

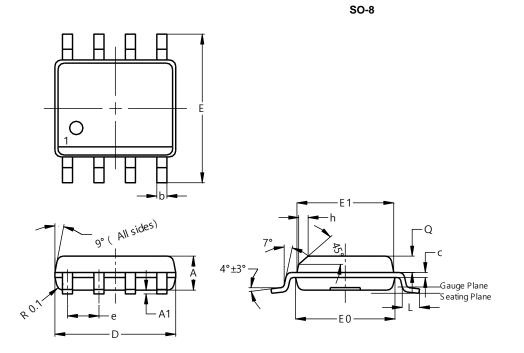






## Package Outline Dimensions

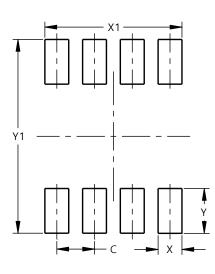
Please see http://www.diodes.com/package-outlines.html for the latest version.



SO-8					
Dim	Min	Max	Тур		
Α	1.40	1.50	1.45		
A1	0.10	0.20	0.15		
b	0.30	0.50	0.40		
С	0.15	0.25	0.20		
D	4.85	4.95	4.90		
Е	5.90	6.10	6.00		
E1	3.80	3.90	3.85		
E0	3.85	3.95	3.90		
е			1.27		
h			0.35		
L	0.62	0.82	0.72		
q	0.60	0.70	0.65		
All Dimensions in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



SO-8

Dimensions	Value (in mm)			
С	1.27			
Х	0.802			
X1	4.612			
Y	1.505			
Y1	6.50			



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