

Product Summary

BV _{DSS}	R _{DS(ON)} Max V _{GS} = 10V	I _D Max T _A = +25°C
100V	35mΩ	6.0A

Description and Applications


This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Wireless charging
- DC-DC converters
- Power managements

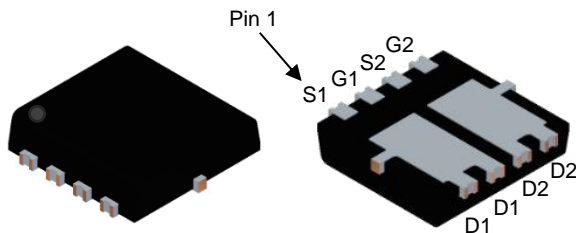
Features and Benefits

- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} – Ensures On-State Losses are Minimized
- Low Input Capacitance
- Fast Switching Speed
- Wettable Flank for Improved Optical Inspection
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at <https://www.diodes.com/products/automotive/automotive-products/>**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.**
<https://www.diodes.com/quality/product-definitions/>

Mechanical Data

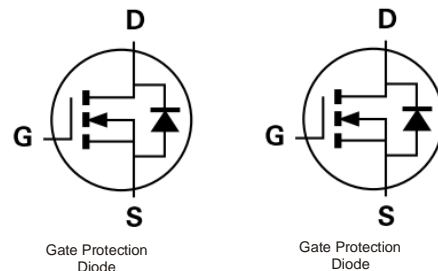
- Package: POWERDI[®]3333-8
- Package Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 
- Weight: 0.03 grams (Approximate)

POWERDI[®]3333-8/SWP (Type UXD)



Top View

Bottom View



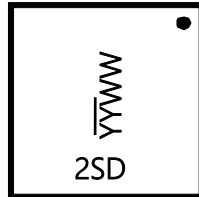
Internal Schematic

Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMT10H032SDVW-7	POWERDI [®] 3333-8/SWP (Type UXD)	2,000	Tape & Reel
DMT10H032SDVW-13	POWERDI [®] 3333-8/SWP (Type UXD)	3,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, “Green” and Lead-free.
 3. Halogen- and Antimony-free “Green” products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



2SD = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 24 = 2024)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	100	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 5)	Steady State	T _A = +25°C	I _D	6.0	A
		T _A = +70°C		4.8	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I _{DM}	35	A
Maximum Continuous Body Diode Forward Current (Note 5)			I _S	2.2	A
Pulsed Body Diode Forward Current (10μs Pulse, Duty Cycle = 1%)			I _{SM}	35	A
Avalanche Current, L = 0.3mH			I _{AS}	13	A
Avalanche Energy, L = 0.3mH			E _{AS}	25.3	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	1.2	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	107	°C/W
Total Power Dissipation (Note 5)	T _A = +25°C	P _D	2.3	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	55	°C/W
Thermal Resistance, Junction to Case (Note 5)		R _{θJC}	5.5	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Notes: 5. Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1 inch square copper plate.
 6. Device mounted on FR-4 PCB, with minimum recommended pad layout.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	100	—	—	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 80V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	2	—	4	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	27	35	mΩ	V _{GS} = 10V, I _D = 5A
Diode Forward Voltage	V _{SD}	—	0.8	1.0	V	V _{GS} = 0V, I _S = 5A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	544	—	pF	V _{DS} = 50V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	181	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	6.0	—	pF	
Gate Resistance	R _g	—	1.2	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	4.3	—	nC	V _{DS} = 50V, I _D = 7A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	8.0	—	nC	
Gate-Source Charge	Q _{gs}	—	1.8	—	nC	
Gate-Drain Charge	Q _{gd}	—	2.4	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	8.5	—	ns	V _{DS} = 50V, I _D = 7A V _{GS} = 10V, R _{GEN} = 6Ω
Turn-On Rise Time	t _R	—	2.7	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	11.9	—	ns	
Turn-Off Fall Time	t _F	—	6.2	—	ns	I _F = 7A, di/dt = 100A/μs
Reverse Recovery Time	t _{RR}	—	33.2	—	ns	
Reverse Recovery Charge	Q _{RR}	—	34.3	—	nC	

Notes: 7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

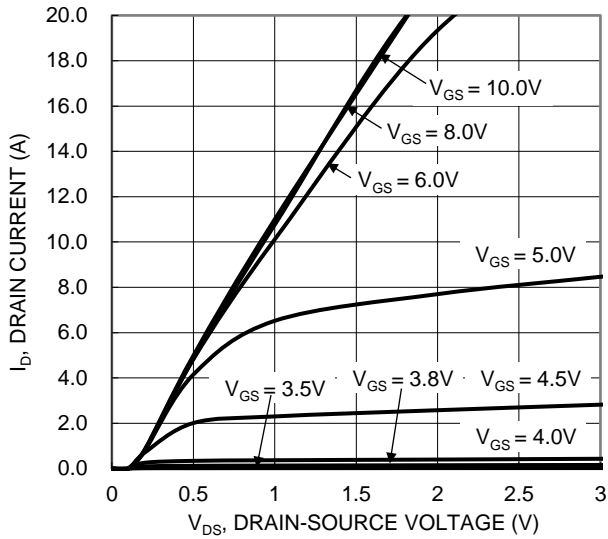


Figure 1. Typical Output Characteristic

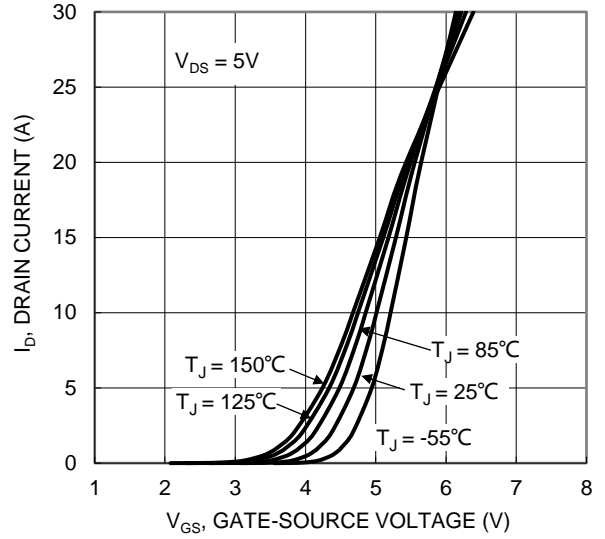


Figure 2. Typical Transfer Characteristic

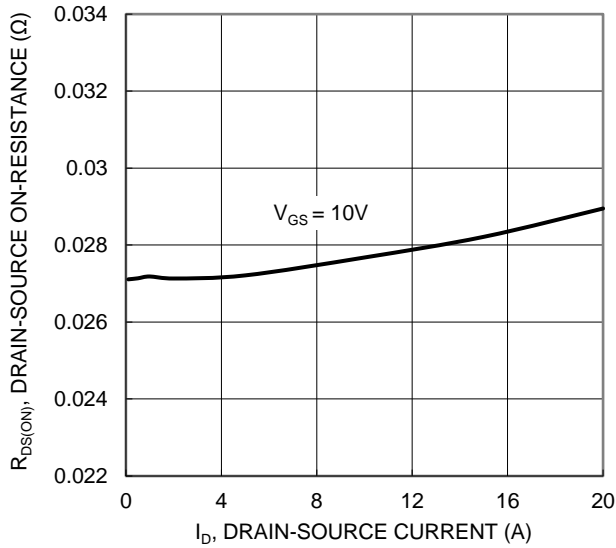


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

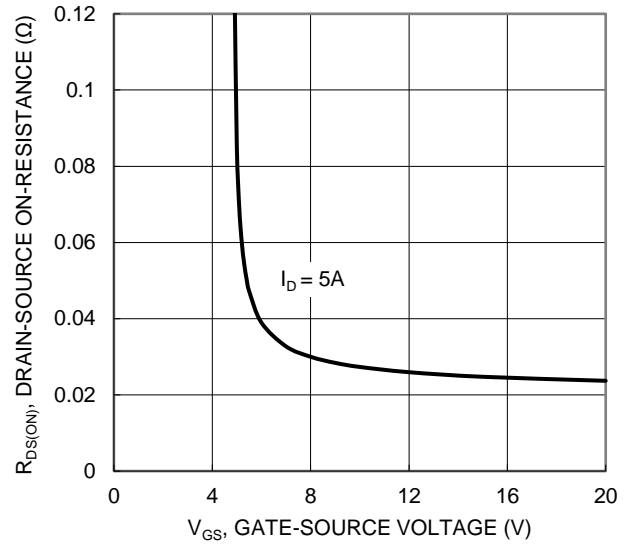


Figure 4. Typical Transfer Characteristic

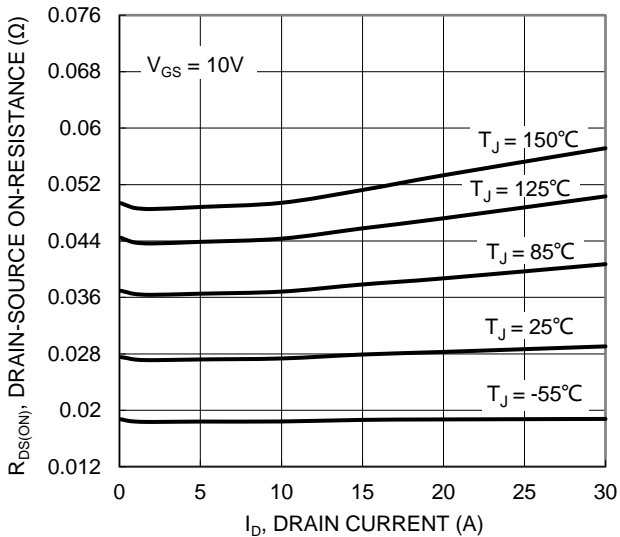


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

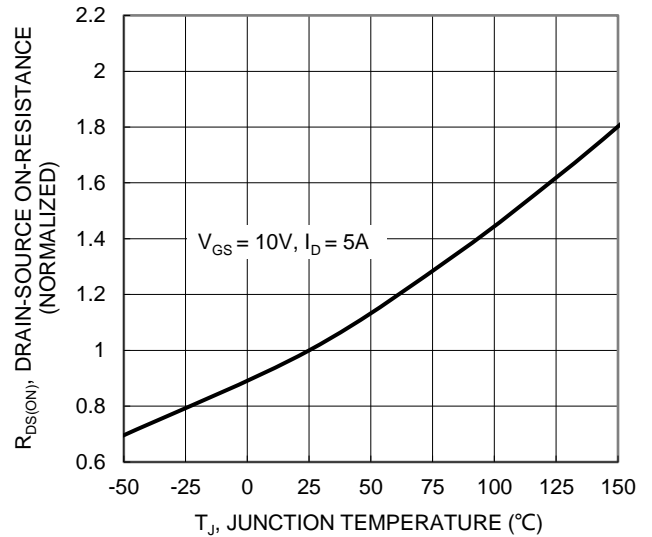


Figure 6. On-Resistance Variation with Temperature

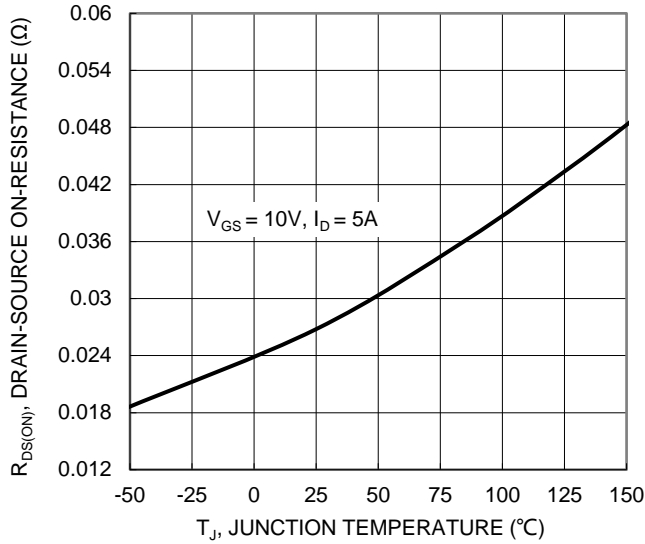


Figure 7. On-Resistance Variation with Temperature

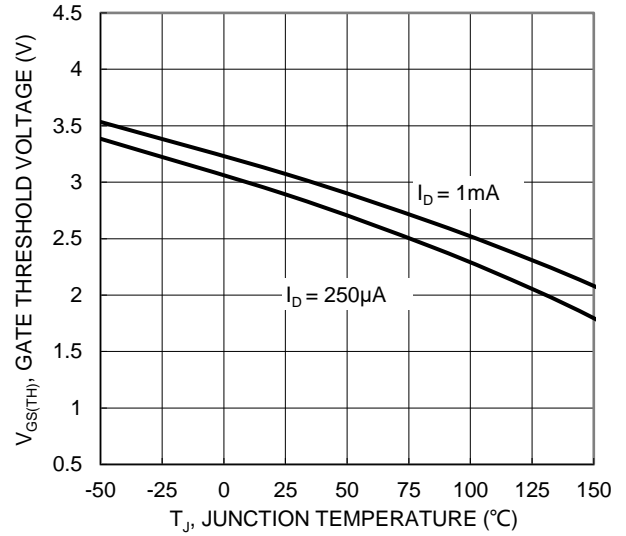


Figure 8. Gate Threshold Variation vs. Junction Temperature

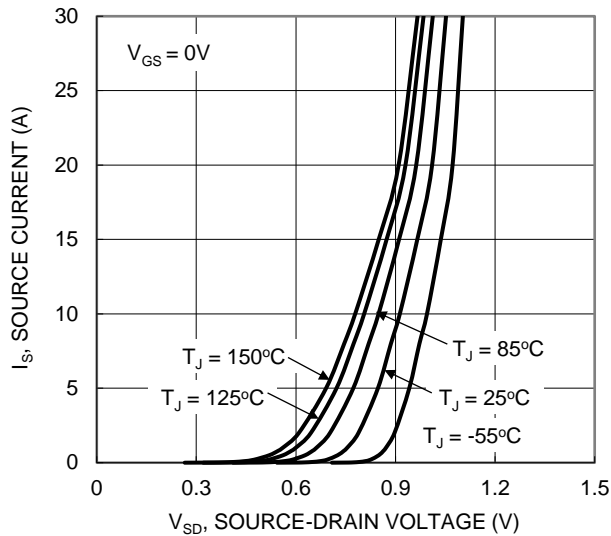


Figure 9. Diode Forward Voltage vs. Current

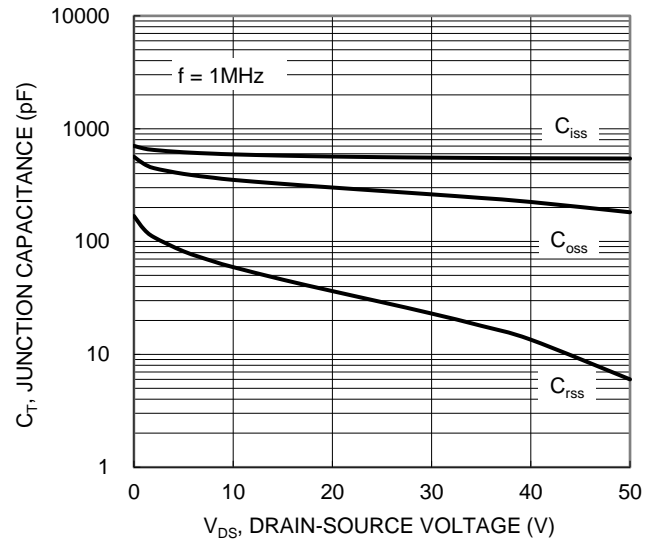


Figure 10. Typical Junction Capacitance

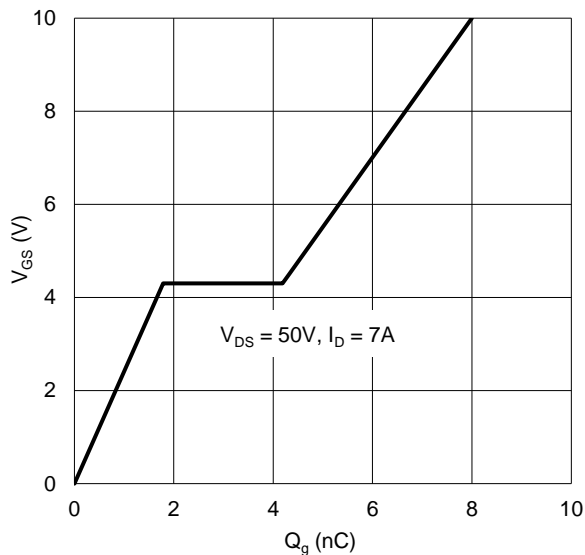


Figure 11. Gate Charge

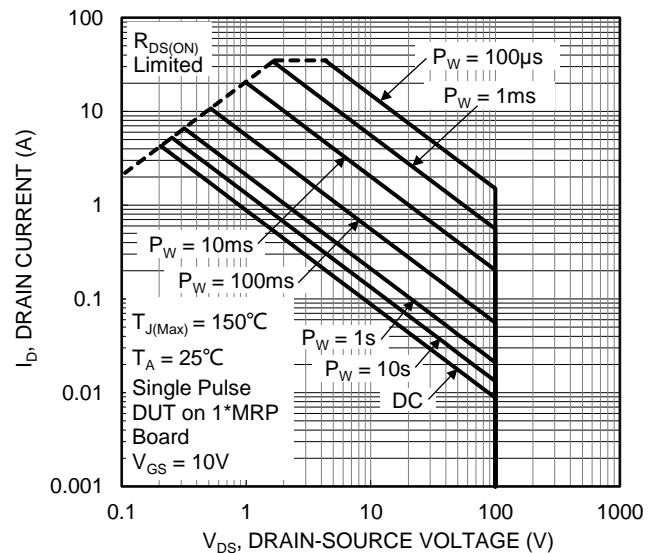


Figure 12. SOA, Safe Operation Area

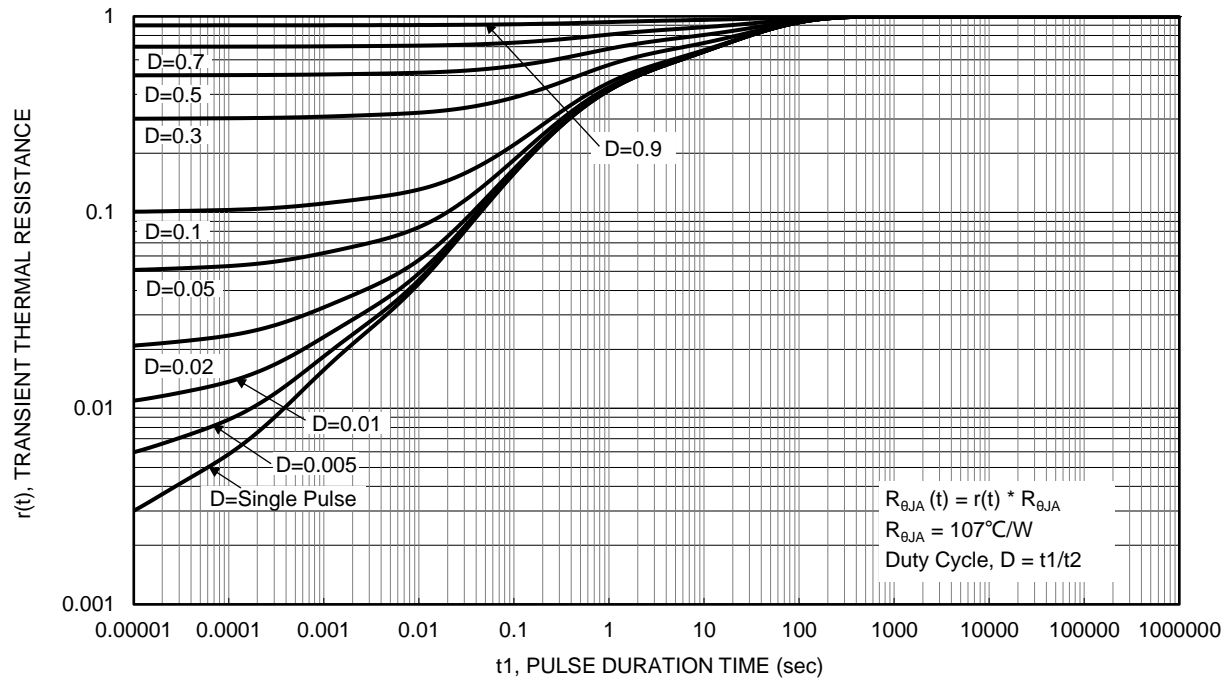
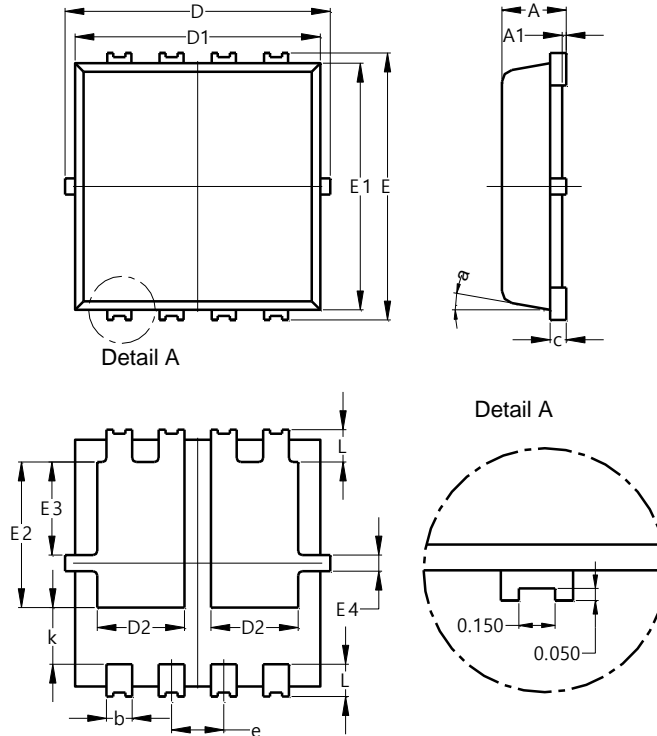


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

POWERDI®3333-8/SWP (Type UXD)

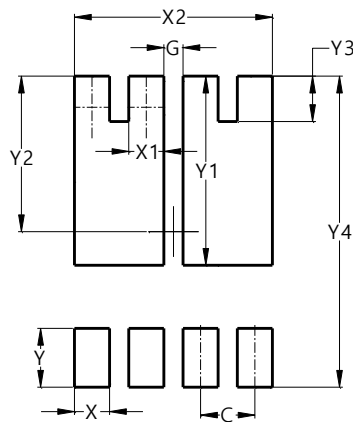


POWERDI®3333-8/SWP (Type UXD)			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	—
b	0.25	0.40	0.32
c	0.10	0.25	0.15
D	3.20	3.40	3.30
D1	2.95	3.15	3.05
D2	1.00	1.20	1.10
E	3.20	3.40	3.30
E1	2.95	3.15	3.05
E2	1.60	2.00	1.80
E3	0.95	1.35	1.15
E4	0.10	0.30	0.20
e	—	—	0.65
L	0.30	0.50	0.40
k	0.50	0.90	0.70
a	0°	12°	10°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

POWERDI®3333-8/SWP (Type UXD)



Dimensions	Value (in mm)
C	0.650
G	0.230
X	0.420
X1	0.420
X2	2.370
Y	0.700
Y1	2.250
Y2	1.850
Y3	0.540
Y4	3.700

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