



DMN10H220LFDF

Product Summary

BV _{DSS}	Rds(on) Max	I _D Max T _A = +25°C
1001/	225mΩ @ V _{GS} = 10V	2.2A
100V	290mΩ @ V _{GS} = 4.5V	1.9A

100V N-CHANNEL ENHANCEMENT MODE MOSFET

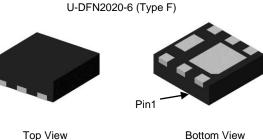
Features and Benefits

- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/guality/product-definitions/

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

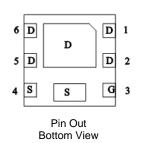
Load Switch

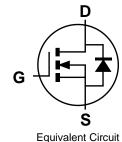


Bottom View

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4)
- Weight: 0.0065 grams (Approximate)





Ordering Information (Note 4)

Part Number	Case	Quantity Per Reel
DMN10H220LFDF-7	U-DFN2020-6 (Type F)	3,000
DMN10H220LFDF-13	U-DFN2020-6 (Type F)	10,000

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information

Date Code Key

Notes:

U-DFN2020-6 (Type F)

22	ΥМХ	

- 22 = Product Type Marking Code
- YWX = Date Code Marking
- Y = Year (ex: 0 = 2020)

W = Week (ex: a = Week 27; z Represents Week 52 and 53)

X = Internal Code (ex: U = Monday)

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	Н		J	K	L	М	N	0	Р	R	S
Week 1-26			27-52				53					
Code	A-Z			a-z			Z					
Internal Code	Sun Mon T		ue	We	d	Thu		Fri		Sat		
Code	Т		U		V	W		Х		Y		Ζ



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	Vdss	100	V	
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	T _A = +25°C T _A = +70°C	ID	2.2 1.7	А
Maximum Body Diode Forward Current (Note 6)	ls	2.2	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	8.8	А	
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)		lsм	8.8	А
Avalanche Current (Note 9)	L = 0.1mH	las	4.7	A
Avalanche Energy (Note 9)	L = 0.1mH	E _{AS}	1.1	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Dowor Dissinction (Note 5)	T _A = +25°C	D -	1.1	W
Total Power Dissipation (Note 5)	T _A = +70°C	PD	0.7	
Thermal Resistance, Junction to Ambient (Note 5)		RθJA	110	°C/W
Total Davier Disaination (Nata C)	T _A = +25°C	6	1.6	W
Total Power Dissipation (Note 6)	T _A = +70°C	PD	1.0	
Thermal Resistance, Junction to Ambient (Note 6)	R _{0JA}	80	°C M/	
Thermal Resistance, Junction to Case (Note 6)	Rejc	12	°C/W	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	100	_	_	V	$V_{GS} = 0V, I_{D} = 250 \mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	$V_{DS} = 100V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_		100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						·
Gate Threshold Voltage	VGS(TH)	1	—	2.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	Preven	_	174	225	mΩ	Vgs = 10V, ID = 2A
	RDS(ON)	_	217	290	mΩ	$V_{GS} = 4.5V, I_D = 1A$
Diode Forward Voltage	Vsd	_	0.8	1.3	V	$V_{GS} = 0V$, $I_{S} = 2A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	—	384	_		V _{DS} = 25V, f = 1MHz, V _{GS} = 0V
Output Capacitance	Coss	_	23	_	pF	
Reverse Transfer Capacitance	Crss	—	17	—		
Gate Resistance	Rg	_	2.4	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	3.7	_		
Total Gate Charge (V _{GS} = 10V)	Qg	_	6.7	_	nC	
Gate-Source Charge	Qgs	_	1.3	_		$V_{DD} = 50V, I_D = 1.6A$
Gate-Drain Charge	Q _{gd}	_	2	_		
Turn-On Delay Time	td(ON)		6.2			
Turn-On Rise Time	tR		8.7	_		$V_{DD} = 50V, V_{GS} = 4.5V,$
Turn-Off Delay Time	tD(OFF)		7.4		ns	$R_G = 6.8\Omega, I_D = 1.0A$
Turn-Off Fall Time	t⊨		4.2			
Body Diode Reverse Recovery Time	trr		20		ns	
Body Diode Reverse Recovery Charge	Qrr	_	11	_	nC	Is = 1.1A, dl/dt = 100A/μs

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

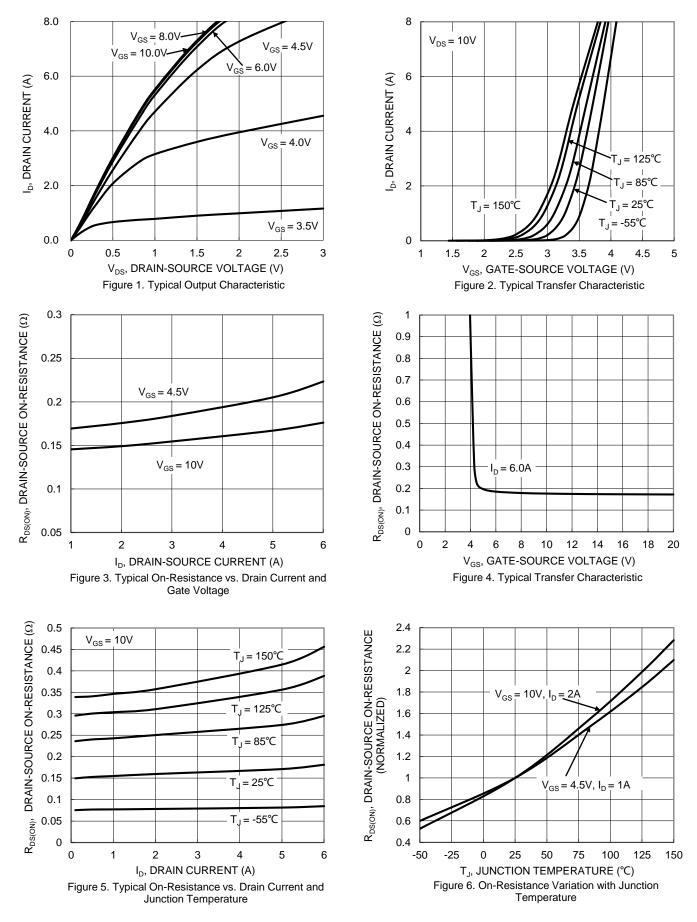
7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

9. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$.

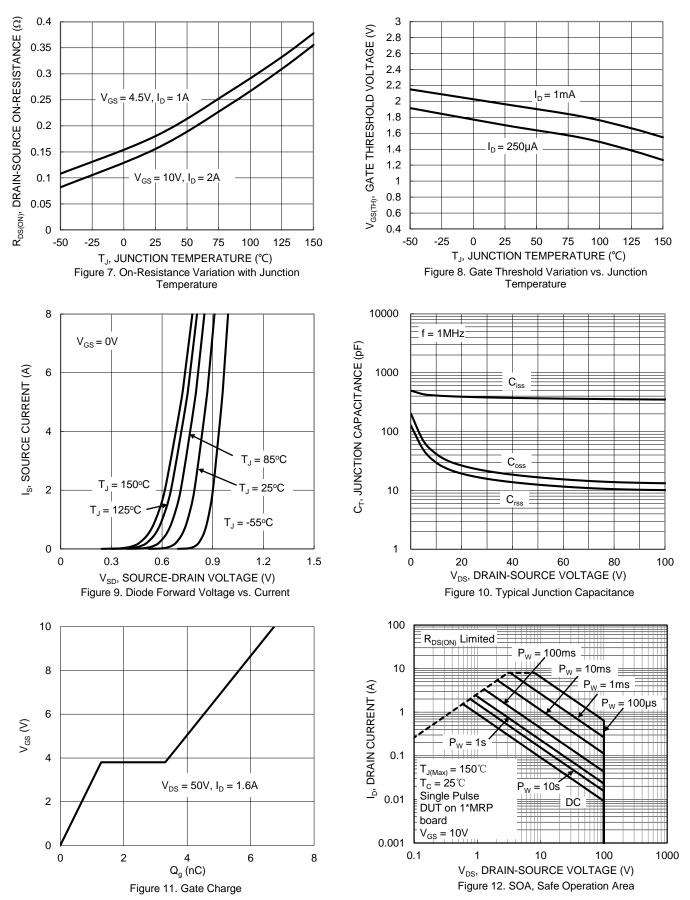


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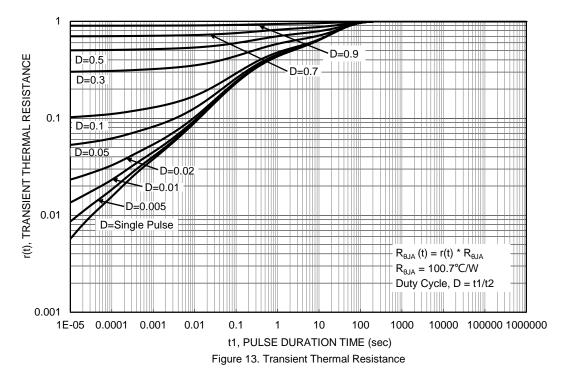


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DMN10H220LFDF Datasheet number: DS42084 Rev. 2 - 2

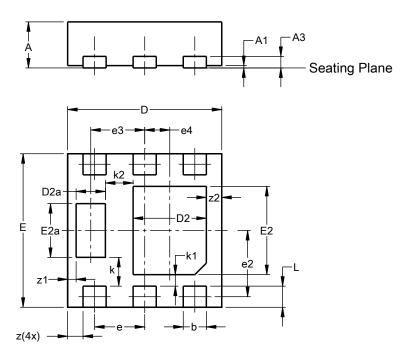






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

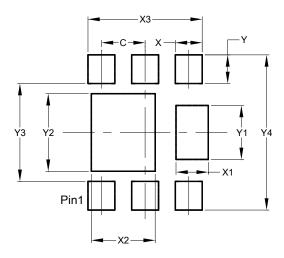


U-DFN2020-6 (Type F)							
Dim	Min	Max	Тур				
A	0.57	0.63	0.60				
A1	0.00	0.05	0.03				
A3	-	-	0.15				
b	0.25	0.35	0.30				
D	1.95	2.05	2.00				
D2	0.85	1.05	0.95				
D2a	0.33	0.43	0.38				
Е	1.95	2.05	2.00				
E2	1.05	1.25	1.15				
E2a	0.65	0.75	0.70				
e	0.65 BSC						
e2	0.863 BSC						
e3		0.70 BSC					
e4).325 BS					
k		0.37 BS					
k1		0.15 BS					
k2		0.36 BS	-				
L		0.325					
z		0.20 BS					
z1).110 BS					
z2		0.20 BS	-				
All D	Dimens	ions in	mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
С	0.650
Х	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300

U-DFN2020-6 (Type F)



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