



24V BIDIRECTIONAL TVS DIODE

Product Summary

V _{BR} (min)	IPP (max)	C _T (typ)
25V	5A	15.6pF

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players. The sidewall plating option of this package allows optical inspection after soldering reflow for easy and reliable quality control.

Applications

- Cellular Handsets
- Portable Electronics
- Computers and Peripheral



Bottom View

Features

- Low Profile Package (0.53mm max) and Ultra-Small PCB Footprint Area (1.08 x 0.68mm max) Suitable for Compact Portable Electronics
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- One Channel of ESD Protection
- Low Channel Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The D24V0LA1B2LPQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: X1-DFN1006-2
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 @
- Weight: 0.001 grams (Approximate)

X1-DFN1006-2



Device Schematic

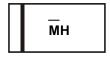
Ordering Information (Note 4)

Part Number Complian		Compliance Package		Reel Size	Tape Width (mm)	Packing	
Fait Number Com	Compliance Fackage		Marking	(inches)	rape widin (ililii)	Qty.	Carrier
D24V0LA1B2LPQ-7B	Automotive	X1-DFN1006-2	MH	7	8	10,000	Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3).compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



MH = Product Type Marking Code
Bar Denotes Cathode Side



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	P _{PP}	230	W	8/20µs, Per Figure 3
Peak Pulse Current	IPP	5	Α	8/20µs, Per Figure 3
ESD Protection – Contact Discharge	VESD_Contact	±30	kV	IEC 61000-4-2 Standard
ESD Protection – Air Discharge	V _{ESD_Air}	±30	kV	IEC 61000-4-2 Standard

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	PD	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	R _θ JA	500	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to +150	°C

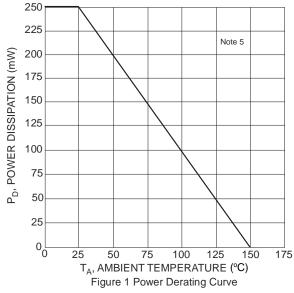
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

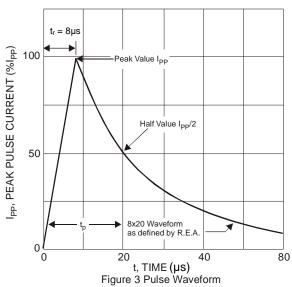
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Standoff Voltage	VRWM	_	_	24	V	_
Channel Leakage Current (Note 6)	I _{RM}	_	_	100	nA	V _R = 24V
	VcL	_		42	V	I _{PP} = 1A, t _P = 8/20μs
			1	46		$I_{PP} = 5A$, $t_P = 8/20 \mu s$
		_	31.4	_		$I_{TLP} = 16A$, $t_P = 100$ ns, Pin1 to Pin2
Clamping Voltage, Positive Transients		_	33.1	_		$I_{TLP} = 30A$, $t_P = 100$ ns, Pin1 to Pin2
		_	32.3	_		I _{TLP} = 16A, t _P = 100ns, Pin2 to Pin1
		_	34.2	_		I_{TLP} = 30A, t_P = 100ns, Pin2 to Pin1
Breakdown Voltage	V _{BR}	25	_	33	V	I _R = 1mA
Channel Input Capacitance	Ст	_	15.6	20	pF	V _R = 0V, f = 1MHz

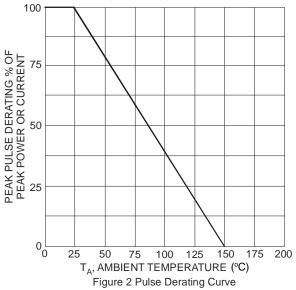
Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.
6. Short duration pulse test used to minimize self-heating effect.

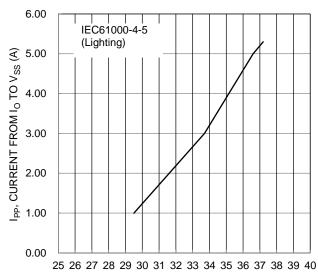




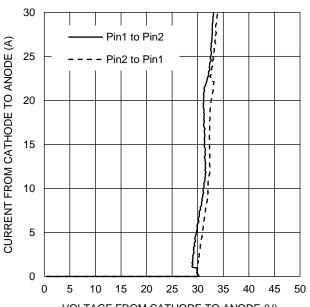








 $\rm V_{\rm C}$ FROM $\rm I_{\rm O}$ TO $\rm V_{\rm SS}$ (V) Figure 4 Clamping Voltage Characteristic



VOLTAGE FROM CATHODE TO ANODE (V) Figure 5 TLP Curve ($t_P = 100ns$)

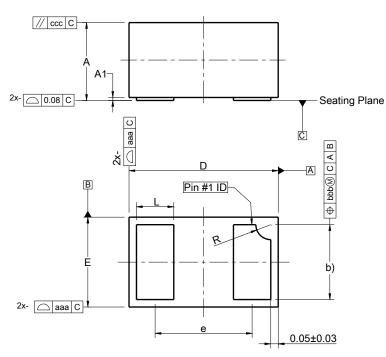
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

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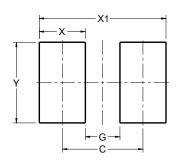


X1-DFN1006-2					
Dim	Min	Max	Тур		
Α	0.47	0.53	0.50		
A1	0.00	0.05	0.03		
b	0.45	0.55	0.50		
D	0.95	1.075	1.00		
Е	0.55	0.675	0.60		
е			0.65		
L	0.20	0.30	0.25		
R	0.05	0.15	0.10		
aaa	0.15				
bbb	0.05				
CCC	0.05				
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-2



Dimensions	Value (in mm)	
С	0.70	
G	0.30	
Х	0.40	
X1	1.10	
Y	0.70	



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