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# PCapØ1-EVA-Kit

Evaluation System for PCapØ1A

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# Content

1 Int	roduction	1-2
1.1	General	1-2
1.2	Component List	1-2
2 Co	nnecting Capacitors and Resistors	2-4
2.1	Capacitance Measurement	2-4
2.2	Temperature Measurement	2-5
2.3	Pulse Code Generation	2-6
2.4	Motherboard	2-6
3 Eva	aluation Software	
3.1	Installing the Software	
3.2	The Graphical User Interface	
3.3	Front Panel	
3.4	Front Panel Menus	
3.5	Special Windows	
3.6	First Measurement: Step-by-Step	3-27
4 As	sembler	4-1
4.1	Installing the Assembler	
4.2	Running the Assembler	4-3
5 Mi	scellaneous	5-1
5.1	Literature Guide	5-1
5.2	Document History	5-1
6 Ар	pendix	6-1
6.1	Schematics	6-1
6.2	Layout	6-3

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# 1 Introduction

### 1.1 General

The PCapØ1-EVA-KIT evaluation system provides a complete system for generally evaluating the PCapØ1 chip. It comprises of a main board, a plug-in module, a Windows based evaluation software, assembler software and the PICOPROG programming device. The PCapØ1 evaluation board is connected to the PC's USB interface through the PICOPROG V2.0 programming device.

The evaluation kit offers user friendly configurations for evaluating the PCapØ1 single-chip solution for capacitance measurement. This kit can be used to evaluate the capacitance measurement, temperature measurement and the pulse generation capabilities of the PCapØ1 chip.

For a proper use of the evaluation system it is strongly recommended to refer to the current PCapØ1A datasheets (DB\_PCapO1-O3O1, DB\_PCapO1\_DSP etc.). You can download these datasheets from <a href="https://www.acam.de/download-center/picocap">www.acam.de/download-center/picocap</a> .

### 1.2 Component List

•	PCapØ1-MB	Motherboard
•	PCO1-AD Plug-in module	Based on PCapØ1-AD in QFN32 package
•	PICOPROG V2.0	Programmer
•	High density DSUB15 cable	Connecting the Evaluation board to the
		programmer
•	USB cable	Connects PICOPROG V2.0 to the PC
•	Wall power supply unit	9 V
•	CD-ROM	Includes software and data sheets





Figure 1-1: Components of the evaluation kit

# 2 Connecting Capacitors and Resistors

This evaluation kit can be used for evaluating capacitance measurement by connecting capacitive sensors. Further, it can be used for evaluating temperature measurement by connecting external temperature sensitive resistors or for generating quasi analog voltage (pulse width/density modulated) that is dependent on the sensor connected to the system.

Depending on the purpose of evaluation, a modification has to be made to the same plug-in module. Following is a picture of the Mother board with the plug-in module.



Figure 2-1: The evaluation kit's motherboard and plug-in module

The following sections describe the modifications for each application in detail.

### 2.1 Capacitance Measurement

For the purpose of evaluating the capacitance measurement using PCapØ1, the plug-in module is

pre-assembled with ceramic capacitors to emulate capacitive sensors. These capacitors, each 47pF in value, are connected to the 8 ports PCO to PC7. They are connected as single sensors in floating mode, i.e. each capacitor is connected between 2 ports, and hence there are 4 x 47pF on-board capacitors. Please refer to Section 3.4 of the PCapØ1 Data Sheet for more information on how to connect capacitors to the chip. The capacitor connected between ports PCO and PC1 is taken as the reference capacitor.

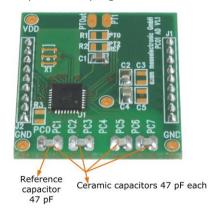


Figure 2-2: Details of the plug-in module



In the process of evaluation, when you are comfortable with interpreting the measurement results from the chip, these fixed capacitors can be replaced with the actual capacitive sensors of your application.

If you want to connect your capacitive sensors in grounded mode, then GND points are provided at the two ends of the module, where the sensor ground connections ought to be soldered.

The typical value of the capacitive sensors that can be connected to the evaluation kit lies in the range of 30pF to 3.5nF. The reference capacitor should be in the same order of magnitude as the sensor. Depending on the value of the sensor, the value of the internal resistor for performing the measurement has to be selected. For the pre-assembled 47pF capacitors, an internal discharge resistor of 180kOhm works well. See section 3.5 of the PCapØ1 data sheet on how to select the value of the internal discharge resistor.

### 2.2 Temperature Measurement

Temperature measurement or other resistive tasks may also be of interest for the user of this kit. The evaluation kit offers this possibility through the RDC (resistive-to-digital converter) ports. An on-chip thermistor coupled with an on-chip temperature-stable reference resistor made of polysilicon is sufficient for observing the temperature measurement capability of the PCapØ1 chip.

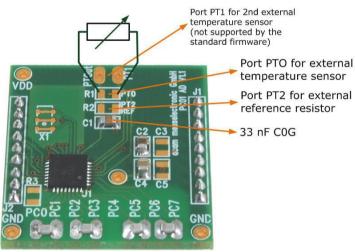


Figure 2-3: Temperature sensor connection pads

However, there is a possibility to connect the reference resistor and the thermistor externally to the chip, too. In case of external resistors, the temperature-stable reference resistor ought to be connected at port PT2REF on the plug-in module. The module allows you to connect the external thermistor, e.g. a PT1000 sensor at port PT0 (or PT1, not supported yet by the standard firmware).

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In any case, for the temperature measurement, an external capacitor 33 nF COG has to be connected to the chip; it is already pre-assembled on board.

### 2.3 Pulse Code Generation

Any of the capacitance or temperature measurement results from the PCapØ1 chip can be given out as a pulse width modulated or pulse density modulated signal. This output can be filtered to generate an analog output signal that can be used for further controlling.

These pulse width or pulse density codes can be generated at Ports PGO, PG1, PG2 or PG3. Since ports PGO and PG1 are used for the SPI Interface in the module, the hardware allows to get a valid pulse width/density modulated signal on PG2 or PG3. However, when I2C communication mode is used the pulsed signals can be optionally obtained on the ports PGO and PG1.

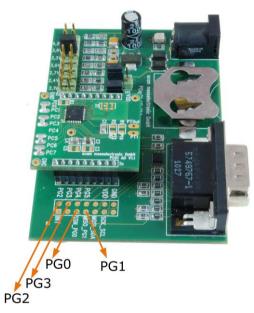


Figure 2-4: General purpose interface ports PGO to PG3

### 2.4 Motherboard

2-6

The motherboard connects to the PICOPROG programmer. It serves the various power options. It can be powered via wall plug supply, the voltage being set from 1.8V to 4.5V by jumpers. Further, it supports a battery power option. Power present is indicated by a green LED.

There is a jumper 'Current' on the mother board. The current consumption of the PCapØ1 chip during operation can be directly measured from these jumper terminals.

All interface signals and general purpose I/O signals can be monitored by means of a separate jumper.



# 3 Evaluation Software

3.1	Inst	Installing the Software		
3.2	The	The Graphical User Interface		
3.3	3.3 Front Panel			
3.3	3.1 Measurement Page			
3.3	.2	Capacitance Page		
3.3	.3	Temperature Page		
3.3	.4	Coefficients for Temperature Linearization		
3.3	.5	PWM / PDM Page 3-14		
3.3	.6	GPIO Page		
3.3	.7	Expert Page		
3.4	Fro	nt Panel Menus		
3.4	.1	File Menu		
3.4	.2	Application Menu		
3.4	.3	Tools Menu		
3.4	.4	Help Menu 3-20		
3.5	Spe	cial Windows		
3.5	.1	OTP/SRAM Communication Window 3-21		
3.5	.2	Diagnostics Window		
3.5	.3	Graph Window		
3.5	.4	Registers Window 3-24		
3.5	.5	Parameter Window		
3.6	3.6 First Measurement: Step-by-Step			

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## 3 Evaluation Software

### 3.1 Installing the Software

The PCapØ1 -EVA software runs under the following operating systems

- Windows 2000
- Windows XP
- Windows Vista (please use the software/drivers for Windows 7)
- Windows 7

Please follow the described procedure to install the software and driver:

**NOTE:** Ensure that the Picoprog V2.0 programmer is disconnected before starting the procedure.

The steps are slightly different for Windows XP and Windows 7. Please note as follows.

### Installation on Windows 7 systems:

- Install the PCapØ1 Evaluation software from Software\ PCapO1-Evaluation-Software\ setup.exe from the CD. This .exe file installs the evaluation software and the respective drivers for the Picoprog programmer too.
- At the end of step 1, a batch file comes up in a separate window. Eventually, it will ask you to unplug the Picoprog V2.0 programmer. Please follow further the instructions on the window.
- The driver will be installed; a windows message may pop up asking you to install the driver though it is not signed. Please install it anyway.
- The batch file asks you to plug in the programmer and it is now enumerated.
- You will be asked to restart the system at this point, please do so. After restart connect the evaluation board to the Picoprog programmer.
- On restart, launch the application from the start menu, if possible as administrator. The software opens and a pop up window asks to select between a 'Standard' mode and 'Humidity'. Please select 'Standard'.
- The LED on the Picoprog programmer will turn green. The software will initialize.
- Perform Verify Interface from the Eval.Software main window. If every is correctly installed, the verify interface must result in an 'Interface OK' message. Also, under Help → Programmer → hdIUSB → USBO::Ox194E::Ox100B::NI-VISA ought to be seen.



 If you want to change from the default SPI to I2C interface, please select under *Tools* → *Interface* → *I2C interface*. The LED on the Picoprog programmer should now turn red.

### Installation on Windows XP systems:

- Install the PCapØ1 Evaluation software from Software\ PCapØ1-Evaluation-Software\ setup.exe from the CD. This .exe file installs the evaluation software and the respective drivers for the Picoprog programmer too.
- At the end of step 1, a batch file comes up in a separate window. It will ask you to reconnect the Picoprog V2.0 programmer.
- You will then be asked to restart the system at this point, please do so.
- On restart, Found New Hardware wizard comes up. Please select 'No, not this time' and continue further with 'automatic installation'.
- The driver will be installed; a windows message may pop up asking you to install the driver though it is not signed. Please install it anyway.
- Now, launch the evaluation software application from the start menu. The software opens and a pop up window asks to select between a 'Standard' mode and 'Humidity'. Please select 'Standard'.
- The Found New Hardware wizard comes up again. Please select again 'No, Not this time' and continue further with 'automatic installation'.
- A message would pop up warning that the driver is not digitally signed. Please install the driver anyway.
- Now the driver installation is complete.
- The LED on the Picoprog programmer will turn green. The software will then initialize and ask you to connect the Evaluation board to the Picoprog programmer.
- Perform Verify Interface from the Eval.Software main window. If every is correctly installed, the verify interface must result in an 'Interface OK' message. Also, under Help → Programmer → hdlUSB → USBO::Ox194E::Ox100B::NI-VISA ought to be seen.
- If you want to change from the default SPI to I2C interface, please select under *Tools* → *Interface* → *I2C interface*. The LED on the Picoprog programmer should now turn red. When the LED does not glow at all, then it indicates that the interface is faulty.

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### 3.2 The Graphical User Interface

The software comes up with a small window offering selections for the operating mode. In general, start with the standard mode as it offers all options. The humidity option simplifies and specifies the displays for the humidity firmware. For details please see the datasheet for the humidity evaluation kit.

	Mode	select:
Sta	ndard	Humidit
	Load Value	s
	Presets	

Figure 3-1: Mode selection

Next, the main front panel comes up. Overall, the graphical user interface offers various windows for on-line configuration, for parameter and calibration data setting, and of course for the graphical and numerical display of the measurement data. The various windows will be explained in this chapter.

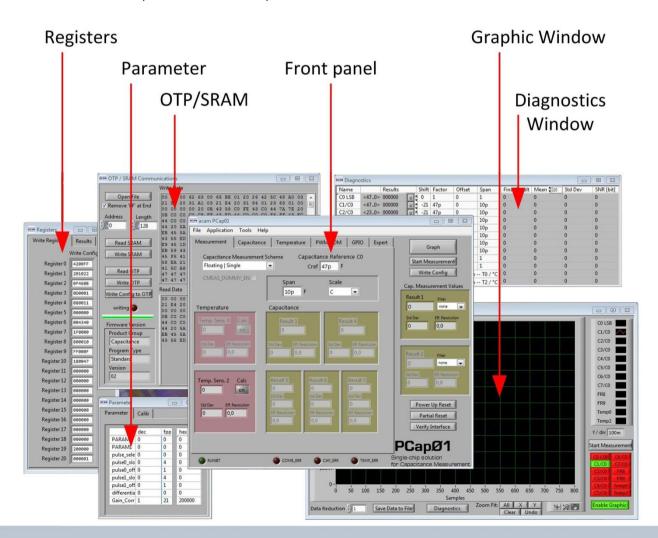




Figure 3-2: GUI Overview

### 3.3 Front Panel

This is the major window. On the right side, the front panel shows six general buttons:

Graph	Open a window for graphic representation of measurement data
Start Measurement	Start or stop a running measurement
Write Config.	Transfer once more, the present settings in the evaluation software to the chip (in case of doubt)
Power up Reset	After Power up reset, 'Write Config.' may be necessary.
Partial Reset	With a partial reset, the chip is re-initialized with respect to its frontend and processor.
Verify Interface	When everything is in order, then pressing this button will confirm if an SPI / I2C interface is present. It also indicates the release version number of the software.



### 3.3.1 Measurement Page

Enter The Application Tools Help		
Measurement Capacitance Capacitance Measurement Sch Grounded   Single CMEAS_DUMMY_EN	Temperature     PWM/PDM     GPIO     Expert       neme     Capacitance Reference CO       Cref     47p     F       Span     Scale       10p     F     C	Graph Start Measurement Write Config Cap. Measurement Values Result 1 Filter
Temperature Temp. Sens. 0 Calc 0 on Std Dev Eff. Resolution 0 0,0	Capacitance       Result 3     Result 4       0     0       Std Dev     Eff. Resolution       0     0,0	O     none       Std Dev     Eff. Resolution       O     0,0         Result 2     Filter       O     none
Temp. Sens. 2 Calc 0 on Std Dev Eff. Resolution 0 0,0	Result 5Result 6Result 7000Std Dev0Std Dev000Eff. Resolution0,00,00,00,00,0	Std Dev Eff. Resolution 0 0,0 Power Up Reset Partial Reset Verify Interface
		PCapØ1 Bingle-chip solution or Capacitance Measurement

Figure 3-3: Measurement page

Capacitance Measurement Scheme	Grounded   Single – Single capacitive sensor connected between a port and ground. Grounded   Differential- Differential capacitive sensor connected between 2 ports with the middle tap of the sensor connected to ground. Floating   Single – Single capacitive sensor connected between 2 ports. Floating   Differential – Differential capacitive sensor connected between 2 ports with the middle tap of the sensor connected to enotion 2 ports.
	another 2 ports. Please see Section 3.4 of PCapØ1 data sheet for more details.



Capacitive Reference CO	Reference capacitance value. This setting has no effect on the chip itself; it is purely a visual aid that helps to interpret the measurement results better.
Span	Maximum span of the reference capacitive sensor connected. This setting has no effect on the chip; it is only used for scaling the "Eff. Resolution" indication.
Capacitance	These fields with an olive green background display the measurement result at capacitive ports 3-7 provided these have been enabled on the 'Capacitance' sheet.
Temperature	These fields with a pink background display the measurement result at each temperature measurement port that has been enabled on the 'Temperature' Sheet. 'on' Button : Pressing the on Button in this part of the sheet comes up with a sub window. This helps to visually manipulate the display of the temperature measurement result – either to display the actual measurement value from chip or to display the temperature directly in Celsius, Fahrenheit or Kelvin (or any other scale) by using polynomial approximation. See section 3.2.4 for further details.
Cmeas_dummy_en	Some differential sensors (MEMS) require mirror symmetry with respect to the charges applied on the plates. This is ensured by "dummy charging" and is activated by this option.



### 3.3.2 Capacitance Page

🚎 acam PCap01	
File Application Tools Help	
Measurement     Capacitance     Temperature     PWM/PDM     GPIO     Expert       Cap. Port Select     Stray Compensation     Discharge Resistance       7     5     3     1     0	Graph Start Measurement Write Config
Port Error Cycle Control LF Clock 50kHz Cycle Time 20u s (Sample Size) CMEAS_CYTIME 0 CMEAS_FAKE 0 COnversion Time 40,0us	Cap. Measurement Values          Result 1       Filter         0       none         Std Dev       Eff. Resolution         0       0,0
Cap. Trigger Select ext. Trigger-Pin Sequence Timer  DSP_IN0 Sequence Control	Result 2 Filter O none Std Dev Eff. Resolution O 0,0
Sequence Time 13 New Measurement every: 328ms Measuring Rate 3,05Hz	Power Up Reset Partial Reset Verify Interface
	PCapØ1 Single-chip solution for Capacitance Measurement

Figure 3-4: Capacitance page

Options on 'Capacitance' page:

Cap. Port Select	Select which capacitive ports have to be measured (Ports 0-7), i.e. at which ports the sensors have been connected in hardware.
Stray compensation	<ul> <li>Internal: One additional measurement performed through only the chip-internal stray capacitance with respect to ground.</li> <li>External: One additional measurement per port pair, performed through a parallel connection of the capacitance at the two ports with respect to ground.</li> <li>Both: Both internal and external compensation together.</li> <li>None: No compensation</li> </ul>



Discharge Resistance	Selects the value of the internal resistance through which the discharge cycles during measurement are to be performed. This value has to be selected in accordance with the capacitance value of the sensor. Please see Section 3.5 of PCapØ1data sheet for more details.
Cycle Time	Can be set in multiples of 20 µs that corresponds to one Cmeas_cytime that is displayed below this box.
C_AVRG (Sample size)	Enables averaging the measurement results over multiple measurement cycles. Setting to 1 $\rightarrow$ No averaging, Setting to any number N, will result in averaging over N measurement cycles for generating one measurement result. 8192 maximum.
CMEAS_FAKE	Number of fake measurements per measurement cycle. Performing fake measurements may help in reducing noise.
Conversion Time	Displays the entire conversion time per measurement, taking into account, the number of ports opened and the the cycles for compensation and fake measurements.
Cap. Trigger Select	Selects the source that triggers the start of a capacitance measurement <b>Single</b> – Started by SPI Command Ox8C (Expert > Capacitance page) <b>Continuous</b> – Continuous measurement, self-triggering. Recommended when no temperature measurement is made in parallel. <b>Sequence timer</b> – Depending on the setting in the 'Sequence control' panel. Generally recommended setting $\rightarrow$ less prone to error conditions. <b>Pin triggered</b> - Triggered by external Pin, selectable from option ext.Trigger- Pin
ext. Trigger-Pin	Used to select the pin to be used as the source of trigger for the capacitance measurement. NOTE: In the delivered EVA module, the pins DSP_INO and DSP_IN1 are part of the SPI communication interface, hence only DSP_IN2 and DSP_IN3 selections are relevant.
Sequence Control :	When the timer is set to N, the capacitive measurement is triggered once every [2^(N+1) $*$ 20] $\mu s$
New Measurement begins every	Displays the rate at which the capacitive measurement is triggered based on the setting of the Sequence timer. It includes the conversion time and the pause time before the beginning of the next cycle
Measuring rate	Displays the frequency at which capacitive measurement data (with fakes and with averaging) is transferred from the DSP to the interface (SPI or I2C).



### 3.3.3 Temperature Page

acam PCap01	
File Application Tools Help	
Measurement Capacitance Temperature PWM/PDM GPIO Expe	rt Graph
Temp. Sens. 0 (R0)Temp. Sens. 1 (R1)Temp. Sens. 2 (R2)noneAlu   internal	Start Measurement Write Config
Temp. Reference (Rref) Poly   internal Cycle Control Cycle Time 280us T_AVRG 1 (Sample Size) 1 TMEAS_FAKE 2	Cap. Measurement Values Result 1 46,879p Std Dev Eff. Resolution 1,732f 12,5
Conversion Time 1,12ms Sequence Control Temp. Trigger Select CMEAS Triggered  New Measurement every: 5,12ms	Result 2 Fiter 3750 none  Sto Dev Eff. Resolution 0 Inf
TMEAS_TRIG_PREDIV	Power Up Reset Partial Reset Verify Interface
ext. Trigger-Pin DSP_IN0   DSP_STARTONTEMP  Temperature Start	PCapØ1
ORUNELT OCCMB_ERR OCAP_ERR TEMP_ERR	Single-chip solution for Capacitance Measurement

Figure 3-5: Temperature page

Options on 'Temperature' page:

Temp.Sens.O (RO)	To select a thermistor connected to port PTO for temperature measurement. This could be e.g. an external PT1000.	
Temp.Sens.1 (R1)	To select a thermistor connected to port PT1 for temperature measurement*	
Temp.Sens.2 (R2)	2 (R2) To select either the internal aluminum thermistor or an external reference resistor at port PT2for temperature measurement.	
Temp. Reference (Rref)	To select either the internal Poly or external resistor at port PT2 as the reference resistance to be used in temperature measurement.	



Cycle Time	Can be set to 140 $\mu s$ or 280 $\mu s$ . 280 $\mu s$ is recommended.
T_AVRG (Sample size)	Enables averaging the measurement results over multiple measurement cycles. Setting to $1 \rightarrow No$ averaging, Setting to any number N, will result in averaging over N measurement cycles for generating one measurement result.
TMEAS_FAKE	Number of fake measurements per temperature measurement cycle
Conversion Time	Displays the entire conversion time per measurement, taking into account, cycles for averaging and fake measurements.
Temp. Trigger Select	<ul> <li>Selects the source that triggers the start of a temperature measurement</li> <li>Off / Opcode triggered: Default setting when no temperature measurement has to be performed automatically. In this case, a temperature measurement can still be started by SPI Command Ox8E. The SPI Command can be sent by pressing the button 'Temperature Start'</li> <li>CMEAS triggered: A temperature measurement is triggered every time when a capacitance measurement is complete.</li> <li>&gt; Recommended setting for working with temperature measurements.</li> <li>Timer triggered: Depending on the setting in the 'TMEAS_TRIG_PREDIV' counter in the Sequence Control panel. This counter is steps up in 20 μs steps. Not recommended, not supported by standard firmware.</li> <li>Pin triggered: Triggered by external Pin, selectable from option ext. Trigger-Pin</li> </ul>
ext. Trigger-Pin	Used to select the pin to be used as the source of trigger for the temperature measurement.
	NOTE: In the delivered EVA module, the pins DSP_INO and DSP_IN1 are part of the SPI communication interface, hence only DSP_IN2 and DSP_IN3 selections are relevant.
DSP_STARTONTEMP	are part of the SPI communication interface, hence only DSP_IN2

### Typical configurations are:

1. No temperature measurement:

Set Temp.Trigger Select to 'Off / Opcode triggered'.

2. Internal temperature measurement:

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Setting like in figure 3-4. Temp.Sens.2 = Alu | internal, Temp.Reference = Poly | internal, Temp.Trigger Select to 'CMEAS triggered'.

3. External sensor, internal reference:

Temp.Sens.O = PTO | external, Temp.Reference = Poly | internal, Temp.Trigger Select to 'CMEAS triggered'. The external sensor has to be connected at pads R1.

4. External sensor, external reference:

Temp.Sens.O = PTO | external, Temp.Reference = PT2REF | internal, Temp.Trigger Select to 'CMEAS triggered'. The external sensor has to be connected at pads R1, the external reference resistor has to be connected at pads R2.

### 3.3.4 Coefficients for Temperature Linearization

When you connect a temperature sensor at Port PTO for temperature measurement, then you can set the coefficients of the 3rd degree polynomial that is used to linearize the temperature measurement within the evaluation software.

When you configure the Temp.Sens.O to PTO on the 'Temperature' sheet, then on the 'Measurement' sheet, the 'on' button in the box with the pink background is enabled. There are two boxes as shown in figure 3-6, the top box is to feed coefficients for the temperature sensor if connected at port PTO and the bottom box is to feed coefficients for the internal temperature sensor at port PT2, if selected.

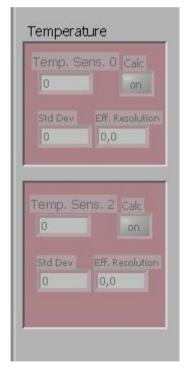


Figure 3-6: Temperature linearization

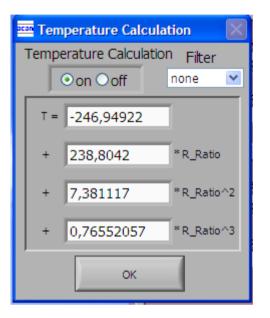
Click on the 'on' button to feed in the coefficients. For e.g. when an external temperature sensor is connected to Port PTO, then click on the off button on the top. The window figure 3-7 pops up.



Here you can feed in the coefficients of the 3rd degree polynomial. The coefficients displayed by default are for a PT1000 sensor. Change them according to the sensor you use. You can additionally also choose a filter to be applied to temperature measurement result in software. Finally select the 'on' option at the top and click OK. If you do not want the result to be linearized at all, then select the 'off' option.

In case you use the internal aluminum sensor for temperature measurement, then click on the 'on' button in the bottom window. The window in figure 3-8 pops up.

-



Temperature Calculation Filter	
⊙on Ooff	none 💌
T = -257,57	
+ 351,26	*R_Ratio
+ 0	*R_Ratio^2
+ 0	*R_Ratio^3
ок	

Figure 3-7: Temperature linearization coefficients, e.g. PT1000

Figure 3-8: Temperature linearization coefficients, e.g. internal aluminum sensor

The coefficients to linearize the temperature measurement when using the internal aluminum sensor are set by default. You just have to enable it by selecting the 'on' option at the top. If you do not want the result to be linearized, then select the 'off' option.



### 3.3.5 PWM / PDM Page

🚥 acam PCap01		
File Application Tools Help		
Measurement Capacitance Temperatur Pulse-Code Generators Pulse Interface 0	e PWM/PDM GPIO Expert	Graph Start Measurement
Pulse Interface Enable off Signal Source none Slope Offset 0 0	Pulse Interface Enable off Signal Source none Slope Offset 0 0	Write Config Cap. Measurement Values Result 1 Filter O none C Std Dev Eff. Resolution O 0,0
Resolution Clock Select	Resolution Clock Select 10 bits 💽 LF_X2 💽	Result 2 Filter 0 none Std Dev Eff. Resolution 0 0,0
		Power Up Reset Partial Reset Verify Interface PCapØ1
	B_ERR 🕐 CAP_ERR 💮 TEMP_ERR	Single-chip solution for Capacitance Measurement

Figure 3-9: PWM/PDM page

Options on 'PWM / PDM' Page:

Pulse Interface Enable	Select the pulse interface – Pulse Width Modulated Output (PWM) or Pulse Density Modulated (PDM) Output. Of the two, the PDM is the recommended interface. With PWM option, 100 kHz clock and 10-bit resolution the resulting PWM output frequency = (100 kHz / 1024) ~ 100 Hz.	
Signal source	Select the measurement result which has to be given out as pulsed output – any of the capacitance or temperature measurement results.	
Slope	Used to set the slope (m) of the linearization function used to scale	



	the range of the PWM / PDM output generation. See Section 4.2 in PCapØ1 data sheet for more details.
Offset	Used to set the Offset (b) of the linearization function. This value determines the range of the PWM / PDM output in the y direction. See Section 4.2 in PCapØ1 data sheet for more details.
Resolution	Resolution of the output in bits. This resolution also determines the pulsed output range.
Clock_select	Selects the clock frequency to be used for the PWM/PDM generation. Recommended setting LF_X2 $\rightarrow$ 100 kHz

These settings refer to the use of the standard firmware. The information set is saved in the parameter registers.



### 3.3.6 GPIO Page

🚥 acam PCap01	
File Application Tools Help	
File       Application       Tools       Help         Measurement       Capacitance       Temperature       PWM/PDM       GPIO       Expert         GPIO       Port Administration       PG0_X_G2       PG0_X_G2       PG1_X_G3       PG1_X_G3       PG0_Y_G3       PG1_Y_G3       PG1_Y_G3	Graph Start Measurement Write Config Cap. Measurement Values Cap. Measurement Values Result 1 Filter O none Std Dev Eff. Resolution O 0,0 Result 2 Filter
© RUNBIT OOMB_ERR O CAP_ERR TEMP_ERR	0       none         Std Dev       Eff. Resolution         0       0,0         Power Up Reset       Partial Reset         Verify Interface       Verify Interface         DCapØ1       Single-chip solution         for Capacitance Measurement

Figure 3-10 GPIO page

Options on 'GPIO' Page:

DSP_FF_IN	Pin mask for latching flip-flop activation (PGO to PG3)	
DSP_STARTPIN	Not supported by standard firmware The DSP can be started externally by a signal on a pin; these buttons select the pin that has to be sensed for detecting the start signal.	
PG_PULLUP	To enable the internal pull up on the ports PGO-PG3	
PG_DIR_IN	To configure the ports PGO-PG3 as input (otherwise output)	
PGO_X_G2	Possible only when the selected interface for communication is IIC.	



	Interchange PortGO with PortG2. This is useful when the Pulsed output is needed on Port PGO instead of PG2.
PG1_X_G3	Possible only when the selected interface for communication is IIC. Interchange PortG1 with PortG3. This is useful when the Pulsed output is needed on Port PG1 instead of PG3.
INT2PG2	Map the Interrupt output from chip, INTN to Port PG2. This setting is useful for 24 pin QFN package, because the dedicated INTN pin is absent in this version.
DSP_MOFLO	Activates anti-bouncing filter in PGO and PG1 lines

### 3.3.7 Expert Page

Please modify the settings on the Expert page only in consultation with acam Support team.

### 3.4 Front Panel Menus

### 3.4.1 File Menu

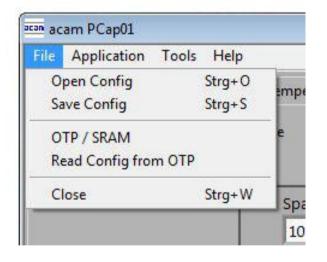


Figure 3-11: File Menu

Open Config	Open configuration file *.cfg that contains the content for the configuration and parameter registers. PCapO1_Initial.cfg is the one to start with when using the kit as delivered.
Save Config	Here you can save your own

	configuration.
OTP/SRAM	Opens the window to download the firmware. See section 3.5.1.
Read Config from OTP	Reads back the configuration in the OTP.
Close	Close the evaluation software

### 3.4.2 Application Menu

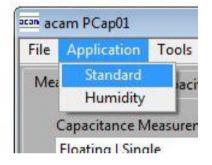


Figure 3-12: Application Menu

Standard	Sets the evaluation software in the standard display mode, as described in this datasheet.
Humidity	Sets the evaluation software in a special display mode dedicated to our humidity firmware. See the datasheet of the humidity evaluation kit for further information.



### 3.4.3 Tools Menu

File Application	Tools	Help		
Measurement	Run	Measurement	Strg+R	h
1	Gra	ph	Strg+G	
Capacitance M	Diag	gnostics	Strg+D	a
Floating   Sing		d Settings e Settings		
CMEAS_DUMN	Rea	d Results rface		•
Temperature	1.1	isters ameter	Strg+P	l

### Figure 3-13: Tools Menu

Run Measurement	Start the measurement
Graph	Opens the window for graphical display of the various measurement results (section 3.5.2)
Diagnostics	Opens the window for numerical display of the various measurement results (section 3.5.3)
Load Settings	Load the settings file *.set that contains the display information (labels, number of fractional digits e.t.c.)
Save Settings	Saves the current display settings.
Read Results	Single read of the result registers
Interface	Select between SPI and I2C interface
Register	Opens the Register window (section 3.5.4)
Parameter	Opens the Parameter/Calibration

### 3.4.4 Help Menu

File Application	Tools	Help	
Measurement	Capacit	He	Ip Contents F1
	Capacit	Pro	ogrammer
Capacitance	Measuren	Ab	out
Floating   Sin	gle		Cri

Figure 3-14: Help Menu

Help Contents	Opens the help window
Programmer	Opens the window with information about the NI VISA interface and the possibility for interface tests
About	Version



### 3.5 Special Windows

### 3.5.1 OTP/SRAM Communication Window

In the 'File' Menu you can select 'OTP/SRAM' and the following window pops up:

	Writ	- D-	4.47														
	Writ	e Da	ata											_			_
Open File	00	00	0.0	62	63	00	65	BE	01	20	26	42	5C	48	AO	03	1
Remove 'FF' at End	21	Ε4	20	31	A1	03	21	E4	20	31	84	01	23	63	01	00	1
	00	00	00	00	20	OB	43	58		FE	43	CO	44	7A	7E	20	1
Address Length	OB	CO	CO	CO	C8	FF	43	ED	44	CO	CO	CO	F6	FF	43	EC	
)×0 (+) d128	44	CO	CO	CO	C8	FF	41	23	94	DO	43	EE	44	D2	43	EF	L
J 120	44	20	5A	70	60	71	61	78	68	02	7A	F3	43	C7	FE	41	L
	EB	45	SA	21	DF	46	46	46	46	EC	44	CO	CO	CO	CB	FF	L
Read SRAM	43 E9	55 45	ED 1D	45	43	51	24	41	23	88	EA	45	F5 EB	41	23	88	L
THEOR BIO HT	EB	45	43	41	43	58	SC	21 A8	99	E9 C0	50 5A	46 EB	45	EB	A9 41	52 F2	L
Write SRAM	45	55 F6	41	23	88	EA	45	F7	41	23	88	E9	45	1F	41	43	L
	58	EA	21	99	00 E9	50	45	EB	44	23 A9	02	EB	40 59	43	CA	FE	L
<u>12 - 17</u>	41	100	AS	03	CO	50 5A	EB	45	EB	41	F3	45	02	40	FF	TT	Ŀ
Read OTP	47	47	47	47	47	47	47	47	70	70	45	41	02	47	47	47	Ŀ
	47	47	47	47	FO	1 2000	6D	45	1.27	60	7D	45	41	02	47	47	5
Write OTP	170	1			20	10	00	15	11	00	12	10		02	71	1/	
Write Config to OTP	Read					-		102700	-	100110	17:30		-		cie das		_
	1000	00	00	12.5	63		65			20	26	42		48		03	1
writing 🕘	21	E4	20	31	A1	03	21	E4	20	31	84	01	23	63	01	00	L
	00	00	00	00	20	OB	43	58	CO	FE	43	CO	44		7E	20	L
	OB	CO	CO	CO	C8	FF	43	ED	44	CO	CO	CO	F6	FF	43	EC	Ŀ
Firmware Version	44	CO	CO	CO	C8	FF	41	23	94	DO	43	EE	44	D2	43	EF	L
Product Group	44	20	5A	70	60	71	61	78	68	02	7A	F3	43	C7	FE	41	L
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	EB	45	5A	21	DF	46	46	46	46	EC	44	CO	CO	CO	CB	FF	Ŀ
Capacitance	43	55	20	45	EL	51	24	41	23	88	LA	45	F5	41	23	88	L
Program Type																	L
Standard																	L
Version																	L
02																	

Figure	3-15:	OTP/SRAM	Communication
--------	-------	----------	---------------

Open File	Select and open a firmware file (.hex). The content is shown in the 'Write Data' window.
Read SRAM	Pressing this button, the content of the SRAM is read and shown in the 'Read Data' window. In 'Address' and 'Length' you can specify how many bytes you want read, starting at which address.
Write SRAM	Writes the firmware into the chips SRAM. The status of the write process is indicated by the green bar. The successful end is indicated by a pop-up window. For verification we recommend to read back the SRAM afterwards and compare it with the source.

Read OTP	Pressing this button the content of the OTP is read and shown in the 'Read Data' window. In 'Address' and 'Length' you can specify how many bytes you want read, starting at which address.
Write OTP	Writes the firmware into the chips OTP. Attention: This option should be used only if you really want to test the OTP functionality. The status of the write process is indicated by the green bar. The successful end is indicated by a pop-up window. For verification we recommend to read back the OTP afterwards and compare it with the source.
Write Config to OTP	Writes to current content of the configuration registers into the reserved OTP space.
Firmware Version	In the firmware, a specific address is reserved to save 3 byte information about the application and the version of the software. The coding is specified in the pcapO1a.h library (see assembler)

### 3.5.2 Diagnostics Window

Name		Results		Shift	Factor	Offset	Span	Final Result	Mean 20	Std Dev	SNR [bit]
C0 LSB	<470>	0222D2	U	0	1	0	1	139,986k	139,993k	7,547	-2,916
C1/C0	<470>	207AD5	U		1	0	1	1,015	1,015	72,163u	13,758
C2/C0	<230>	000000	U	-21	47p	0	10p	0	0	0	Inf
C3/C0	<230>	000000	U	21	47p	0	10p	0	0	0	Inf
C4/C0	<230>	000000	U	-21	47p	0	10p	0	0	0	Inf
C5/C0	<230>	000000	U	-21	47p	0	10p	0	0	0	Inf
C6/C0	<230>	000000	U	-21	47p	0	10p	0	0	0	Inf
C7/C0	<230>	000000	U	21	47p	0	10p	0	0	0	Inf
FR8	<230>	800000	U	0	1	0	1	8	8	0	Inf
FR9	<230>	800000	U	0	1	0	1	8	8	0	Inf
Temp0	<230>	000000	U	200	erature Po	olynom on	T0 / °C	-246,949	-246,949	0	Inf
Temp2	<230>	1A5407	U	Temp	erature Po	olynom or	T2 / °C	31,432	31,415	20,313m	12,528

Figure 3-16: Diagnostics Window

The 'Diagnostics' window is used for real-time numerical display of the measurement results. In principal it shows the content of the read registers. The content itself depends on the firmware. Figure 3-16 shows the content as it is given with the standard firmware. The first eight rows show the capacitance ratios, the last two rows show the temperature result (resistance ratio or linearized temperature).

The 'Diagnostics' window has 12 columns of information, defining labels, data format, resolution specification (white background) and results (grey background). The information in the white fields increase convenience of reading and is stored in the setting files (\*.set).



All number may get a character to indicate the well known prefixes for denoting the factor in thousands ('p', 'f', 'a', 'k'...).

Name	Label for the register content, depends on the firmware.
Results	Raw hex data display of the result register content. The column before shows the width. The button column after shows whether the result is signed or unsigned.
Shift	This column shows the size of the fractional part of the fixed point number and the necessary shift. Depends on the fimware.
Factor	The factor is a scaling factor that allows to scale the result according to the reference capacitor. Factor = '1' gives back the initial capacitance ratio in column 'Final result' (see figure 3-16). '47p' scales the result to a 47 pF reference capacitor as it is assembled default on the evaluation board (see figure 3-17).
Offset	Offset to be added or subtracted in the evaluation software.
Span	Number that defines the maximum span of the sensor. Is relevant only for the calculation of the resultion in column SNR[bit]. Example figure 3-16b: The sensor has a base capacitance of 47 pF and varies by 10 pF. The 10 pF span are resolved with 11.4 bit.
Final Result	Display of the final result, scaled by 'Factor' and the 'Offset' added.
Mean	Display of the mean value. The sample size can be selected.
Std.Dev	Standard deviation of the 'Final Result'.
SNR	Signal-to-Noise ratio in bit, calculated as 'Span'/ 'Std.Dev.'

Name		Results		Shift	Factor	Offset	Span	Final Result	Mean \$20	Std Dev	SNR [bit]
CO LSB	<470>	0222C4	U	0	1	0	1	139,972k	139,964k	8,879	-3,15
C1/C0	<470>	207A9C	U	-21	47p	-700f	10p	47,003p	47,004p	3,601f	11,439
C2/C0	<230>	000000	U	-21	47p	0	10p	0	0	0	Inf
C3/C0	<230>	000000	U	-21	47p	0	10p	0	0	0	Inf
C4/C0	<230>	000000	U	-21	47p	0	10p	0	0	0	Inf
C5/C0	<230>	000000	U	100 C 100 C	47p	0	10p	0	0	0	Inf
C6/C0	<230>	000000	U	-21	47p	0	10p	0	0	0	Inf
C7/C0	<230>	000000	U	-21	47p	0	10p	0	0	0	Inf
FR8	<230>	800000	U	0	1	0	1	8	8	0	Inf
FR9	<230>	800000	U	0	1	0	1	8	8	0	Inf
Temp0	<230>	000000		202	erature Po	olynom on	T0 / °C	-246,949	-246,949	0	Inf
Temp2	<230>	1A59E8	U	Temp	erature Po	olvnom on	T2 / °C	31,684	31,693	14,762m	12,989

Figure 3-16b: Diagnostics Window



### 3.5.3 Graph Window

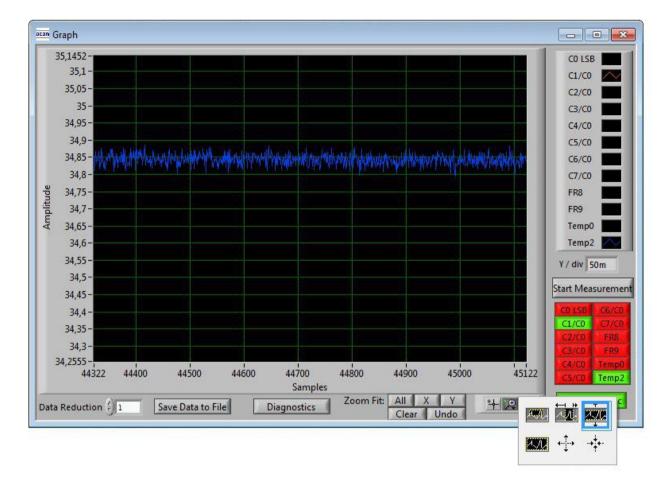


Figure 3-17: Graph Window

The data to be displayed are selected in the field at the bottom right. The labels in the buttons are the same as in the diagnostics window. To display data press the corresponding button so that it gets green. Below the graph are various options to zoom in/out, center or scale in other ways.

The data displayed can be stored into a text file. For long-term investigations it is possible to reduce the data displayed and stored. The field 'Data Reduction' allows to define the level of data reduction.

### 3.5.4 Registers Window

These windows display the configuration data in hexadecimal format as they are currently used. Also the result registers' content is shown in hexadecimal format, but updated only when the button is pressed. Finally, the various status bits are shown.

acan Registers	
Write Registers	Results
1	Write Config
Register 0	4200FF
Register 1	201022
Register 2	0F460B
Register 3	050001
Register 4	080011
Register 5	000000
Register 6	004340
Register 7	1F0000
Register 8	800010
Register 9	FF000F
Register 10	180047
Register 11	000000
Register 12	000000
Register 13	000000
Register 14	000000
Register 15	000000
Register 16	000000
Register 17	000000
Register 18	000000
Register 19	200000
Register 20	000001
]	

Write Registers	Results
	Results
Res 0 <470>	022206
Res 1 <470>	207A8D
Res 2 <230>	000000
Res 3 <230>	00000
Res 4 <230>	00000
Res 5 <230>	000000
Res 6 <230>	00000
Res 7 <230>	00000
Statusreg	100000
Res 8 <230>	000008
Res 9 <230>	000008
Res 10 <230>	000000
Res 11 <230>	1A5EC4
Statusreg	Read Results
Port Error	
CAP_ERR	SENSE_TES
MUP_ERR	Q_MODE
TEMP_ERR	TENDFLAG
	SEBORUN
PS_UNLOCKED	:0> 🔘 CYC_ACTIVI

Figure 3-18: Write and Read Registers

### 3.5.5 Parameter Window

This window shows the content of the parameter registers and, if supported, the calibration data. The content strongly depends on the firmware. The standard firmware does not use calibration data but e.g. the humidity firmware and pressure firmware do.

The parameter registers are used to scale the pulse output interfaces and to tell the firmware whether differential sensors are used.



# **PICO**CAP<sup>®</sup>

arameter	Calib		
-	dec	fpp	hex
PARAMO	0	0	0
PARAM1	0	0	0
pulse_sele	0	0	0
pulse0_slo	1k	4	3E80
pulse0_off	12	1	18
pulse1_slo	988	4	3DC0
pulse1_off	6	1	С
differentia	55	0	37
Gain_Corr	476,837n	21	1

Figure 3-19: Parameter Window

	dec	fpp	hex	Calibration Index
Calib1	-1	0	FFFFFF	Select PCap01
Calib2	-63,219k	0	FF090D	
Calib3	3,91257M	0	3BB378	
Calib4	3,86983M	0	3B0C87	No. of Elements
Calib5	1,60541M	0	187F25	
Calib6	-1	0	FFFFFF	(r) 22
Calib7	809,715k	0	C5AF3	
Calib8	-2,67351N	0	D73497	Read SRAM
Calib9	2,79085M	0	2A95C6	
Calib10	-914,256k	0	F20CB0	Write SRAM
Calib11	2,936k	0	B78	
Calib12	2,22298M	0	21EB85	Read OTP
Calib13	-531,838k	0	F7E282	
Calib14	-790	0	FFFCEA	Write OTP
Calib15	1,637k	0	665	
Calib16	512	0	200	
Calib17	2,619k	0	A3B	Recalibration Address
Calib18	1	0	1	20F
Calib19	21,76k	0	5500	
Calib20	-163,841k	0	FD7FFF	
Calib21	3,95264M	0	3C4FFF	
Calib22	-1,969k	0	FFF84F	

Figure 3-20: Calibration Window



### 3.6 First Measurement: Step-by-Step

**Step 1**: The first to do after starting the evaluation software is to download a firmware into the PCapØ1. In the initial phase start with our standard firmware that calculates the capacitance ratios and resistance ratios. It automatically recognizes the operation mode and takes care of the set number of capacitors and the kind of connection. But it does no further processing.

Open the 'File' menu and select OTP/SRAM. The following 'OTP/SRAM communications window' opens. If you press 'Open File' initially the content in the 'data'-folder of the PCapØ1 program folder is opened. There you find the firmware PCapO1\_standard.hex. Select this one.

	Writ	e Da	ta																		
Open File	00	00	00	62	63 0	0 65	BE	01	20	26	42	5C	48	AO	03						
Remove 'FF' at End	21 00	E4 00	20	31 00	00.000	3 21 B 43		20 C0	31 FE	84 43	01 C0	23 44	33	01 7E	00 20		-				
Address Length	2.2	CO	CO	CO	C8 F	F 43	ED	44	CO	CO	CO	F6	FF	43	EC		-				_
×0 (+) d128	44	1000	CO	CO		F 41	23	94		122	1000	44	D2	1.500	EF						
10 7/0128	44	20	5A	70	60 7	100.000	1.27		02		1222	43	13/10	1000	41		DM	CDIO	. 1	Francisco	1
	EB		5A ED	27	DF 4 EC 5	6 46 1 F4	1000	46	EC 88	1000	CO	CO FS	62252	2200	FF 88		DIVI	GPIO		Expert	IJ
Read SRAM	43 E9	22	10		Contraction of the	1 14 8 EA	1000	99	00 E9	50	375	EB			02						1
	1000	10.00	12.2	2000	45 5 FE 4	200	AB	- 20751	1000	5A	-329	45	22	1000	F2		eferer	nce CO	ł.		1
Write SRAM	45				88 E			41			E9	CORE			43		E				1
: 	100.00	EA	0.000	1000	10000	70 <u>7</u> 17	1000	12.12	A9	Contraction of	0.000	59		CA	2.2		1.1				I
Read OTP	12.2				C0 5	A EB	45	EB	41	F3	45	02	FF	FF	FF		122			-	I
Kead OTP	47	47 47 47 47 47 47 47 47 7C 7D 45 41 02 47 47 47					cale				1										
Write OTP	47	47	47	47	F0 7	C 6D	45	41	6C	7D	45	41	02	47	47	-		-			1
	Rea	d Da	ta														1	10 N			4
Write Config to OTP			(		100																
			903	n Op	en Fi	e															
writing 🕘			Suchen in:				data							-	0	-	1				
			Suchen In:					Ju udia								9	1 De	0	17		
Firmware Version			a					Name								Än	Änderungsdatum				
Product Group					a by			PCap01_humidity.hex									15.	15.09.2011 09:44			
Capacitance			Z	Lulet	zt bes	ucht	PCap01_standard.hex									31.	31.08.2011 14:36				
Program Type					100	uni_prg_v064.hex									30.	30.03.2011 13:13					
riogram type					-				-F00	2-0											100
				D	eskto	-															
Standard				0	ESKLO	P															
Standard Version																					
				10	-																

### Figure 3-21 File menu

Press 'Write SRAM' and wait till the green bar indicates that the write process is finished. For verification press 'Read SRAM' to check if the content is correctly written into the SRAM. In the 'Read Data' you should see the same content as in the first 128 byte of 'Write Data'.

**Step 2**: The next step you need to load a configuration file. This file contains the configuration registers content and the parameter registers content. Select in the 'File' menu item 'Open Config' and select the file PCapO1\_initial.cfg. This configuration fits to the evaluation board in its initial assembly with floating 47pF capacitors.

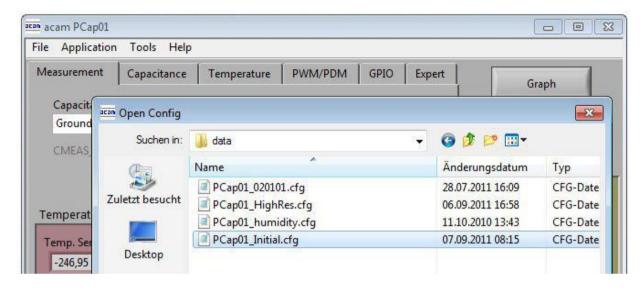


Figure 3-22: Load Configuration Data

**Step 3**: Finally, load the standard settings file to have a reasonable display in the Diagnostis window and Graph window.

e Applicati	on Tools Hel	p				
leasurement	Capacitance	Temperature	PWM/PDM	GPIO Exp	pert G	raph
Capaci Floatin	Load Settings					X
CMEAS	Suchen in:	📙 data		*	G 🤌 📂 🖽 •	
	(Ala	Name	*		Änderungsdatum	Тур
	27	PCap01_humid	ity.set		23.02.2011 09:45	SET-Datei
2	Zuletzt besucht	PCap01_standa	rd.set		07.09.2011 08:16	SET-Datei

Figure 3-23: Load Settings Data

Step 4: Open the Diagnostics window and Graph window and press 'Start Measurement'.



## 4 Assembler

4.1	Inst	alling the Assembler	4-2
4.2	Run	ning the Assembler	4-3
4.2.	. 1	Assembler Online Help	4-3
4.2.	2	Assembler Page	4-4
4.2.	3	Download Page	4-7
4.2.	.4	Assembler Settings	4-8



## 4 Assembler

#### Features

Platform:	Windows 2000, XP, Vista (32-Bit), Windows-7 (32 and 64)		
Type of Assembler:	Single Path Assembler		
Source-File:	*.asm, according to the PCapØ1 assembler syntax		
Target-File:	Dutput. hex, downloadable Hex-File		
Instruction:	<command/> [ <parameter1>, <parameter2>] For example: nop or add a, b or jCarC case1</parameter2></parameter1>		
Comment(s):	Single line comment:    ; Multi line comment: <comment> <endcomment></endcomment></comment>		
Includes:	<command/> [ <parameter1>, <parameter2>] For example: nop or add a, b or jCarC case1</parameter2></parameter1>		
Constants:	Constants can be defined with: CONST myConst8 15 The constants have to be declared before they are used for the first time.		
Number formatting:	Numbers can be written in decimal or hexadecimal notation e.g.: load a, 16355 or load a, 0x3FE3		
Jump labels:	To jump within the code, you can use jump labels together with the various conditional jump op-codes like jcd p1, jEQ p1 etc. E.g.: jCarC label1 label1: move a, b		
Addressing:	Addressing is automatically done by the assembler. Please note that the maximum user program size is 4kB (including all header files, library files and calibration banks).		
Subroutines:	Subroutines can be executed with the special opcode ,jsb' and ,jrt'.		

### 4.1 Installing the Assembler

Insert the CD-ROM. Run Software\PCapO1\_Assembler 2.0.3\setup.exe to install the program. Restart the computer if required. After the installation you will find in the START menu an item "PCapØ1 Assembler". Run this to start the assembler program.



### 4.2 Running the Assembler

In the START menu there is an item "PCapØ1 Assembler". Run this to start the assembler program.

### 4.2.1 Assembler Online Help

The following sections give just a short description of the assembler program. For a detailed description of the assembler software please use the online help of the program. The online help can be opened from the Menu or by pressing the F1 button.

The following help window opens up:

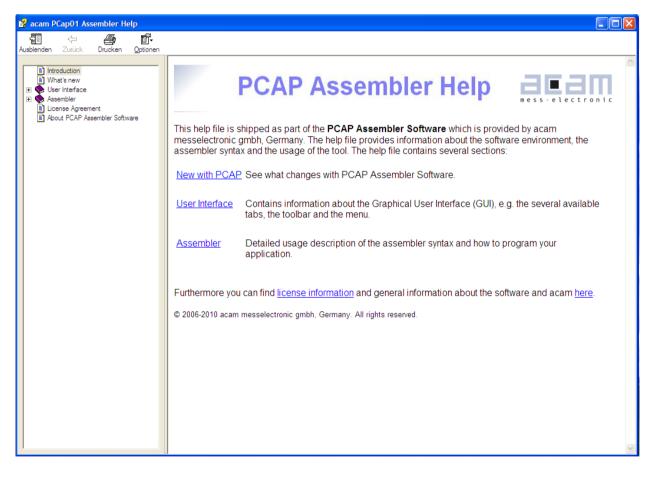
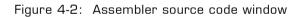


Figure 4-1: Integrated online help of PCapO1 Assembler



### 4.2.2 Assembler Page

acam	n PCap01	Assembler	- c:\Programme\acai	m-messelectronic	\PCap01\Assembler\examples\pressure sensor\PCap01_pressure.prj	
e <u>E</u> di	t <u>V</u> iew	<u>A</u> ssembler	<u>T</u> ools <u>H</u> elp			
° 1	6 🔓	] 🔏 🗊	🗊 🗟 🗞 🥱	2 🔊 🖉	😨 🚰 🛃 💌	
ы	PCap01	_pressure.as	m cdc_inv.h media	n.h rdc.h signe	d24_to_signed48.h standard.h pcap01a.h	
Assembler	001	3;	RES4: Capaci	tance measuren	ment ratio with 22 fd	
SSB	001	4;	RES5: a2 : Co	pefficient in	polynomial for linearization of pressure	
<	001	5;	RES6: a1 : Co	pefficient in	polynomial for linearization of pressure	
τ	001	6;	RES7: a0 : Co	pefficient in	polynomial for linearization of pressure	
log	001	7;	RES8: -			
Do <u>w</u> nload		8;	RES9: -			
		9;	RES10: -			
		0;	RES11: t_ref	/ t_meas valu	ue from Capacitance ratio measurement	
		1;				
					are that have to be adapted to the sensor are marked with	
					in the following source code.	
					ronic gmbh (c) 2010	
	002		1			
	002		lude <pcap01a.h:< td=""><td><b>&gt;</b></td><td>;</td><td></td></pcap01a.h:<>	<b>&gt;</b>	;	
		-			Addresses for Register Values	
	002				Addresses for Register Values	
			firstSigned	CalValue 9		
	003		j ti sto tynet			
	003	_				
		-	signed Co	alibration Val	lues Registers ( must be placed at ascending Ram-Addresses!)	
		5 CONST	tk0	0	; coefficient Temperature Polynom	
	003	6 CONST	tk1	1	; coefficient Temperature Polynom	
	003	7 CONST	tk2	2	; coefficient Temperature Polynom	
	003	8 CONST	tk3	3	; coefficient Temperature Polynom	
	003	9				
	004	0 CONST	pc32	4	; coefficient Pressure Polynom A2	
		c:∖Pro		voic\PCap01\Assemble	er\examples\pressure sensor\PCap01_pressure.asm	
	Compile	e 0. 110	gramme acamenesselectre	and a coport of settion	er waanpies presente senser in eepen_presente.com	Opcode-Help (F2)
			puration for prog Size: 2450 Byte	gram memory mo	odel not found, assuming single mode	<u>^</u>
	INFO:	c:∖Ṕrogra	umme∖acam-messele	ectronic\PCap(	01\Assembler\examples\pressure_sensor\PCap01_pressure.asm: Process	ing was
	succes	sful				
<u>cit</u>						
						<b>.</b>
	)					



The 'Assembler' tab is the main window to open all the source code and include files. It consists of a status message window at the bottom and buttons to operate the assembler. By clicking the green link labeled ,Opcode-Help (F2)' an integrated online help pops up which provides detailed information about the available op codes.

The controls in detail:

Source code window:	This window is used as the source code and include files editor for the assembler listings (*.asm & *.h files). This is a pure editor window; any number of tabs can be opened in this window. This page allows the user to display and edit any file. The files can be manually opened, saved and closed. The location of the file displayed in the window currently is displayed next to the 'Compile' button.
	Additional features are line numbering, syntax highlighting and integrated comment handling. Standard editor tasks like cut, copy &



paste or find & replace, etc. can be performed. You can modify the settings for the editor via the tool settings dialog. To know more about 'Include' files and how to use them, see section 4.2.2.1 below.         Assembler button:       The assembler button to compile the source code along with the respective header files is: Compile. On pressing the Compile button the source file is compiled to a downloadable hex code. If the compilation was successful a message is displayed accordingly. If the compilet on the source the the *.asm that will be compiled on pressing the Compile Button, has the filename in bold letters on the tab. It is the first opened .asm file in the window that is always compiled.         Seve Project button:       When a source code (*.asm) file and all the respective include (*.h) files are open in the Assembler tab, then all the files can be combined and saved in a project file (*.pr)), by pressing the Save Project button. The .pr ji file always takes the name of the *.asm file. For e.g. A source code file sample asm uses 'include' files and by the name sample.prj. Once saved, the project file can be opened from the meru File → Open project → *.pr jile. This automatically obeigning to the project in one go. The *.pr jile. This automatically obeigning to the project. The *.pr jile stores the location of the file while saving the project. Nota2: When the PCapØ1 Assembler software is closed, a .pr j project file will be used to open all these files again when the PCapØ1 Assembler software is stored again.         Open Project button:       An already saved project (with a *.pr jile), can be opened using this button.         Nota2: When the PCapØ1 Assembler software is closed, a .pr j project file will be again when the PCapØ1 Assembler software is stored again.         Open Project button:       An		
respective header files is: Compile.         Dr pressing the Compile button the source file is compiled to a downloadbable hex code. If the compilation was successful a message is displayed accordingly. If the compiling files, an error message occurs. If more than one source files are currently open, then the *.asm that will be compiled on pressing the Compile Button, has the filename in bold letters on the tab. It is the first opened .asm file in the window that is always compiled.         Save Project button:       When a source code (*.asm) file and all the respective include (*.h) files are open in the Assembler tab, then all the files can be combined and saved in a project file (*.prj), by pressing the Save Project button. The .prj file always takes the name of the *.asm file. For e.g. A source code file sample.asm uses 'include' files include1.h and include2.h. Then the corresponding prj file would be saved by the name sample.prj. Once saved, the project file can be opened from the menu File $\rightarrow$ Open project $\rightarrow$ *.prj file can also be opened and edited in this Window.         Note1: The .asm and .h files can be in different folders or locations. The *.prj file stores the location of the file will be that of the open source code file with all the files that are currently open in the window. The name of this project file will be that of the open source code file with a .prj extension. This .prj file will be used to open all these files gain.         Dpen Project button:       An already saved project (with a *.prj file), can be opened using this button         Status message window:       An already saved project (with a *.prj file), can be opened using this button         Status message window:       In this window there are the output messages of the assembler displayed. In case the ass		settings for the editor via the tool settings dialog. To know more about
files are open in the Assembler tab, then all the files can be combined and saved in a project file (*, prj), by pressing the Save Project button. The , prj file always takes the name of the *.asm file.         For e.g. A source code file sample.asm uses 'include' files include1.h and include2.h. Then the corresponding prj file would be saved by the name sample.prj. Once saved, the project file can be opened from the menu File → Open project → *.prj file. This automatically closes all currently open files and opens all the files (*.asm and *.h) belonging to the project in one go. The *.prj file can also be opened and edited in this Window.         Note1: The .asm and .h files can be in different folders or locations. The *.prj file stores the location of the file while saving the project.         Note2: When the PCapØ1 Assembler software is closed, a .prj project file is automatically generated with all the files that are currently open in the window. The name of this project file will be that of the open source code file with a .prj extension. This .prj file will be used to open all these files again when the PCapØ1 Assembler software is started again.         Dpen Project button:       An already saved project (with a *.prj file), can be opened using this button         Status message window:       In this window there are the output messages of the assembler displayed. In case the assembly process was successful, a corresponding message appears with the path of the file that was compiled. If an error occurs while assembling, an error message appears together with the line number and the file name in which the error occurred. An error in any of the source code (*.asm) file or "include" (*.h) files is identified and intimated with the path of the file.	Assembler button:	respective header files is: Compile. On pressing the Compile button the source file is compiled to a downloadable hex code. If the compilation was successful a message is displayed accordingly. If the compiling fails, an error message occurs. If more than one source files are currently open, then the *.asm that will be compiled on pressing the Compile Button, has the filename in bold letters on the tab. It is the first opened .asm file in the window
button         Status message window:         In this window there are the output messages of the assembler displayed. In case the assembly process was successful, a corresponding message appears with the path of the file that was compiled. If an error occurs while assembling, an error message appears together with the line number and the file name in which the error occurred. An error in any of the source code (*.asm) file or "include" (*.h) files is identified and intimated with the path of the file.	Save Project button:	files are open in the Assembler tab, then all the files can be combined and saved in a project file (*.prj), by pressing the Save Project button. The .prj file always takes the name of the *.asm file. For e.g. A source code file sample.asm uses 'include' files include1.h and include2.h. Then the corresponding prj file would be saved by the name sample.prj. Once saved, the project file can be opened from the menu File → Open project → *.prj file. This automatically closes all currently open files and opens all the files (*.asm and *.h) belonging to the project in one go. The *.prj file can also be opened and edited in this Window. Note1: The .asm and .h files can be in different folders or locations. The *.prj file stores the location of the file while saving the project. Note2: When the PCapØ1 Assembler software is closed, a .prj project file is automatically generated with all the files that are currently open in the window. The name of this project file will be that of the open source code file with a .prj extension. This .prj file will be used to open all these files again when the PCapØ1 Assembler software is started
displayed. In case the assembly process was successful, a corresponding message appears with the path of the file that was compiled. If an error occurs while assembling, an error message appears together with the line number and the file name in which the error occurred. An error in any of the source code (*.asm) file or "include" (*.h) files is identified and intimated with the path of the file.	Open Project button:	
Opcode-Help: Between the source code window and the status message window	Status message window:	displayed. In case the assembly process was successful, a corresponding message appears with the path of the file that was compiled. If an error occurs while assembling, an error message appears together with the line number and the file name in which the error occurred. An error in any of the source code (*.asm) file or
	Opcode-Help:	Between the source code window and the status message window

there can be found a green link labeled ,Op code-Help (F2) <sup>6</sup> . By clicking this link another window pops up. The window contains the op code online help. Every supported op code is explained there in detail. The
additional windows are based on the integrated Microsoft Windows ® Help system and can be operated separately. (Windows is a trademark of Microsoft Corporation)

Running the assembler in order to compile your source file is the first step when using the PCapØ1 Assembler Software.

An integrated 'examples' folder provides some readymade firmware code for downloading into the PCapØ1 chip. The standard firmware (.asm and .hex) is present in the 'standard' folder under 'examples'.

The user can also add his own examples to the 'Examples' folder with the respective prj file. The prj file can be written manually (See format of existing prj files in the Examples folder) or can be allowed to be generated by the assembler with the Save Project button. The Search-Examples dialog box can be accessed via the Help menu or by pressing F4 or by clicking on the 'Bulb' icon on the top. Only \*.prj project files can be opened via the Search Examples option. To open individual files, use the File  $\rightarrow$  Open option.

#### 4.2.2.1 'Include' files

The basic idea of supporting 'include' files is to provide more modularity and flexibility within your code. That means, instead of packing all needed source code to a single assembler listing (\*.asm files) you can roll out some pieces of code to the include files (also called header files with extension \*.h). The advantage of this method is, that the readability of your code will be improved and that once written parts of code can be reused (e.g. configuration information).

To include parts of code by using a include file there are two steps needed.

Step 1: Create an include file (e.g. config.h).

Step 2: Declare the inclusion in your assembler listing as follows: #include "config.h"

Include files that are in the same folder as the source file (\*.asm) can be included with #include "filename.h". Include files that are in a path one level higher than the source file can be included with "../filename.h". If there is a set of common include files, they can be segregated in a folder called Lib in PCapO1 \Assembler\lib. The 'include' files in the lib



folder can be included in the program by using #include <filename.h>. They are automatically included from the lib folder.

On installing the assembler software, the PCapO1 \Assembler\lib is pre-installed. It contains all the library and header files that are used in the examples. You can add your library or header files to this folder.

The alternative method of using a single assembler files without using 'include' files at all is also supported.

#### 4.2.3 Download Page

The 'Download' tab displays the compiled hex code that can be downloaded into the PCapØ1 chip. The hex-file is displayed in rows with 16 bytes per row. The location of the displayed hex file is shown at the top of the window. Now this hex file can be written into the OTP or the SRAM of the PCapØ1 chip for e.g. using the Front panel software.

<u>F</u> ile <u>E</u> di	t <u>V</u> iew <u>A</u> ssembler	er <u>T</u> ools <u>H</u> elp	
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ad		1 20 2F A4 01 20 2F A4 01 20 2F A5 D7 00 4 43 44 D5 43 F7 44 20 45 20 5F 78 68 01	
Do <u>w</u> nload		0 20 07 D5 41 7F F6 43 A9 03 6F F6 58 58 2 42 5D 5D 42 A8 03 60 5C A9 00 70 02 7A	
<u> </u>	C0 C0 C0	0 D5 FF 43 EC 44 F4 41 2B 16 EA 45 F5 41	
		9 45 1D 41 43 58 EA 29 2D E9 50 46 EB 44 B 59 43 CA FE 41 5C A8 03 D3 5A EB 45 EB	
		5 02 D3 43 D2 59 AA 06 75 65 7A DA 43 D2 E 7A DA 43 D2 44 6A C0 FE 43 D3 44 EF 45	
	5B 22 10	0 D8 FE 41 5D C0 FE 43 5D AB 13 47 AB 12	
		1 47 AB 10 47 AB 0F 47 AB 0E 47 AB 0D 20 7 22 A1 23 2B 23 B5 24 3F 24 C9 25 53 25	
	DD D7 FE	E 43 CF FE 41 D9 45 C0 C0 C0 C0 FF 41 ED	
		1 2B 16 1F 45 D3 41 ED 53 D9 41 47 45 AA 0 FF	
		F FF FF F FF F	
	FF FF FF	F FF	
		F FF FF F FF F	
		F FF FF F FF F	
	FF FF FF	F FF	
		F FF FF F FF F	
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	FF FF FF	F FF	
		F FF FF F FF F	
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Figure 4-3: Hex-file display

## 

#### 4.2.4 Assembler Settings

Different settings for the assembler and the editor can be done by selecting the item "Settings" in the "Tools" menu. A screen appears that shows the following sheets:

Settings
Environment Editor
Window settings
Allow Window Maximization
Show hints
Show tooltips
Languages:
Automatic codepage detection at startup
Restore default settings     Apply <u>Cancel</u>

Figure 4-4: Environment settings

Editor	settings;
0.0000000000000000000000000000000000000	how line numbering
	ne numbering in gray
A <b>N</b>	pply syntax highlighting
Гв	nable Multibyte Support

Figure 4-5: Editor settings

4-8



### 5 Miscellaneous

### 5.1 Literature Guide

#### Data Sheets

Title	Document-No	Date
PCapØ1Ax-0301 Single-chip Solution for Capacitance Measurement with Standard Firmware version 3.0	DB_PCapO1Ax_O3O1_en.p df	May 2011
PCapØ1AK-0101 Single-chip Solution for Humidity Measurement	DB_PCapO1AK- O1O1_en.pdf	February 2011
PCapØ1-DSP Description of the digital signal processor	DB_PCapO1_DSP_e.pdf	Aug 2011

### White Papers

Title	Document-No	Date
Linearization of Pressure Sensors with PCapØ1	WPOO5_e.pdf	Sep 2011

### **Application Notes**

Title	Document-No	Date
Cable compensation	AN026 V1.0	In preparation

All available documents can be downloaded from the acam website at:

http://www.acam.de/download-center/picocap

### 5.2 Document History

26.02.2013 Correction in section 2.2, C1 = 33 nF COG, Version 0.3

## **PICO**CAP<sup>®</sup>

- 12.12.2012 Minor updates and corrections, Version 0.2
- 27.10.2011 Assembler description added, Evaluation software description expanded.
- 29.04.2011 First release



## 6 Appendix

### 6.1 Schematics

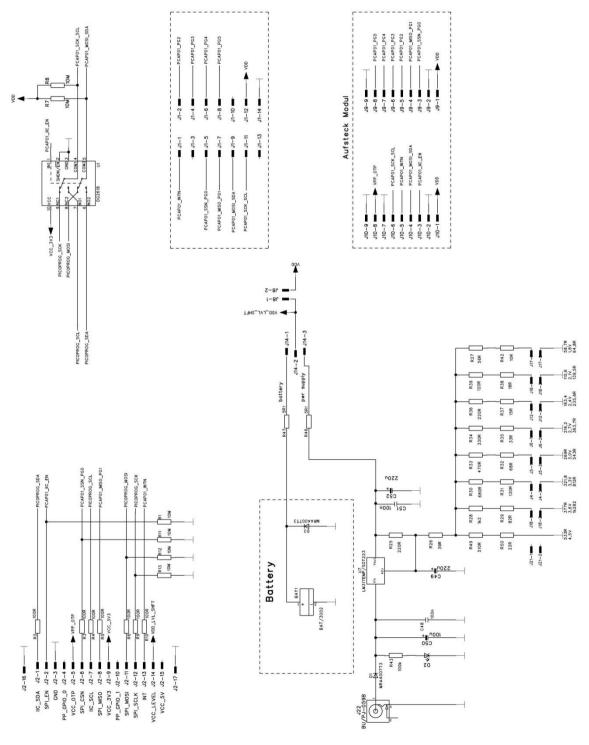


Figure 6-1: Motherboard schematics

## 

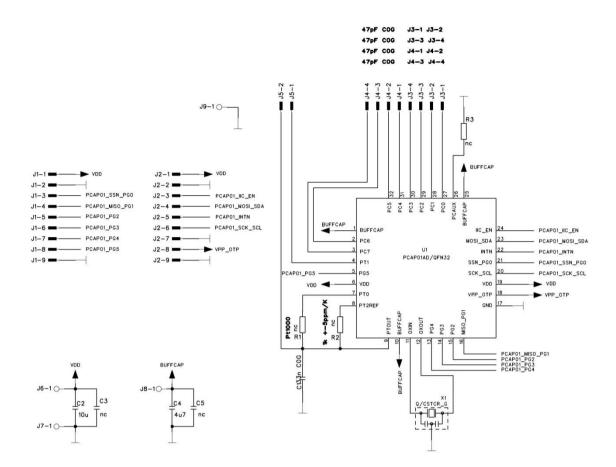
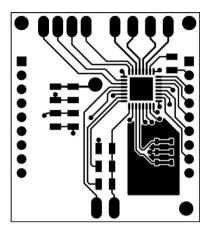


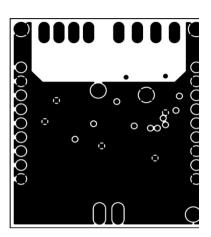
Figure 6-2: PCaO1 AD module schematics

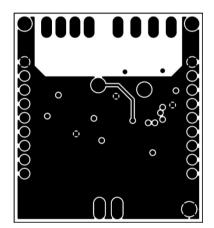
6-2

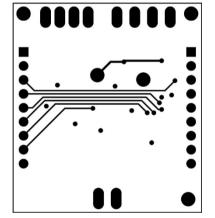


6.2 Layout









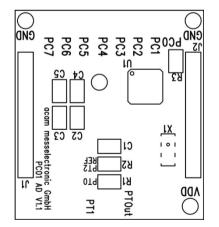


Figure 6-3: PCaO1 AD module layout





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