



60V P-Channel Enhancement Mode MOSFET

Voltage

-60 V

Current

-14 A

Features

- $R_{DS(ON)}$, $V_{GS}@-10V$, $I_D@-6A<110m\Omega$
- $R_{DS(ON)}$, $V_{GS}@-4.5V$, $I_D@-3A<130m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

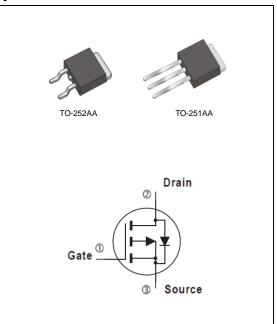


• Case: TO-251AA, TO-252AA Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• TO-251AA Approx. Weight: 0.0104 ounces, 0.297grams

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Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V_{DS}	-60	V	
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V	
Continuous Drain Current	T _C =25°C	I _D	-14	A	
	T _C =100°C		-9		
Pulsed Drain Current (Note 1)	T _C =25°C	I _{DM}	-42		
Power Dissipation	T _C =25°C	Po	40	W	
	T _C =100°C		16		
Continuous Drain Current	T _A =25°C	I _D	-3.2	Α	
	T _A =70°C		-2.5	Α	
Power Dissipation	T _A =25°C		2.0	W	
Power Dissipation	T _A =70°C	Pb	1.3		
Single Pulse Avalanche Energy (Note 6)		E _{AS}	20	mJ	
Operating Junction and Storage Temperature Range		T_{J} , T_{STG}	-55~150	°C	
Typical Thermal Resistance (Note 4,5)	Junction to Case	$R_{ heta JC}$	3.1	°C/W	
	Junction to Ambient	$R_{\theta JA}$	62.5		

Limited only By Maximum Junction Temperature





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static			•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =-250uA	-60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-1.0	-1.7	-2.5	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V,I _D =-6A	-	87	110	mΩ
		V_{GS} =-4.5V, I_{D} =-3A	-	110	130	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} =-60V, V_{GS} =0V	-	-	-1.0	uA
Gate-Source Leakage Current	I_{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 7)						
Total Gate Charge	Q_g	V _{DS} =-30V, I _D =-4A, V _{GS} =-10V ^(Note 2,3)	-	10	-	nC
Gate-Source Charge	Q_gs		-	1.6	-	
Gate-Drain Charge	Q_gd		-	3	-	
Input Capacitance	Ciss	V 20V V 0V	-	785	-	pF
Output Capacitance	Coss	V_{DS} =-30V, V_{GS} =0V, f =1.0MHZ	-	175	-	
Reverse Transfer Capacitance	Crss	I=1.UIVIHZ	-	112	-	
Turn-On Delay Time	td _(on)	V 20V BL 200	-	8	-	ns
Turn-On Rise Time	t _r	V_{DS} =-30V,RL=30 Ω ,	-	15	-	
Turn-Off Delay Time	td _(off)	V_{GS} =-10V, R_{G} =6.2 Ω (Note 2,3)	-	43	-	
Turn-Off Fall Time	t _f		-	8.4	-	
Drain-Source Diode						
Maximum Continuous Drain-Source			-	-	-14	А
Diode Forward Current	I _S					
Reverse Recovery Time	V_{SD}	I _S =-1A,V _{GS} =0V	-	-0.76	-1.0	V

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics
- 3. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
- 4. The maximum current rating is package limited
- 5. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper
- 6. L=0.1mH, I_{AS} =-20A, V_{GS} =-10V, V_{DS} =-25V, R_{G} =25 ohm
- 7. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

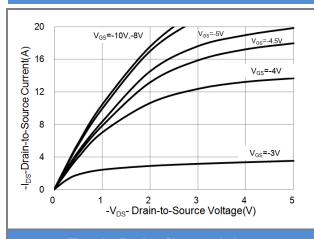


Fig.1 On-Region Characteristics

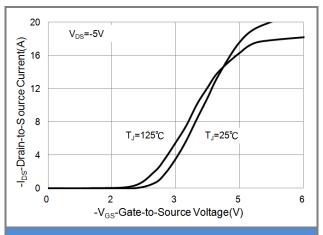


Fig.2 Transfer Characteristics

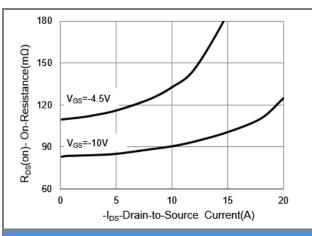


Fig.3 On-Resistance vs. Drain Current

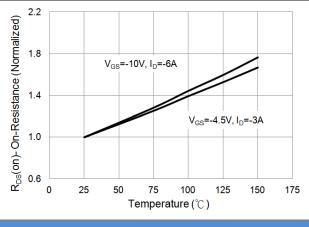


Fig.4 On-Resistance vs. Junction temperature

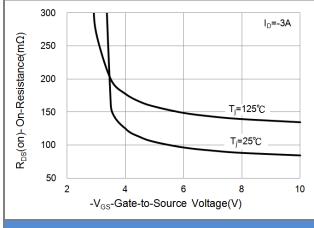


Fig.5 On-Resistance Variation with VGS.

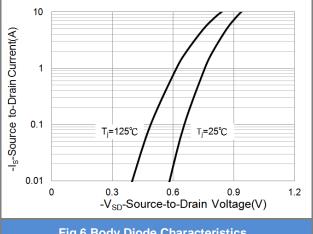


Fig.6 Body Diode Characteristics





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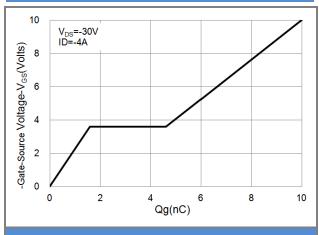


Fig.7 Gate-Charge Characteristics

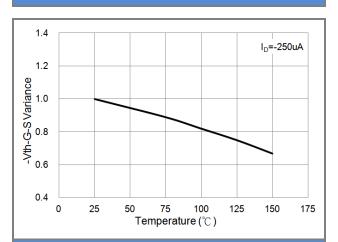
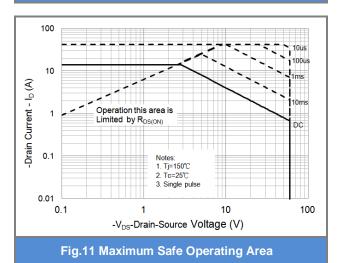


Fig.9 Threshold Voltage Variation with Temperature



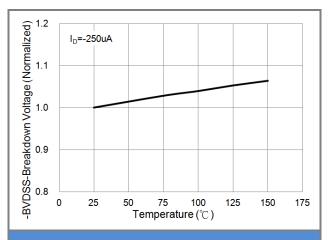


Fig.8 Breakdown Voltage Variation vs. Temperature

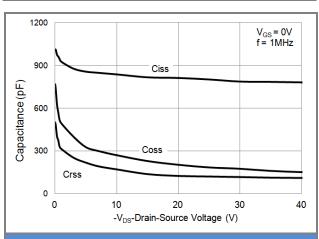


Fig.10 Capacitance vs. Drain-Source Voltage





TYPICAL CHARACTERISTIC CURVES

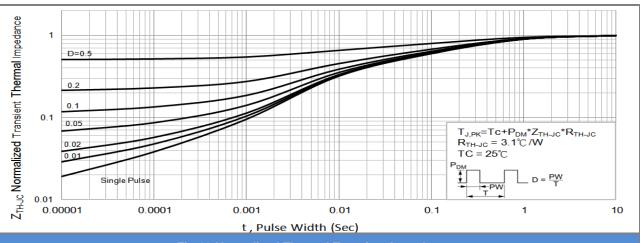
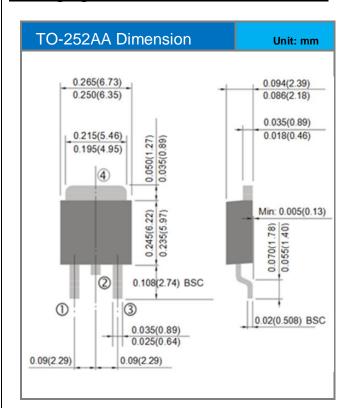


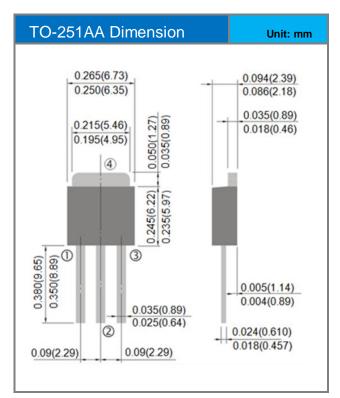
Fig.12 Normalized Thermal Transient Impedance





Packaging Information





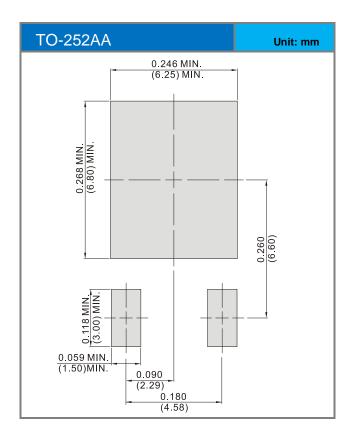




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type Marking		Version
PJU14P06A_T0_00001	TO-251AA	80pcs / Tube	U14P06A	Halogen free
PJD14P06A_L2_00001	TO-252AA	3,000pcs / 13" reel	D14P06A	Halogen free

MOUNTING PAD LAYOUT







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