



### **60V P-Channel Enhancement Mode MOSFET**

Voltage

-60 V

Current

-4 A

#### **Features**

- R<sub>DS(ON)</sub>, V<sub>GS</sub>@-10V, I<sub>D</sub>@-4A<110mΩ
- $R_{DS(ON)}$ ,  $V_{GS}@-4.5V$ ,  $I_D@-2A<130m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

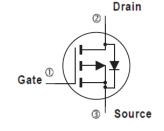
• Case: SOT-223 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.043 ounces, 0.123 grams

#### SOT-223





### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		$V_{DS}$	-60	V	
Gate-Source Voltage		$V_{GS}$	<u>+</u> 20		
Continuous Drain Current (Note 4)	T <sub>A</sub> =25°C	- I <sub>D</sub>	-4		
	T <sub>A</sub> =70°C		-3.2	A	
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	-16		
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	3.1		
	T <sub>A</sub> =70°C		2	W	
Single Pulse Avalanche Energy (Note 6)		E <sub>AS</sub>	12.8	mJ	
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient (Note 4,5)		$R_{ hetaJA}$	40.3	°C/W	

• Limited only By Maximum Junction Temperature





### **Electrical Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-60	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=-250uA$	-1	-1.7	-2.5		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-4A	-	87	110	mΩ	
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A	-	110	130		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V	-	-	-1	uA	
Gate-Source Leakage Current	$I_{GSS}$	V <sub>GS</sub> = <u>+</u> 20V, V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA	
Dynamic (Note 7)			_				
Total Gate Charge	$Q_g$	$V_{DS}$ =-30V, $I_{D}$ =-4A, $V_{GS}$ =-10V (Note 2,3)	-	10	-	nC	
Gate-Source Charge	$Q_gs$		-	1.6	-		
Gate-Drain Charge	$Q_{gd}$		-	3	-		
Input Capacitance	Ciss	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V, f=1MHZ	-	785	-	pF	
Output Capacitance	Coss		-	175	-		
Reverse Transfer Capacitance	Crss	I = IIVII IZ	-	112	-		
Turn-On Delay Time	td <sub>(on)</sub>	$V_{DS}$ =-30V, $R_{L}$ =30 $\Omega$ $V_{GS}$ =-10V, $R_{G}$ =6.2 $\Omega$ (Note 2,3)	-	8	-	ns	
Turn-On Rise Time	t <sub>r</sub>		-	15	-		
Turn-Off Delay Time	td <sub>(off)</sub>		-	43	-		
Turn-Off Fall Time	t <sub>f</sub>		-	8.4	-		
Drain-Source Diode							
Maximum Continuous Drain-Source	ı		-	-	-4	А	
Diode Forward Current	I <sub>S</sub>						
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V	_	-0.76	-1	V	

#### NOTES:

- 1. Pulse width<a>300us</a>, Duty cycle<2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
- 4. The maximum current rating is package limited.
- 5. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. The test condition is L=0.1mH,  $I_{AS}$ =16A,  $V_{DD}$ =25V,  $V_{GS}$ =10V
- 7. Guaranteed by design, not subject to production testing.





#### TYPICAL CHARACTERISTIC CURVES

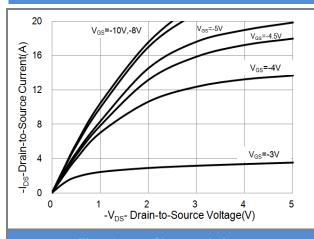
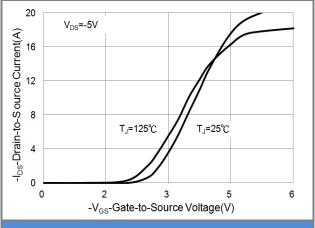


Fig.1 Output Characteristics



**Fig.2 Transfer Characteristics** 

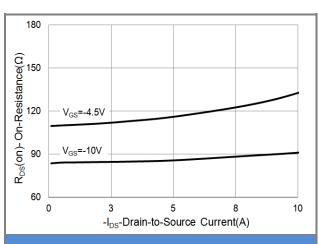


Fig.3 On-Resistance vs. Drain Current

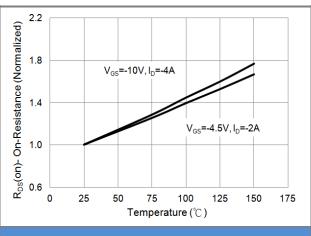
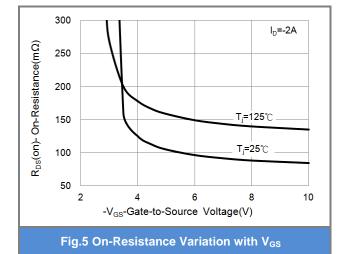


Fig.4 On-Resistance vs. Junction temperature



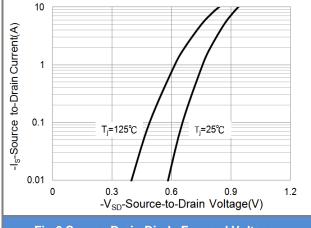


Fig.6 Source-Drain Diode Forward Voltage





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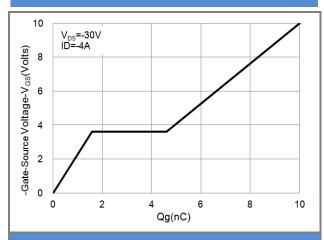


Fig.7 Gate-Charge Characteristics

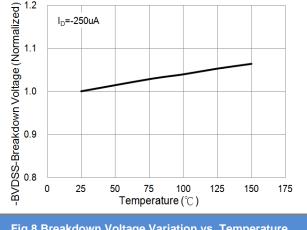


Fig.8 Breakdown Voltage Variation vs. Temperature

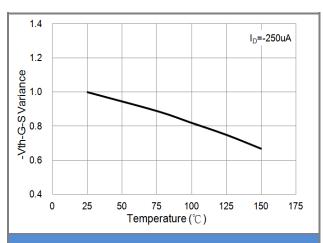


Fig.9 Threshold Voltage Variation with Temperature

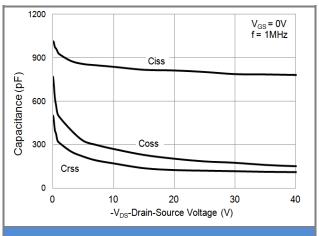


Fig.10 Capacitance vs. Drain-Source Voltage

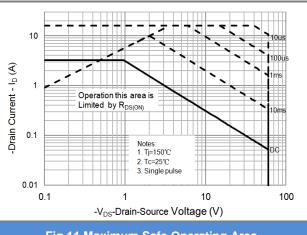


Fig.11 Maximum Safe Operating Area





#### **TYPICAL CHARACTERISTIC CURVES**

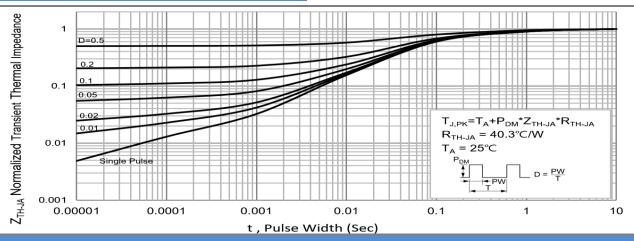


Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

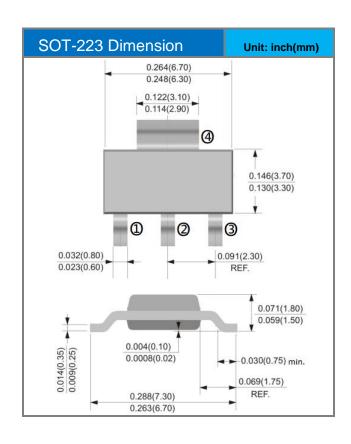


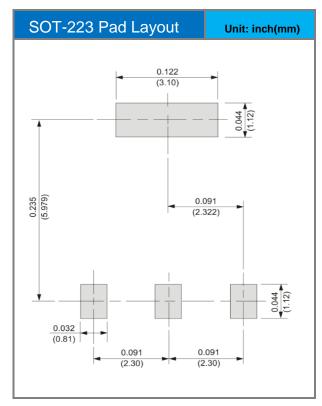


### **Part No Packing Code Version**

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJW4P06A-AU_R2_000A1	SOT-223	2,500pcs / 13" reel	W4P06A	Halogen free

### **Packaging Information & Mounting Pad Layout**









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