

# PJW4N06A-AU

## 60V N-Channel Enhancement Mode MOSFET

**Voltage**

**60 V**

**Current**

**4 A**

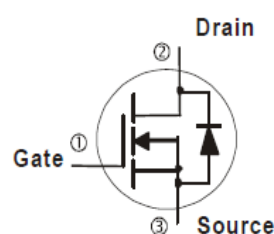
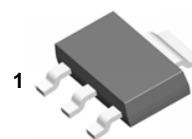
### Features

- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@3A<100m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@4.5V$ ,  $I_D@2A<110m\Omega$
- Advanced Trench Process Technology
- High density cell design for ultra low on-resistance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : SOT-223 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.043 ounces, 0.123grams

SOT-223



### Maximum Ratings and Thermal Characteristics ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	60	V
Gate-Source Voltage		V <sub>GS</sub>	±20	
Continuous Drain Current <sup>(Note 4)</sup>	T <sub>A</sub> =25°C	I <sub>D</sub>	4	A
	T <sub>A</sub> =70°C		3.2	
Pulsed Drain Current <sup>(Note 1)</sup>		I <sub>DM</sub>	8	
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	3.7	W
	T <sub>A</sub> =70°C		2.6	
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~175	°C
Typical Thermal Resistance - Junction to Ambient <sup>(Note 4,5)</sup>		R <sub>θJA</sub>	40.3	°C/W

- Limited only By Maximum Junction Temperature



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### Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1	1.86	2.5	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3A	-	85	100	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =2A	-	95	110	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V	-	-	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Dynamic (Note 6)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =48V, I <sub>D</sub> =3A, V <sub>GS</sub> =4.5V (Note 2,3)	-	5.1	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.2	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	1.9	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHZ	-	509	-	pF
Output Capacitance	C <sub>oss</sub>		-	39	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	26	-	
Turn-On Delay Time	td(on)	V <sub>DD</sub> =30V, I <sub>D</sub> =3A, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω (Note 2,3)	-	1.6	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	7.3	-	
Turn-Off Delay Time	td(off)		-	25	-	
Turn-Off Fall Time	t <sub>f</sub>		-	14	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>	---	-	-	4	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V	-	0.8	1.2	V

#### NOTES :

1. Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ .
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ\text{C}$ . Ratings are based on low frequency and duty cycles to keep initial  $T_J=25^\circ\text{C}$ .
4. The maximum current rating is package limited.
5.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing.

# PJW4N06A-AU

## TYPICAL CHARACTERISTIC CURVES

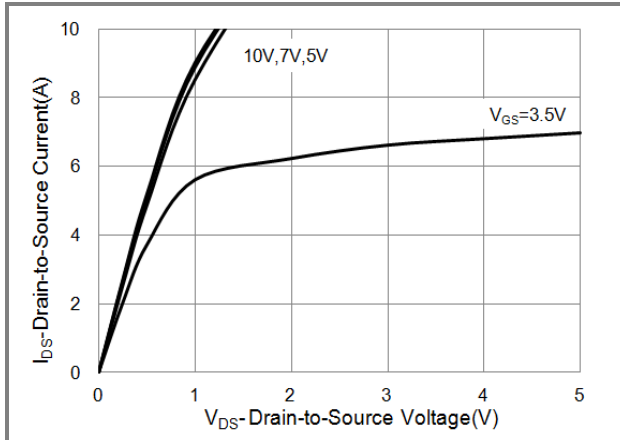


Fig.1 Output Characteristics

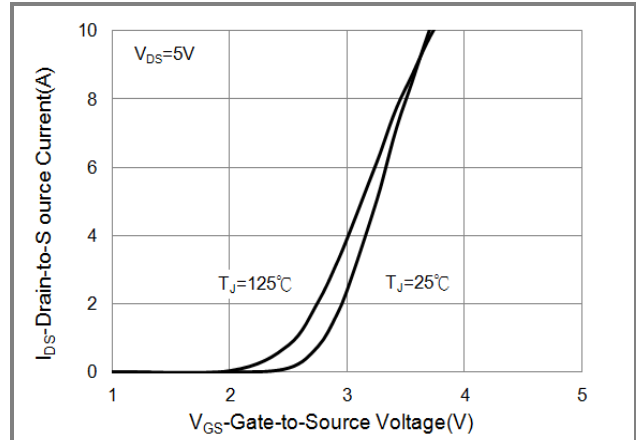


Fig.2 Transfer Characteristics

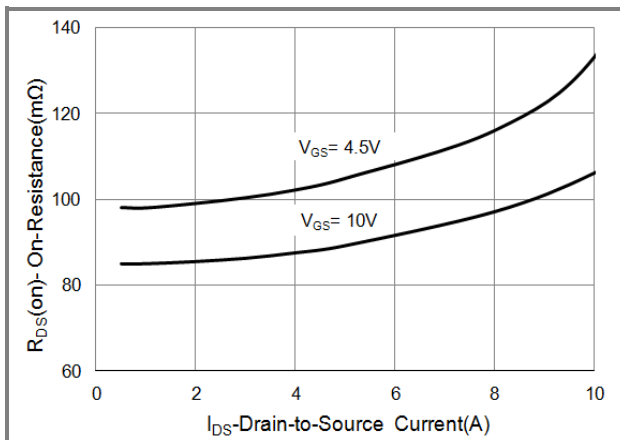


Fig.3 On-Resistance vs. Drain Current

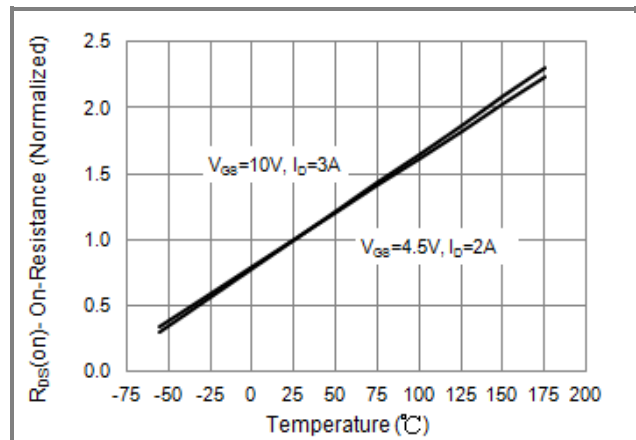


Fig.4 On-Resistance vs. Junction temperature

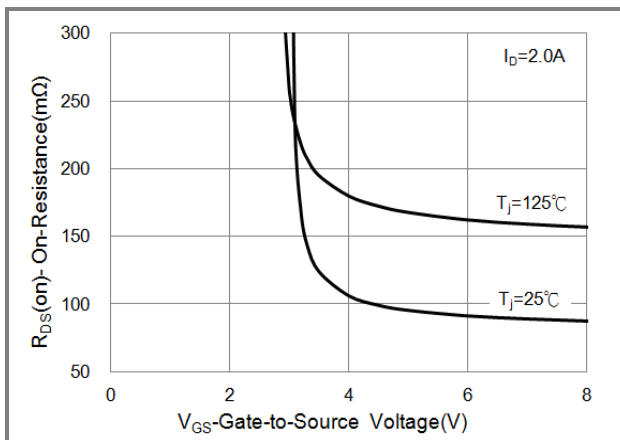


Fig.5 On-Resistance Variation with  $V_{GS}$

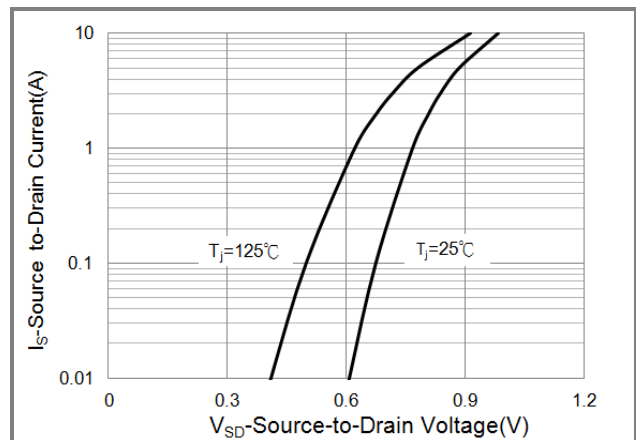


Fig.6 Source-Drain Diode Forward Voltage

## PJW4N06A-AU

### TYPICAL CHARACTERISTIC CURVES

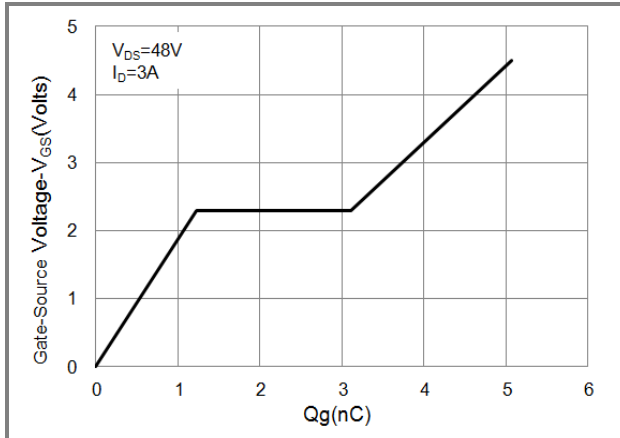


Fig.7 Gate-Charge Characteristics

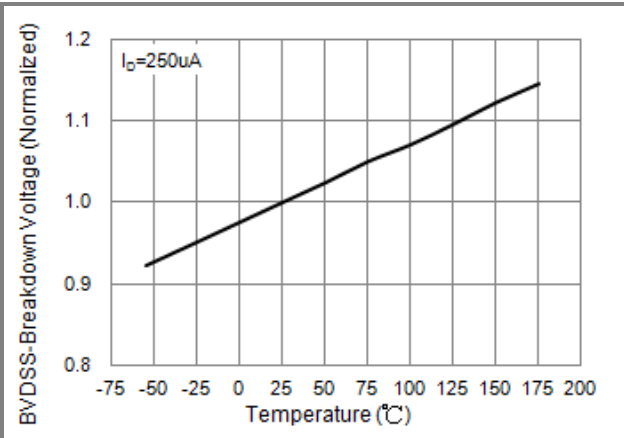


Fig.8 Breakdown Voltage Variation vs. Temperature

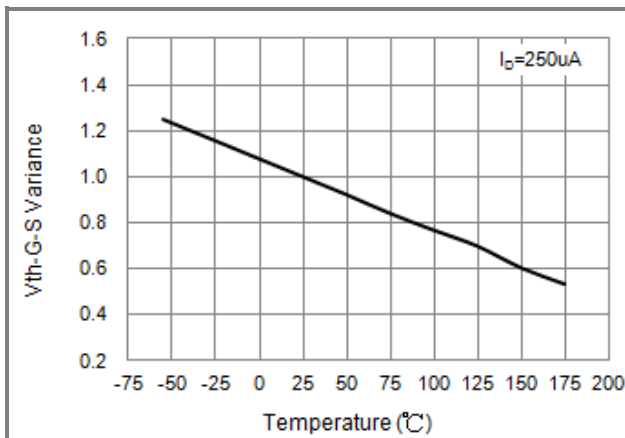


Fig.9 Threshold Voltage Variation with Temperature

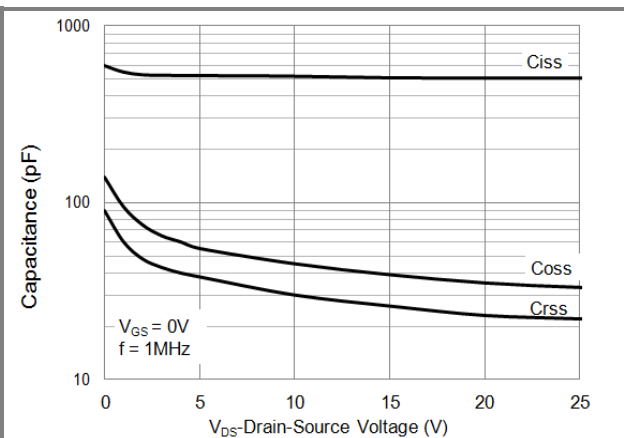


Fig.10 Capacitance vs. Drain-Source Voltage

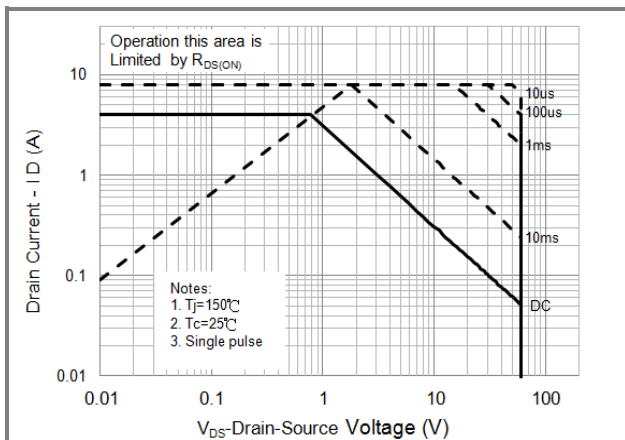


Fig.11 Maximum Safe Operating Area



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### TYPICAL CHARACTERISTIC CURVES

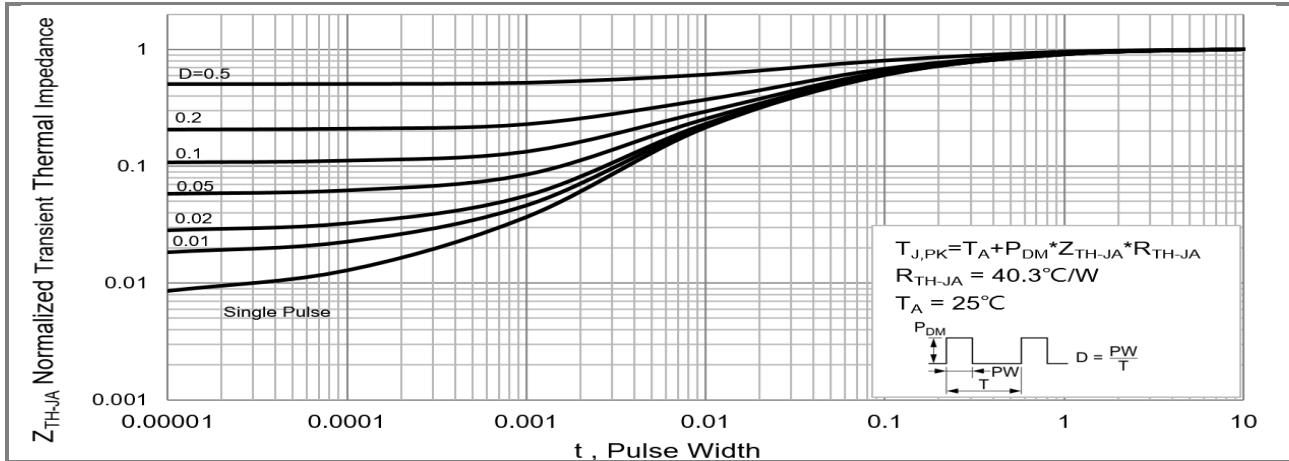


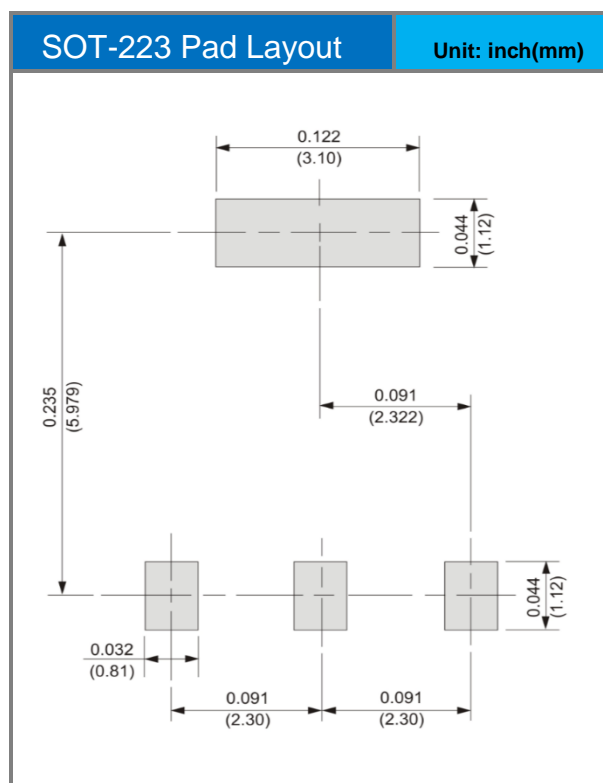
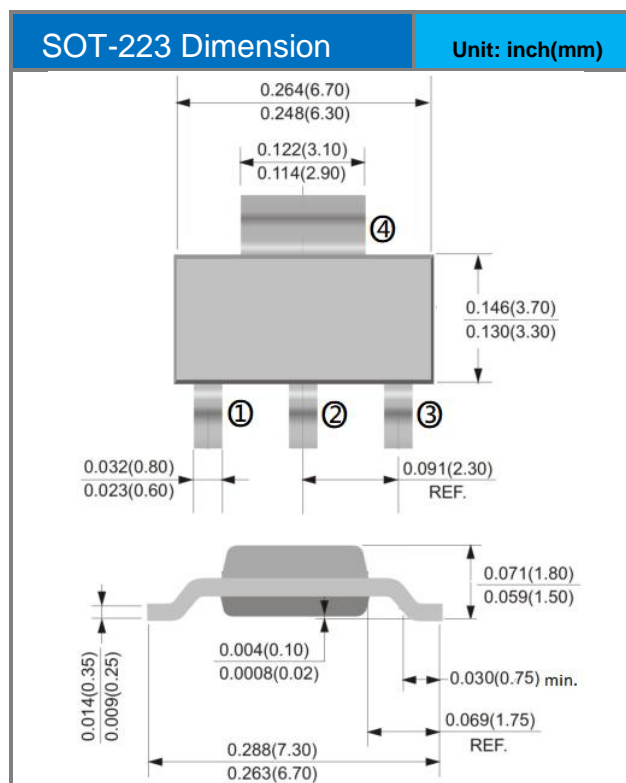
Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

# PJW4N06A-AU

## Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJW4N06A-AU_R2_000A1	SOT-223	2,500pcs / 13" reel	W4N06A	Halogen free

## Packaging Information & Mounting Pad Layout





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