



# 400W LOW CLAMPING VOLTAGE SINGLE TVS FOR PROTECTION

This TVS/Zener Series has been designed to Protect Sensitive Equipment against ESD and to prevent Latch-Up events in very sensitive CMOS circuitry operating at 5V, 12V, 15V and 24Vdc .These devices come in an industry standard SOD123 package making them suitable for Portable/Computing Electronics, where the board space is a premium.

#### SPECIFICATION FEATURES

- 400W Power Dissipation (8/20µs Waveform)
- Very Low Leakage Current
- IEC61000-4-2 ESD 15kV air, 8kV Contact Compliance
- SOD123 Package
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std. . (Halogen Free)

#### **APPLICATIONS**

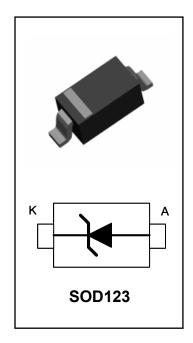
- Personal Digital Assistant (PDA)
- Digital Cameras
- Portable Instrumentation
- Mobile Phones and Accessories
- Desktops, Laptops

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Units
Peak Pulse Power (8/20µs Waveform)	P <sub>pp</sub>	400	W
ESD Voltage (HBM)	V <sub>ESD</sub>	25	kV
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C

# ELECTRICAL CHARACTERISTICS Tj = 25°C PJSD05 Marking T1S

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	$V_{WRM}$				5	V
Reverse Breakdown Voltage	$V_{BR}$	I <sub>BR</sub> =1 mA	6.0			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>R</sub> =5V			20	μΑ
Clamping Voltage (8/20µs)	V <sub>c</sub>	1 <sub>pp</sub> =5A			7.5	V
Clamping Voltage (820µs)	V <sub>c</sub>	I <sub>pp</sub> =24A			16	٧
Off State Junction Capacitance	Cj	0 Vdc Bias f = 1MHz			550	pF
Off State Junction Capacitance	Cj	5 Vdc Bias f = 1MHz			235	pF







#### **ELECTRICAL CHARACTERISTICS** Tj = 25°C

## **PJSD12 Marking T4S**

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	$V_{WRM}$				12	V
Reverse Breakdown Voltage	$V_{BR}$	I <sub>BR</sub> =1mA	13.3			V
Reverse Leakage Current	Ι <sub>R</sub>	V <sub>R</sub> = 12V			1	μΑ
Clamping Voltage (8/20µs)	V <sub>c</sub>	I <sub>pp</sub> =5A			14.5	V
Clamping Voltage (8/20µs)	V <sub>c</sub>	I <sub>pp</sub> = 17A			23	V
Off State Junction Capacitance	Cj	0 Vdc Bias f = 1MHz			180	pF

## **PJSD15 Marking T5S**

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	$V_{WRM}$				15	V
Reverse Breakdown Voltage	$V_{BR}$	I <sub>BR</sub> =1mA	16.7			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>R</sub> = 15V			1	μA
Clamping Voltage (8/20µs)	V <sub>C</sub>	I <sub>pp</sub> = 5A			19	V
Clamping Voltage (8/20µs)	V <sub>c</sub>	I <sub>pp</sub> = 14A			28	V
Off State Junction Capacitance	Cj	0 Vdc Bias f = 1MHz			165	pF

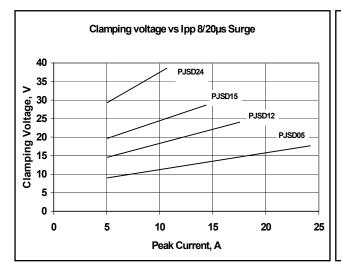
## **PJSD24 Marking T6S**

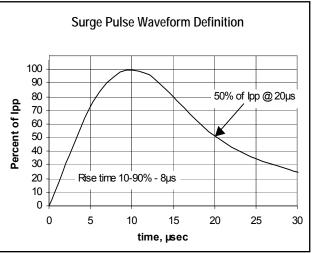
Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	$V_{WRM}$				24	V
Reverse Breakdown Voltage	$V_{BR}$	I <sub>BR</sub> =1mA	26.7			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>R</sub> = 24V			1	μA
Clamping Voltage (8/20µs)	V <sub>C</sub>	I <sub>pp</sub> = 5A			29	V
Clamping Voltage (8/20µs)	V <sub>c</sub>	I <sub>pp</sub> = 11A			37	V
Off State Junction Capacitance	Cj	0 Vdc Bias f = 1MHz			120	pF

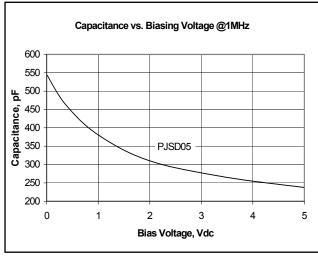


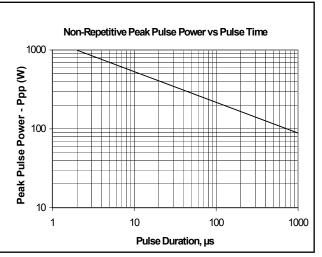


#### **TYPICAL CHARACTERISTICS**





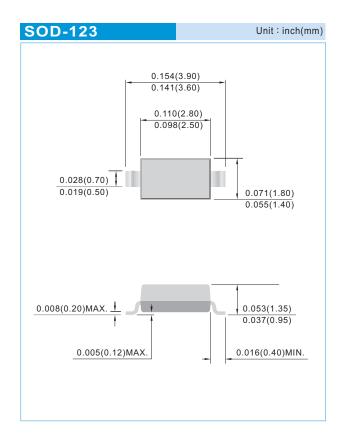


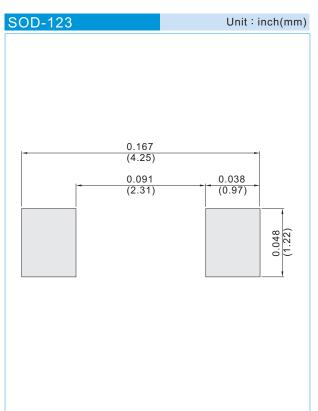






#### PACKAGE DIMENSIONS AND BOND PAD LAYOUT









# **PJSD05 SERIES**

#### Part No\_packing code\_Version

PJSD05\_R1\_00001 PJSD05\_R2\_00001

# For example : RB500V-40\_R2\_00001 Part No. Serial number Version code means HF Packing size code means 13"

Packing Code XX					Version Code XXXXX			
Packing type	1 <sup>st</sup> Code	Packing size code	2 <sup>nd</sup> Code	HF or RoHS	1 <sup>st</sup> Code	2 <sup>nd</sup> ~5 <sup>th</sup> Code		
Tape and Ammunition Box (T/B)	Α	N/A	0	HF	0	serial number		
Tape and Reel (T/R)	R	7"	1	RoHS	1	serial number		
Bulk Packing (B/P)	В	13"	2					
Tube Packing (T/P)	Т	26mm	Х					
Tape and Reel (Right Oriented) (TRR)	S	52mm	Υ					
Tape and Reel (Left Oriented) (TRL)	L	PANASERT T/B CATHODE UP (PBCU)	U					
FORMING	F	PANASERT T/B CATHODE DOWN (PBCD)	D					

Packing type means T/R

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# **PJSD05 SERIES**

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