

### 20V N-Channel Enhancement Mode MOSFET

20 V Current 750mA

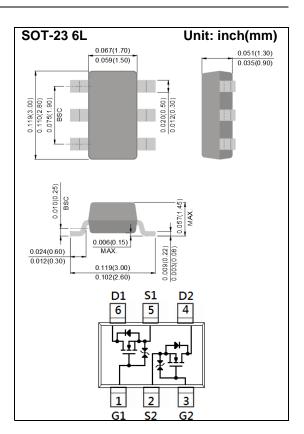
#### Features

Voltage

- Low Voltage Drive (1.2V).
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case: SOT-23 6L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.0141 grams
- Marking: SG4



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMI	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	20	V
Gate-Source Voltage	V <sub>GS</sub>	<u>+</u> 10	V	
Continuous Drain Current		lо	750	mA
Pulsed Drain Current <sup>(Note 4)</sup>		ldм	1500	mA
Power Dissipation	T <sub>a</sub> =25⁰C	PD	500	mW
	Derate above 25°C		4	mW/ºC
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	٥C
Typical Thermal Resistance - Junction to Ambient <sup>(Note 3)</sup>		Reja	100	°C/W



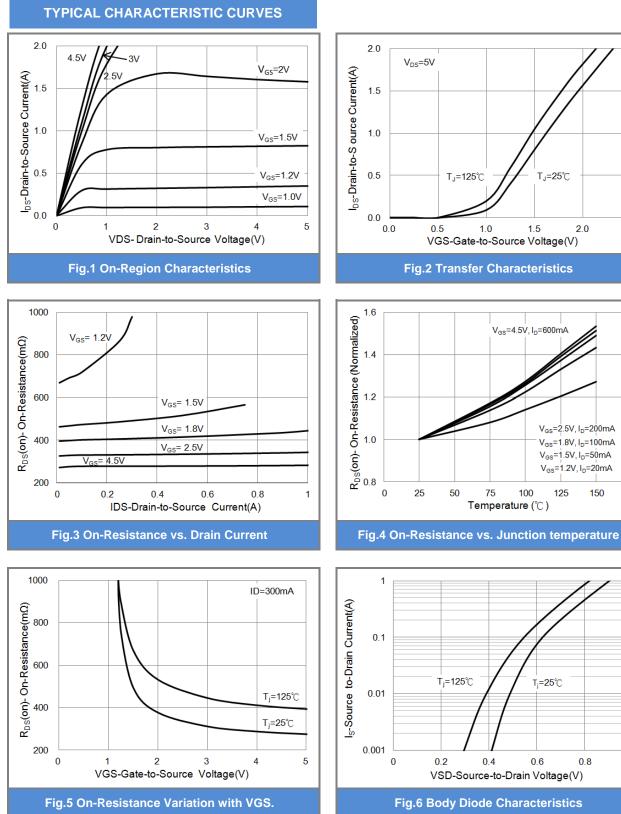
### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	0.3	0.65	0.9	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =600mA	-	280	400	mΩ
		$V_{GS}=2.5V, I_{D}=200mA$	-	350	650	
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =100mA	-	400	800	
		V <sub>GS</sub> =1.5V, I <sub>D</sub> =50mA	-	500	1200	
		V <sub>GS</sub> =1.2V, I <sub>D</sub> =20mA	-	1000	3000	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =16V, V <sub>GS</sub> =0V	-	-	1	uA
Gate-Source Leakage Current	Igss	V <sub>GS</sub> = <u>+</u> 8V, V <sub>DS</sub> =0V	-	<u>+</u> 0.5	<u>+</u> 10	uA
Dynamic <sup>(Note 5)</sup>						
Total Gate Charge	Qg	V <sub>DS</sub> =10V, I <sub>D</sub> =600mA, V <sub>GS</sub> =4.5V <sup>(Note 1,2)</sup>	-	1.4	-	nC
Gate-Source Charge	$Q_{gs}$		-	0.22	-	
Gate-Drain Charge	$Q_gd$		-	0.21	-	
Input Capacitance	Ciss		-	67	-	pF
Output Capacitance	Coss	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V,	-	19	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	6	-	
Turn-On Delay Time	td <sub>(on)</sub>		-	2.8	-	ns
Turn-On Rise Time	tr	V <sub>DD</sub> =10V, I <sub>D</sub> =150mA, V <sub>GS</sub> =4.0V,	-	20	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	23	-	
Turn-Off Fall Time	tf	R <sub>G</sub> =10Ω <sup>(Note 1,2)</sup>	-	23	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	500	mA
Diode Forward Voltage	V <sub>SD</sub>	Is=500mA, V <sub>GS</sub> =0V	-	0.87	1.3	V

NOTES :

- 1. Pulse width <300us, Duty cycle <2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R<sub>0JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.







2.5

V<sub>GS</sub>=2.5V, I<sub>D</sub>=200mA V<sub>GS</sub>=1.8V, I<sub>D</sub>=100mA V<sub>GS</sub>=1.5V, I<sub>D</sub>=50mA V<sub>gs</sub>=1.2V, I<sub>D</sub>=20mA 150 175

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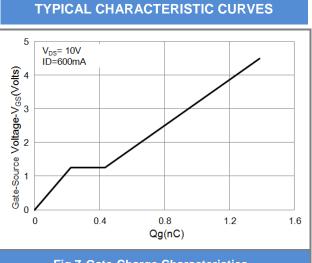


Fig.7 Gate-Charge Characteristics

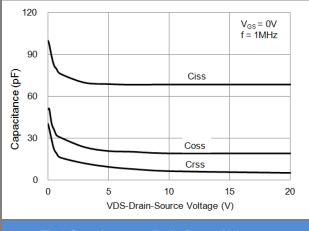


Fig.9 Capacitance vs. Drain-Source Voltage.

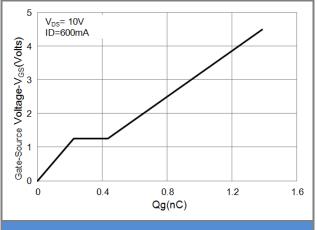


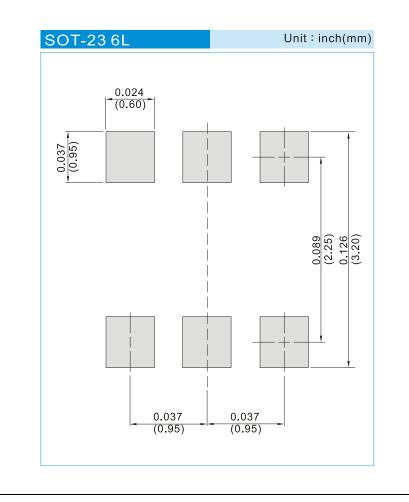
Fig.8 Threshold Voltage Variation with Temperature.



### PART NO. PACKING CODE VERSION

PART NO. PACKING CODE	Package Type	Packing Type	Marking	Version
PJS6834_S1_00001	SOT-23 6L	3K pcs / 7" reel	SG4	Halogen free RoHS compliant
PJS6834_S2_00001	SOT-23 6L	10K pcs / 13" reel	SG4	Halogen free RoHS compliant

### MOUNTING PAD LAYOUT







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