



30V P-Channel Enhancement Mode MOSFET - ESD Protected

Voltage -30 V Current -1.1A

Features

- RDS(ON), VGS@-4,5V, ID@-1.1A<370mΩ
- RDS(ON) , VGS@-2.5V, ID@-0.5A<540mΩ
- RDS(ON), VGS@-1.8V, ID@-0.1A<970mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected 2KV HBM
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

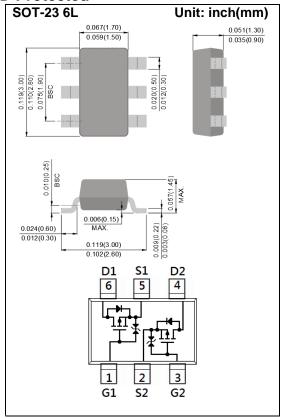
Mechanical Data

• Case: SOT-23 6L Package

Terminals: Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.0005 ounces, 0.0141 grams

Marking: SG3



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMET	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-30	V
Gate-Source Voltage		V _{GS}	<u>+</u> 8	V
Continuous Drain Current		ID	-1.1	А
Pulsed Drain Current ^(Note 4)		I _{DM}	-4.4	Α
Power Dissipation	T _a =25°C	P _D	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		R _{θJA}	100	°C/W





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA	-0.5	-0.98	-1.3	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-1.1A	-	293	370	mΩ
		V _{GS} =-2.5V, I _D =-0.5A	-	387	540	
		V _{GS} =-1.8V, I _D =-0.1A	-	750	970	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	<u>+</u> 3.4	<u>+</u> 10	uA
Dynamic ^(Note 5)						
Total Gate Charge	Q_g	V _{DS} =-15V, I _D =-1.1A, V _{GS} =-4.5V ^(Note 1,2)	-	1.6	-	nC
Gate-Source Charge	Q_gs		-	0.5	-	
Gate-Drain Charge	Q_{gd}		-	0.3	-	
Input Capacitance	Ciss	\/ 45\/ \/ 0\/	-	125	-	pF
Output Capacitance	Coss	V _{DS} =-15V, V _{GS} =0V,	-	22	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	6	-	
Turn-On Delay Time	td _(on)	\(\frac{1}{2}\)	-	11	-	
Turn-On Rise Time	tr	V _{DD} =-15V, I _D =-1.1A, V _{GS} =-4.5V,	-	51	-	ns
Turn-Off Delay Time	td _(off)		-	65	-	
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	46	-	
Drain-Source Diode						
Maximum Continuous Drain-Source			-	-	-1.0	А
Diode Forward Current	ls					
Diode Forward Voltage	V_{SD}	I _S =-1.0A, V _G S=0V	-	-0.9	-1.2	V

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

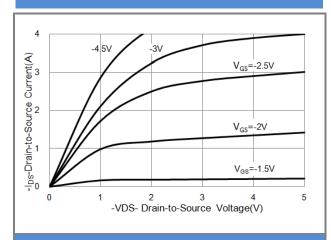


Fig.1 On-Region Characteristics

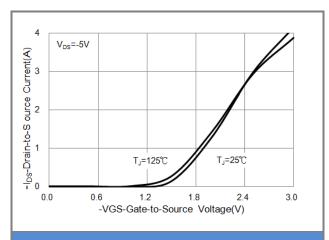


Fig.2 Transfer Characteristics

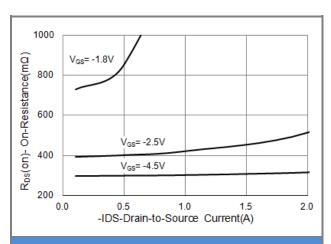


Fig.3 On-Resistance vs. Drain Current

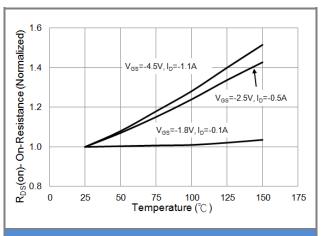


Fig.4 On-Resistance vs. Junction temperature

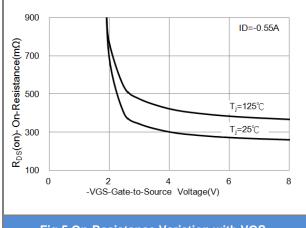
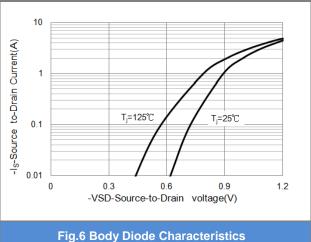


Fig.5 On-Resistance Variation with VGS.







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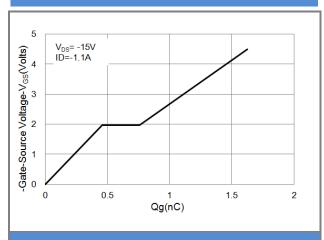


Fig.7 Gate-Charge Characteristics

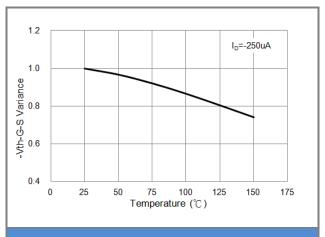


Fig.8 Threshold Voltage Variation with Temperature.

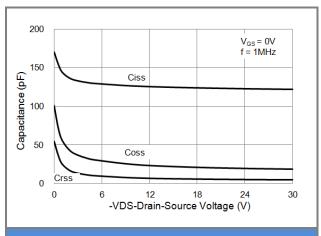


Fig.9 Capacitance vs. Drain-Source Voltage.

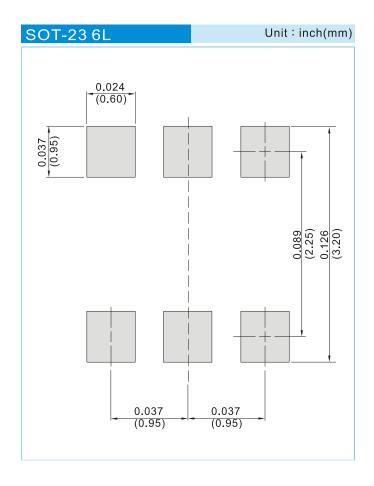




PART NO. PACKING CODE VERSION

PART NO. PACKING CODE	Package Type	Packing Type	Marking	Version
PJS6833_S1_00001	SOT-23 6L	3K pcs / 7" reel	SG3	Halogen free RoHS compliant
PJS6833_S2_00001	SOT-23 6L	10K pcs / 13" reel	SG3	Halogen free RoHS compliant

MOUNTING PAD LAYOUT







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