

Page 1



PJS6417

Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-0.35	-0.59	-0.9	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-6.5A	-	29	35	mΩ
		V _{GS} =-2.5V, I _D =-4.6A	-	33	40	
		V _{GS} =-1.8V, I _D =-2.6A	-	40	50	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =+8V, V _{DS} =0V	-	+10	+100	nA
Dynamic						
Total Gate Charge	Q _g	V _{DS} =-10V, I _D =-6.5A, V _{GS} =-4.5V (Note 1,2)	-	18.9	-	nC
Gate-Source Charge	Q _{gs}		-	2.8	-	
Gate-Drain Charge	Q _{gd}		-	4.2	-	
Input Capacitance	C _{iss}	V _{DS} =-10V, V _{GS} =0V, f=1.0MHZ	-	1760	-	pF
Output Capacitance	C _{oss}		-	148	-	
Reverse Transfer Capacitance	C _{rss}		-	120	-	
Switching						
Turn-On Delay Time	td _(on)	V _{DS} =-10V, I _D =-6.5A, V _{GS} =-4.5V, R _G =6Ω (Note 1,2)	-	12	-	ns
Turn-On Rise Time	tr			68		
Turn-Off Delay Time	td _(off)		-	82	-	
Turn-Off Fall Time	tf		-	35	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S	---	-	-	-2.0	A
Diode Forward Voltage	V _{SD}	I _S =-1.0A, V _{GS} =0V	-	-0.69	-1.2	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%
2. Essentially independent of operating temperature typical characteristics.
3. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited

PJS6417

TYPICAL CHARACTERISTIC CURVES

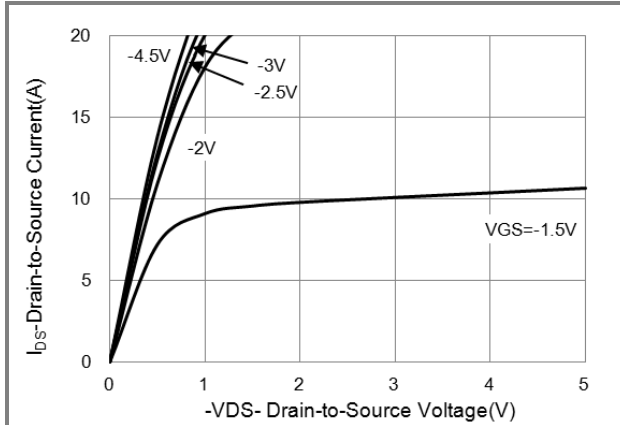


Fig.1 On-Region Characteristics

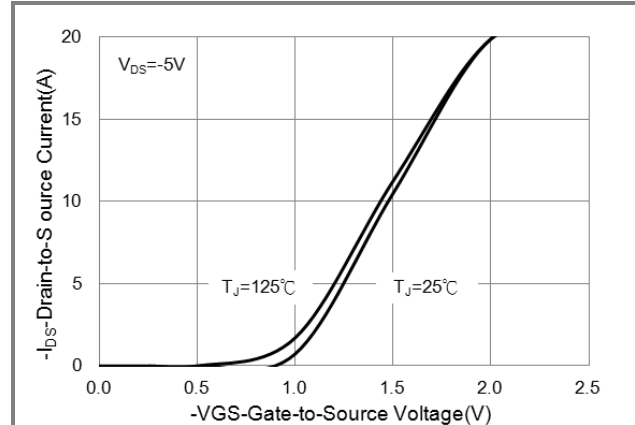


Fig.2 Transfer Characteristics

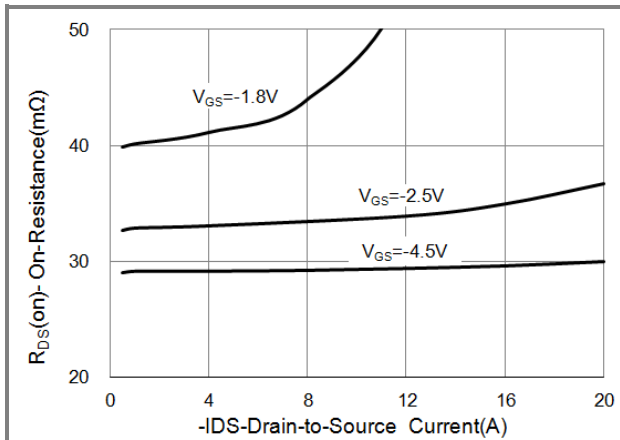


Fig.3 On-Resistance vs. Drain Current

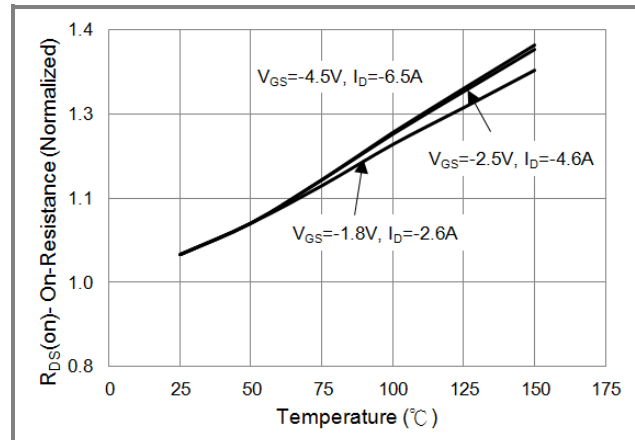


Fig.4 On-Resistance vs. Junction temperature

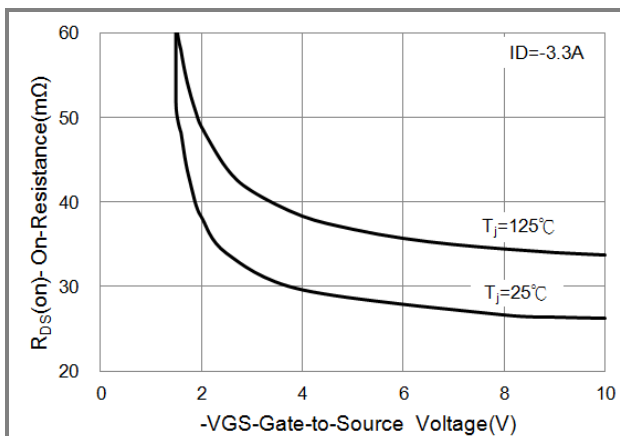


Fig.5 On-Resistance Variation with VGS.

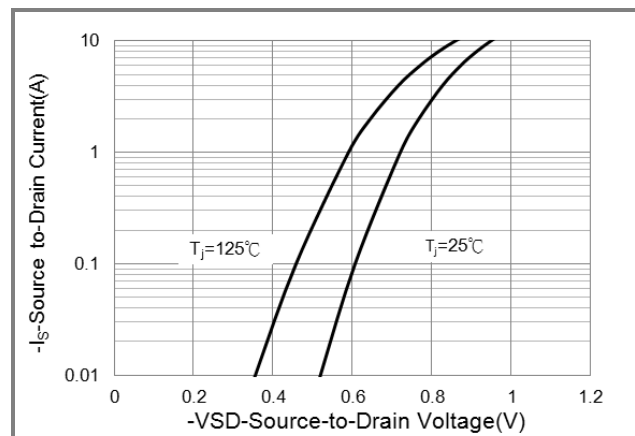


Fig.6 Body Diode Characteristics



PJS6417

TYPICAL CHARACTERISTIC CURVES

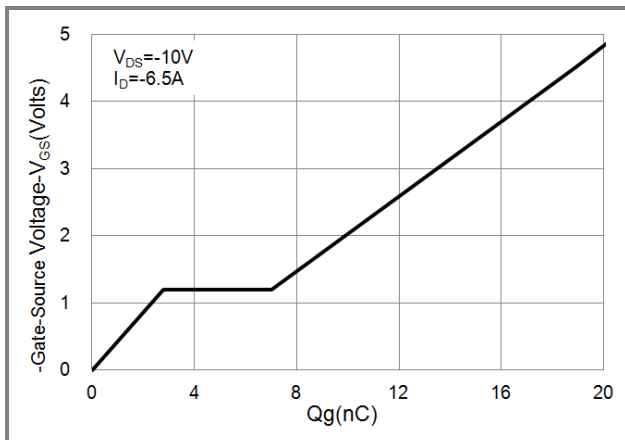


Fig.7 Gate-Charge Characteristics

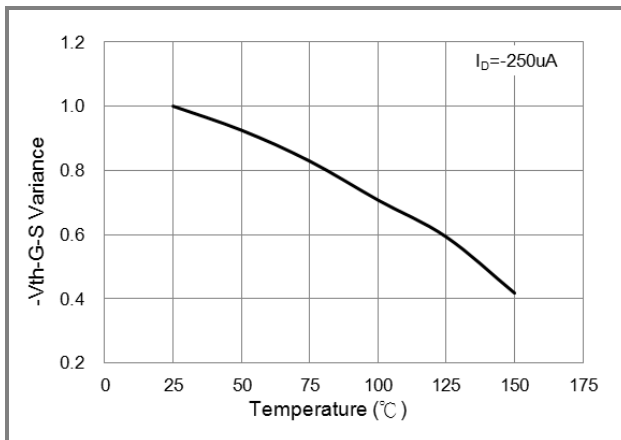


Fig.8 Threshold Voltage Variation with Temperature

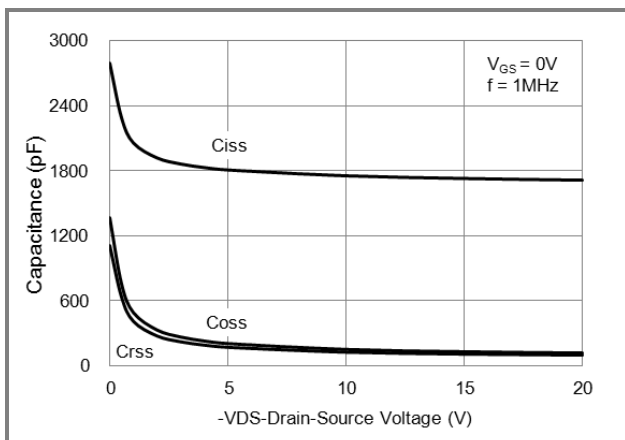


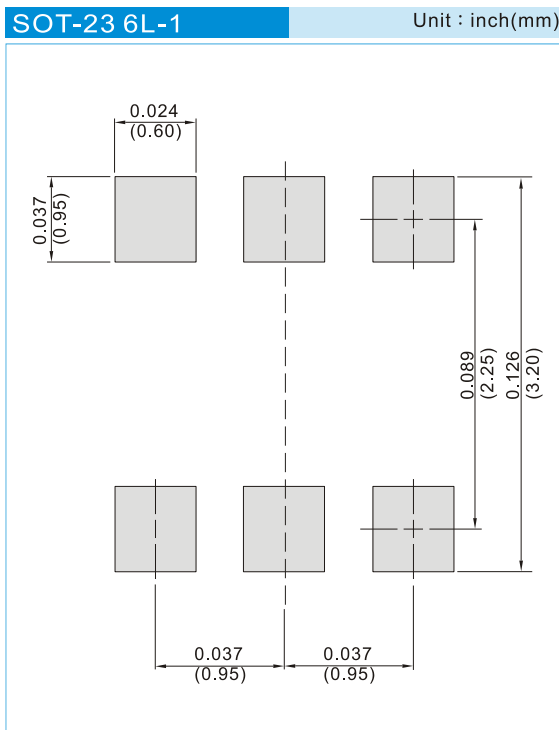
Fig.9 Threshold Voltage Variation with Temperature.

PJS6417

PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6417_S1_00001	SOT-23 6L-1	3K pcs / 7" reel	S17	Halogen free RoHS compliant

MOUNTING PAD LAYOUT





PJS6417

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