



### 20V P-Channel Enhancement Mode MOSFET

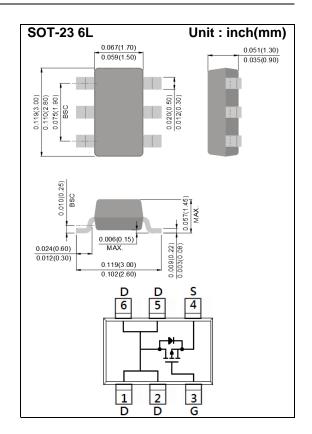
Voltage -20 V Current -5.2A

#### **Features**

- RDS(ON), VGS@-4.5V, ID@-5.2A<46mΩ
- RDS(ON) , VGS@-2.5V, ID@-3.0A<56mΩ</li>
- RDS(ON), VGS@-1.8V, ID@-1.5A<88mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case: SOT-23 6L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.014 grams



### **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	-20	V
Gate-Source Voltage		V <sub>G</sub> s	<u>+</u> 12	V
Continuous Drain Current		I <sub>D</sub>	-5.2	А
Pulsed Drain Current		I <sub>DM</sub>	-20.8	Α
Power Dissipation	T <sub>a</sub> =25°C	P <sub>D</sub>	2	W
	Derate above 25°C		16	mW/°C
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C
Typical Thermal Resistance				
- Junction to Ambient <sup>(Note 3)</sup>		$R_{\theta JA}$	62.5	°C/W





# **Electrical Characteristics** (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-0.5	-0.74	-1.3	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5.2A	-	38	46	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-3.0A	-	47	56	
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-1.5A	-	68	88	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-16V, V <sub>GS</sub> =0V	-	-	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 12V, V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	$Q_g$	V <sub>DS</sub> =-10V, I <sub>D</sub> =-5.2A, V <sub>GS</sub> =-4.5V <sup>(Note 1,2)</sup>	-	10	-	nC
Gate-Source Charge	$Q_gs$		-	1.7	-	
Gate-Drain Charge	$Q_gd$		-	2.4	-	
Input Capacitance	Ciss	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V,	-	980	-	pF
Output Capacitance	Coss		-	100	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	81	-	
Switching						
Turn-On Delay Time	td <sub>(on)</sub>	101/ 1 5 0 1	-	9.8	-	ns
Turn-On Rise Time	tr	$\begin{array}{c} V_{DD} \!$	-	54	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	44	-	
Turn-Off Fall Time	tf		-	31	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	-2.0	А
Diode Forward Voltage	$V_{\text{SD}}$	I <sub>S</sub> =-1.0A, V <sub>GS</sub> =0V	-	-0.78	-1.2	V

#### NOTES:

- 1. Pulse width<300us, Duty cycle<2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





#### **TYPICAL CHARACTERISTIC CURVES**

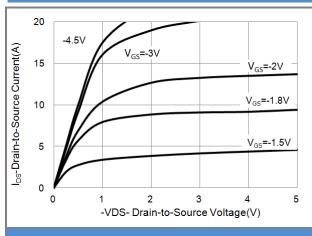
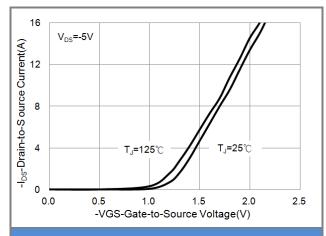


Fig.1 On-Region Characteristics



**Fig.2 Transfer Characteristics** 

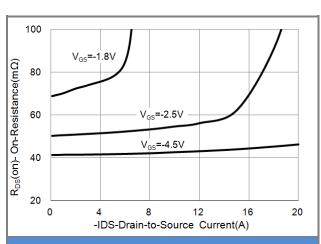


Fig.3 On-Resistance vs. Drain Current

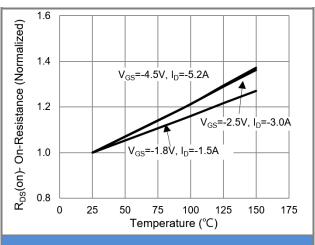
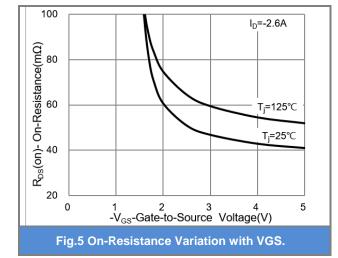
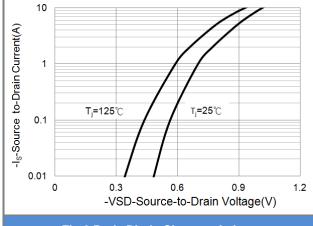


Fig.4 On-Resistance vs. Junction temperature



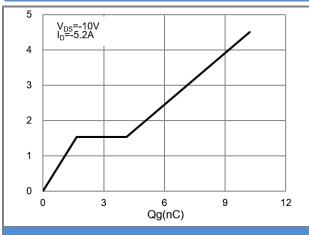


**Fig.6 Body Diode Characteristics** 





#### **TYPICAL CHARACTERISTIC CURVES**



**Fig.7 Gate-Charge Characteristics** 

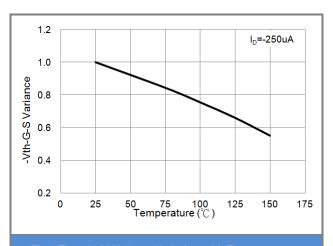


Fig.8 Threshold Voltage Variation with Temperature.

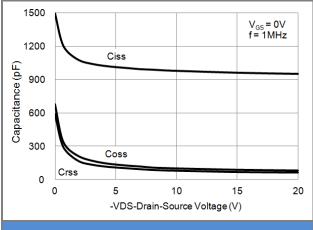


Fig.9 Capacitance vs. Drain-Source Voltage.

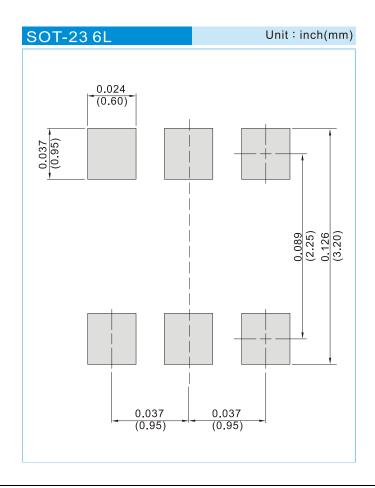




### PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6415A_S1_00001	SOT-23 6L	3K pcs / 7" reel	S5A	Halogen free RoHS compliant
PJS6415A_S2_00001	SOT-23 6L	10K pcs / 13" reel	S5A	Halogen free RoHS compliant

### **MOUNTING PAD LAYOUT**







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