	1 A A A A A A A A A A A A A A A A A A A
ΡΛΝ	JIT
	SEMI
	CONDUCTOR



Current

Features

Voltage

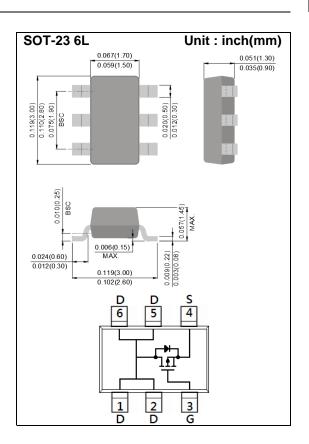
• RDS(ON) , VGS@-4.5V, ID@-4.4A<82mΩ

-20 V

- RDS(ON) , VGS@-2.5V, ID@-2.8A<110mΩ
- RDS(ON) , VGS@-1.8V, ID@-1.5A<146mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: SOT-23 6L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.014 grams
- Marking: S13



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

-4.4A

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 12	V
Continuous Drain Current		ID	-4.4	А
Pulsed Drain Current		ldм	-17.6	А
Power Dissipation	T _a =25°C	PD	2	W
	Derate above 25°C		16	mW/ºC
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		R _{θJA}	62.5	°C/W



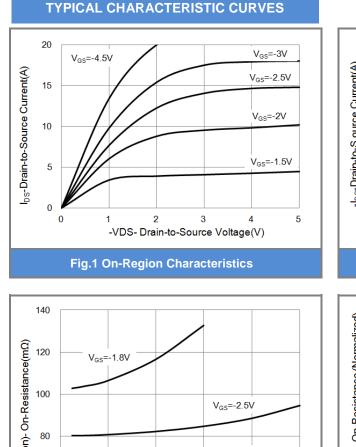
Electrical Characteristics (T_A=25°C unless otherwise noted)

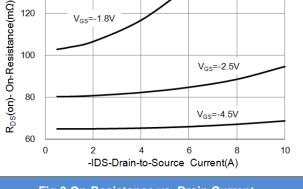
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static		·				
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-0.4	-0.65	-1.2	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-4.4A	-	65	82	mΩ
		V _{GS} =-2.5V, I _D =-2.8A	-	82	110	
		V _{GS} =-1.8V, I _D =-1.5A	-	104	146	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-20V, V _{GS} =0V	-	0.01	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Qg	V _{DS} =-10V, I _D =-4.4A, V _{GS} =-10V ^(Note 1,2)	-	7	-	nC
Gate-Source Charge	Q_{gs}		-	1.1	-	
Gate-Drain Charge	Q_gd		-	1.8	-	
Input Capacitance	Ciss	V _{DS} =-10V, V _{GS} =0V,	-	522	-	
Output Capacitance	Coss		-	55	-	pF
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	40	-	
Switching						
Turn-On Delay Time	td _(on)		-	10	-	
Turn-On Rise Time	tr	V _{DD} =-10V, I _D =-4.4A,	-	4	-	ns
Turn-Off Delay Time	td _(off)	V_{GS} =-10V,		35		
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	5	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	-2.0	А
Diode Forward Voltage	V _{SD}	I _S =-1.0A, V _{GS} =0V	-	0.75	-1.2	V

NOTES :

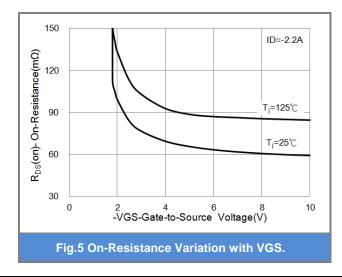
- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. ReJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited

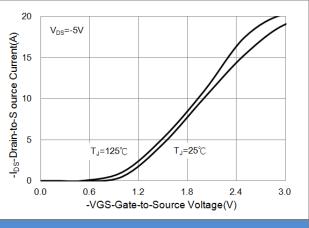














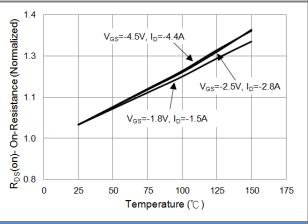
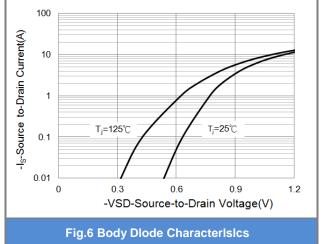
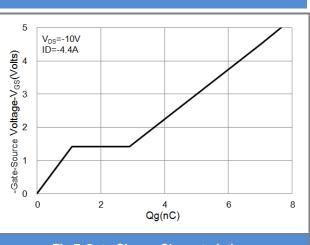


Fig.4 On-Resistance vs. Junction temperature







TYPICAL CHARACTERISTIC CURVES

Fig.7 Gate-Charge Characteristics

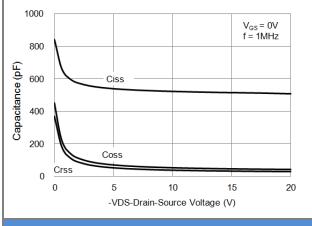


Fig.9 Capacitance vs. Drain-Source Voltage.

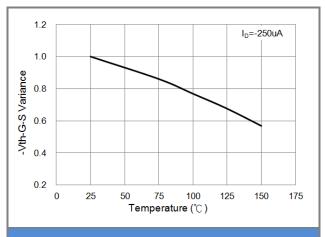
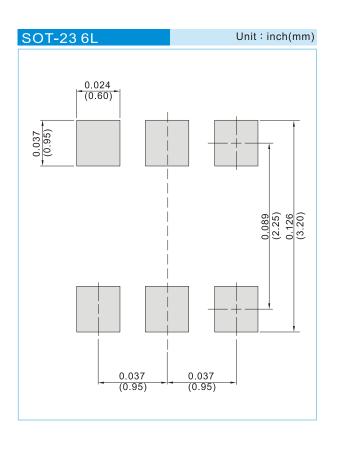


Fig.8 Threshold Voltage Variation with Temperature.



Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6413_S1_00001	SOT-23 6L	3K pcs / 7" reel	S13	Halogen free RoHS compliant
PJS6413_S2_00001	SOT-23 6L	10K pcs / 13" reel	S13	Halogen free RoHS compliant

MOUNTING PAD LAYOUT



January 20,2022





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