CONDUCTOR

### **150V N-Channel Enhancement Mode MOSFET**

Voltage

#### 40A Current

### Features

PAN

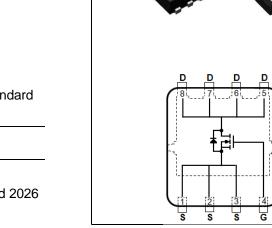
•  $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@20A<35m\Omega$ 

150 V

- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

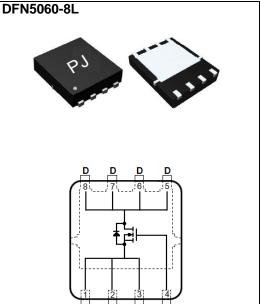
### **Mechanical Data**

- Case: DFN5060-8L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0028 ounces, 0.08 grams



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	150	
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20	V
Continuous Drain Current	T <sub>C</sub> =25°C	l <sub>D</sub>	40	
	$T_c=100^{\circ}C$		25	А
Pulsed Drain Current <sup>(Note 1)</sup>	T <sub>C</sub> =25°C	I <sub>DM</sub>	120	
Power Dissipation	T <sub>C</sub> =25°C	Po	131	
	T <sub>C</sub> =100°C		52	W
Continuous Drain Current	T <sub>A</sub> =25°C	I <sub>D</sub>	5.0	•
	T <sub>A</sub> =70°C		4.0	— A
Power Dissipation	T <sub>A</sub> =25°C	D-	2.0	
Power Dissipation	T <sub>A</sub> =70°C	PD	1.3	W
Single Pulse Avalanche Energy <sup>(Note 6)</sup>		E <sub>AS</sub>	31.5	mJ
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150	°C
Typical Thermal Resistance <sup>(Note 4,5)</sup>	Junction to Case	$R_{ extsf{ heta}JC}$	0.95	°C (M)
	Junction to Ambient	R <sub>0JA</sub>	62.5	°C/W







## **Electrical Characteristics** ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static					L	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	150	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$		2.0	3.0	4.0	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	30	35	mΩ
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =120V, $V_{GS}$ =0V	-	-	1.0	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 20V, V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 7)						
Total Gate Charge	Qg	V <sub>DS</sub> =120V, I <sub>D</sub> =30A, V <sub>GS</sub> =10V <sup>(Note 1,2)</sup>	-	52	-	nC
Gate-Source Charge	$Q_{gs}$		-	10	-	
Gate-Drain Charge	$Q_gd$		-	19	-	
Input Capacitance	Ciss	V <sub>DS</sub> =75V, V <sub>GS</sub> =0V, f=1.0MHZ	-	2207	-	pF
Output Capacitance	Coss		-	136	-	
Reverse Transfer Capacitance	Crss		-	58	-	
Turn-On Delay Time	td <sub>(on)</sub>	V <sub>DS</sub> =75V, RL=1.7Ω, V <sub>GS</sub> =10V, R <sub>G</sub> =25Ω (Note 1.2)	-	17	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	100	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	35	-	
Turn-Off Fall Time	t <sub>f</sub>		-	106	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	1		-	-	40	А
Diode Forward Current	I <sub>S</sub>					
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =1A,V <sub>GS</sub> =0V	-	0.7	1.3	V

NOTES :

- 1. Pulse width</br>200us, Duty cycle2%.
- 2. Essentially independent of operating temperature typical characteristics.
- Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
- 4. The maximum current rating is package limited.
- 5. R<sub>®JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 6. The test condition is L=0.1mH, I\_{AS}=38A, V\_{DD}=25V, V\_{GS}=10V, Starting T\_J=25^{o}C.
- 7. Guaranteed by design, not subject to production testing.

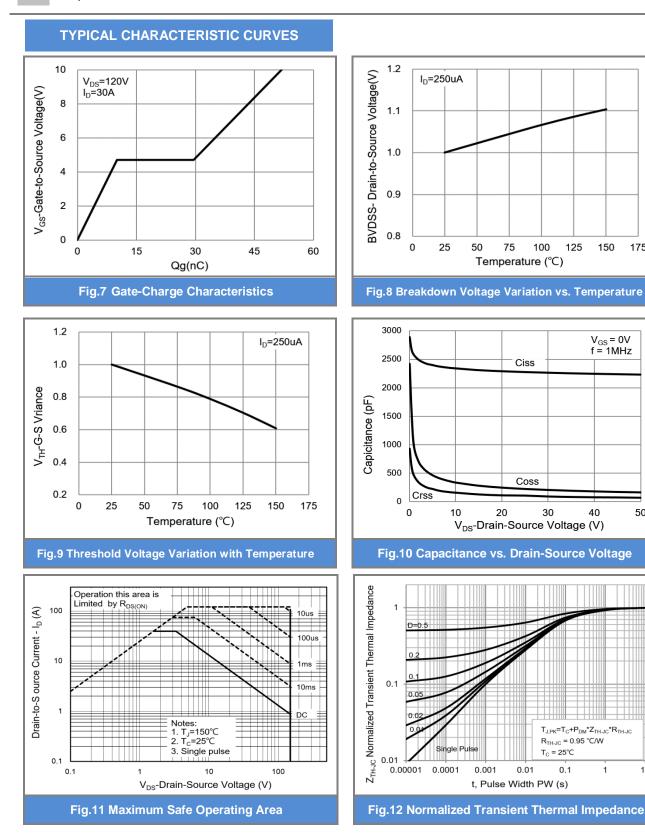




#### **PJQ5494 TYPICAL CHARACTERISTIC CURVES** 20 80 10V,8V,7V V<sub>DS</sub>=5V V<sub>GS</sub>=5V I<sub>DS</sub>-Drain-to-S ource Current(A) l<sub>DS</sub>-Drain-to-S ource Current(A) 15 60 10 40 V<sub>GS</sub>=4.5V TJ=25℃ 5 T<sub>J</sub>=125°C 20 V<sub>GS</sub>=4V 0 0 2 1 3 5 6 4 0 4 8 12 16 V<sub>DS</sub>- Drain-to-Source Voltage(V) V<sub>GS</sub>-Gate-to-Source Voltage(V) Fig.1 On-Region Characteristics **Fig.2 Transfer Characteristics** 32 2.5 R<sub>DS</sub>(on)- On-Resistance (Normalized) $R_{DS}(on)$ - On-Resistance(m $\Omega$ ) 2.0 31 V<sub>GS</sub>= 10V $V_{GS}$ =10V, $I_D$ =20A 30 1.5 1.0 29 28 0.5 0 25 50 75 100 125 150 175 0 6 12 18 24 30 Temperature (°C) I<sub>DS</sub>-Drain-to-Source Current(A) Fig.3 On-Resistance vs. Drain Current Fig.4 On-Resistance vs. Junction temperature 100 100 I<sub>D</sub>=10A l<sub>sD</sub>-Source-to-Drain Current(A) $R_{DS}(on)$ - On-Resistance(m $\Omega$ ) 80 10 60 T<sub>i</sub>=125℃ 1 т<sub>ј</sub>=25°С T<sub>i</sub>=125℃ 40 0.1 T<sub>i</sub>=25°C 20 0.01 4 5 6 8 7 0 0.9 0.3 0.6 1.2 V<sub>GS</sub>-Gate-to-Source Voltage(V) V<sub>SD</sub>-Source-to-Drain Voltage(V)

Fig.5 On-Resistance Variation with V<sub>GS</sub>





10

1

150

 $V_{GS} = 0V$ 

f = 1MHz

40

50

175

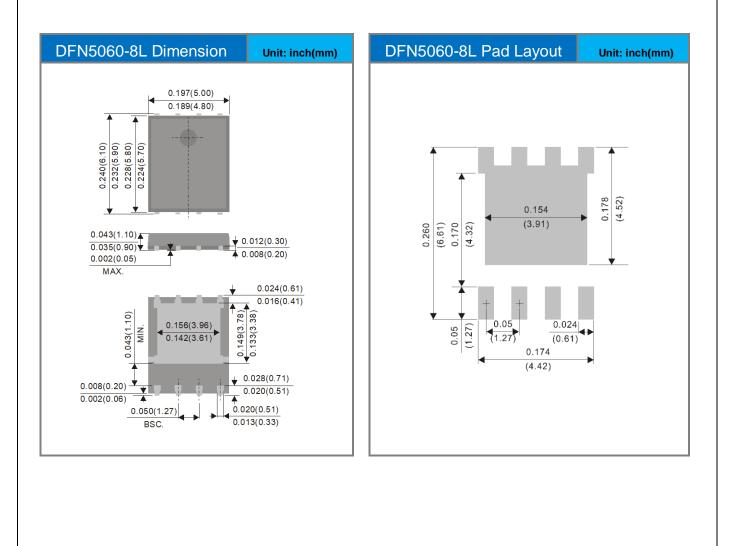




### Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJQ5494_R2_00001	DFN5060-8L	3000pcs / 13" reel	Q5494	Halogen free

### **Packaging Information & Mounting Pad Layout**





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