

# PJQ4468AP

## 60V N-Channel Enhancement Mode MOSFET

**Voltage**

**60 V**

**Current**

**18 A**

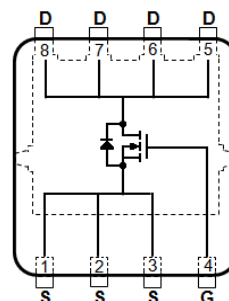
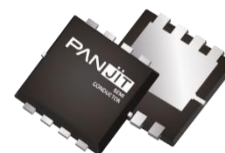
### Features

- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@10A<34m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@4.5V$ ,  $I_D@5A<40m\Omega$
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : DFN3333-8L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.03 grams

DFN3333-8L



## Maximum Ratings and Thermal Characteristics ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		$V_{DS}$	60	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	
Continuous Drain Current <sup>(Note 4)</sup>	$T_C=25^{\circ}C$	$I_D$	18	A
	$T_C=100^{\circ}C$		12	
Pulsed Drain Current <sup>(Note 1)</sup>	$T_C=25^{\circ}C$	$I_{DM}$	72	
Power Dissipation	$T_C=25^{\circ}C$	$P_D$	24	W
	$T_C=100^{\circ}C$		8	
Continuous Drain Current <sup>(Note 4)</sup>	$T_A=25^{\circ}C$	$I_D$	5	A
	$T_A=70^{\circ}C$		4	
Power Dissipation	$T_A=25^{\circ}C$	$P_D$	2	W
	$T_A=70^{\circ}C$		1.3	
Single Pulse Avalanche Energy <sup>(Note 6)</sup>		$E_{AS}$	24	mJ
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150	$^{\circ}C$
Typical Thermal Resistance <sup>(Note 4,5)</sup>	Junction to Case	$R_{\theta JC}$	6.3	$^{\circ}C/W$
	Junction to Ambient	$R_{\theta JA}$	62.5	

- Limited only By Maximum Junction Temperature

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## Electrical Characteristics ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1	1.83	2.5	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =10A	-	28	34	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	-	33	40	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	-	-	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Dynamic <sup>(Note 7)</sup>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =30V, I <sub>D</sub> =10A, V <sub>GS</sub> =10V <sup>(Note 1,2)</sup>	-	20	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	3.8	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	3.9	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHZ	-	1173	-	pF
Output Capacitance	C <sub>oss</sub>		-	63	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	44	-	
Turn-On Delay Time	td <sub>(on)</sub>	V <sub>DD</sub> =15V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω (Note 1,2)	-	7.1	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	25	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	31	-	
Turn-Off Fall Time	t <sub>f</sub>		-	20	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>	---	-	-	17	A
Reverse Recovery Time	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V	-	0.72	1	V

### NOTES :

1. Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ .
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^{\circ}\text{C}$ . Ratings are based on low frequency and duty cycles to keep initial  $T_J=25^{\circ}\text{C}$ .
4. The maximum current rating is package limited.
5.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz. square pad of copper.
6. The test condition is  $L=0.1\text{mH}$ ,  $I_{AS}=22A$ ,  $V_{DD}=25V$ ,  $V_{GS}=10V$ , Starting  $T_J=25^{\circ}\text{C}$ .
7. Guaranteed by design, not subject to production testing.

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## TYPICAL CHARACTERISTIC CURVES

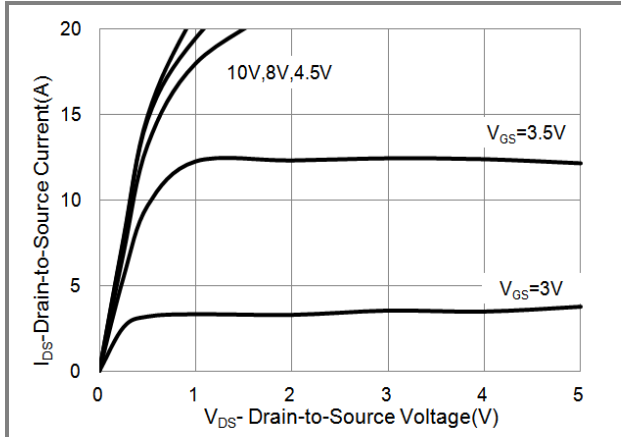


Fig.1 On-Region Characteristics

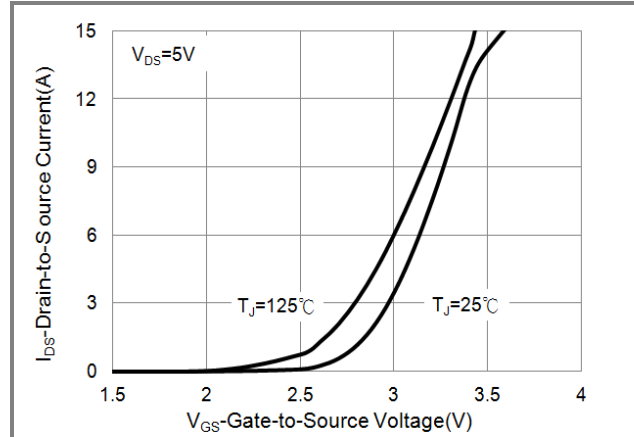


Fig.2 Transfer Characteristics

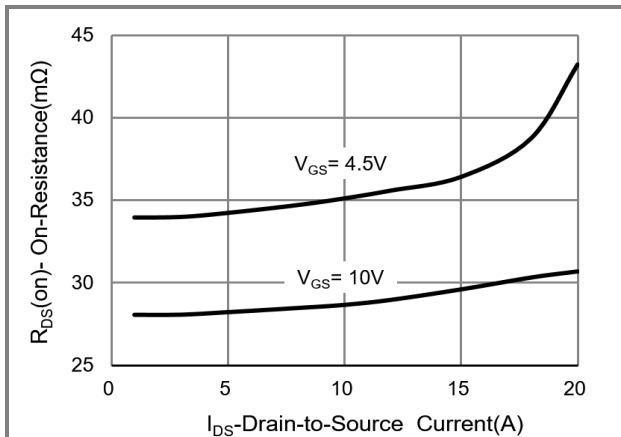


Fig.3 On-Resistance vs. Drain Current

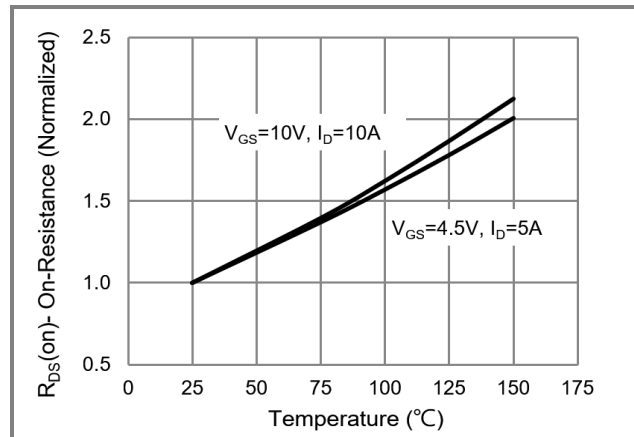


Fig.4 On-Resistance vs. Junction temperature

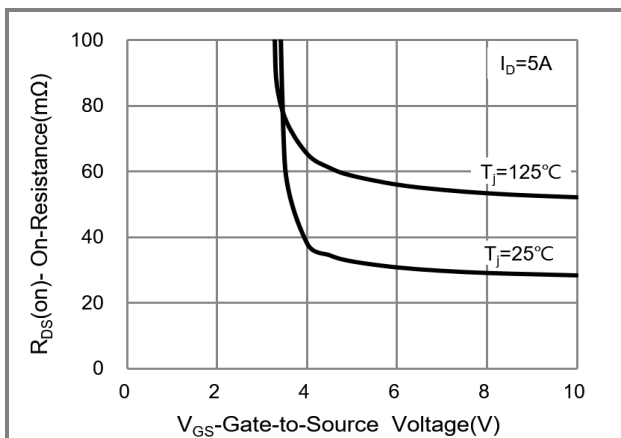


Fig.5 On-Resistance Variation with  $V_{GS}$

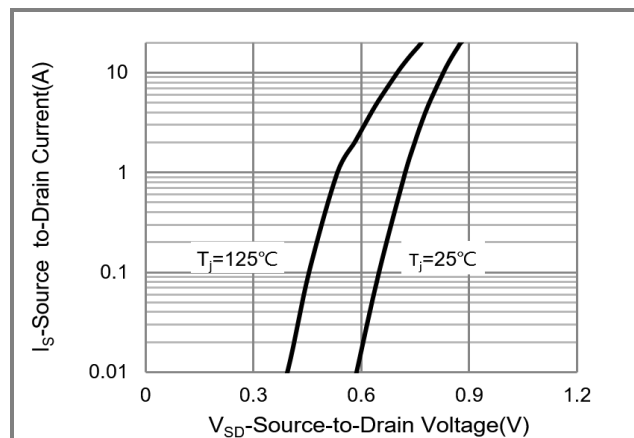
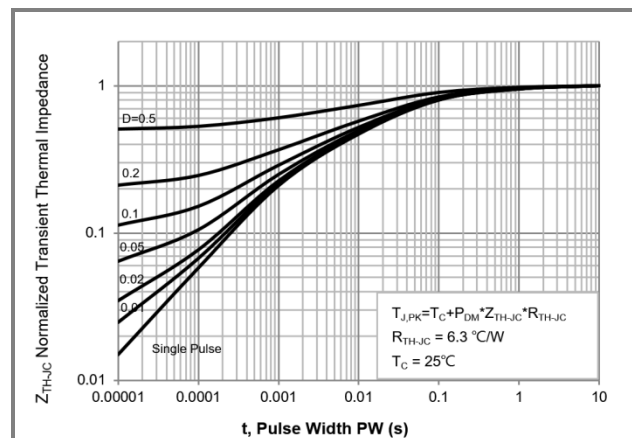
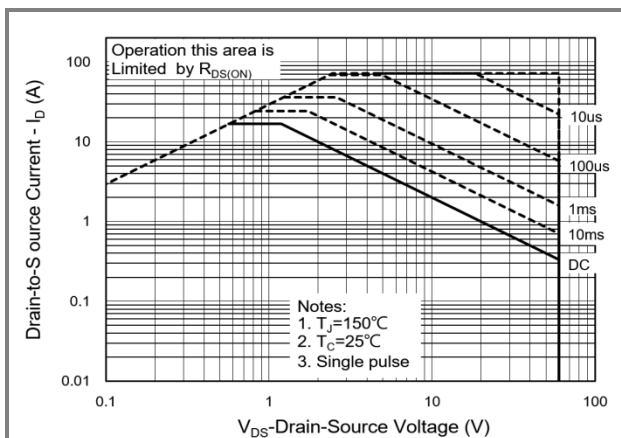
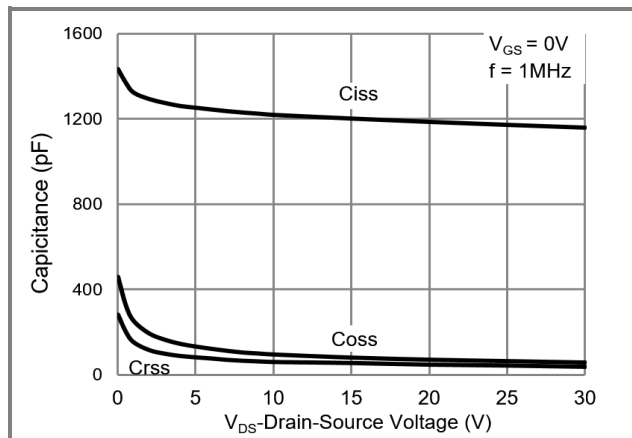
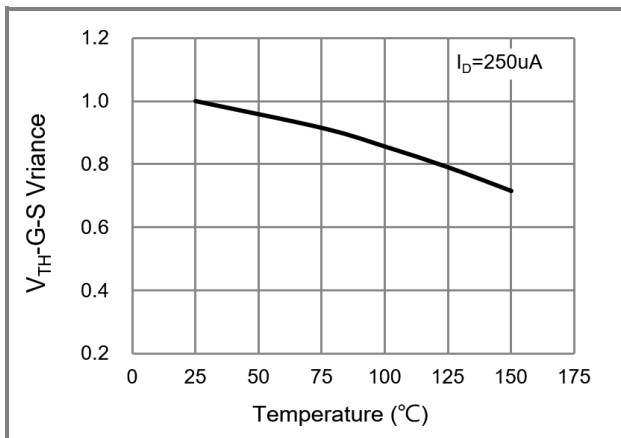
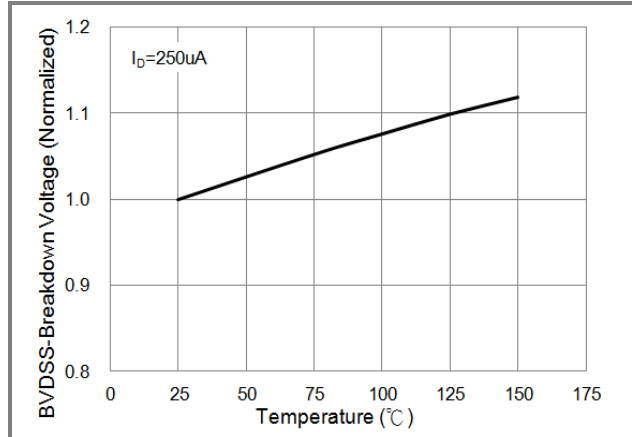
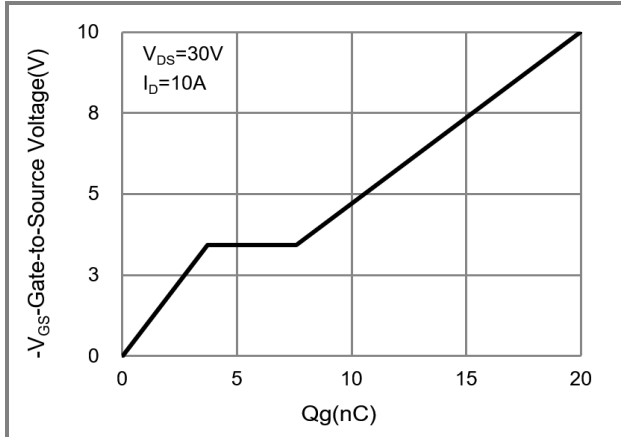


Fig.6 Source-Drain Diode Forward Voltage

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## TYPICAL CHARACTERISTIC CURVES

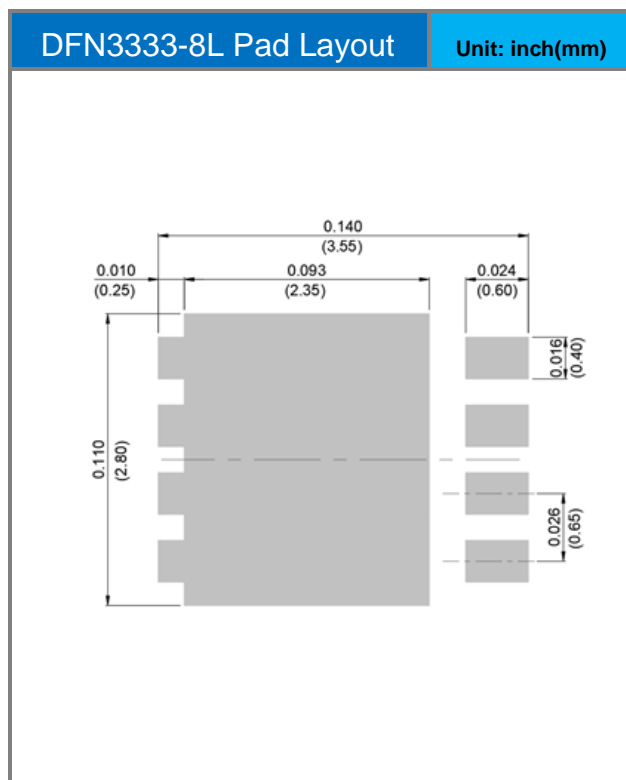
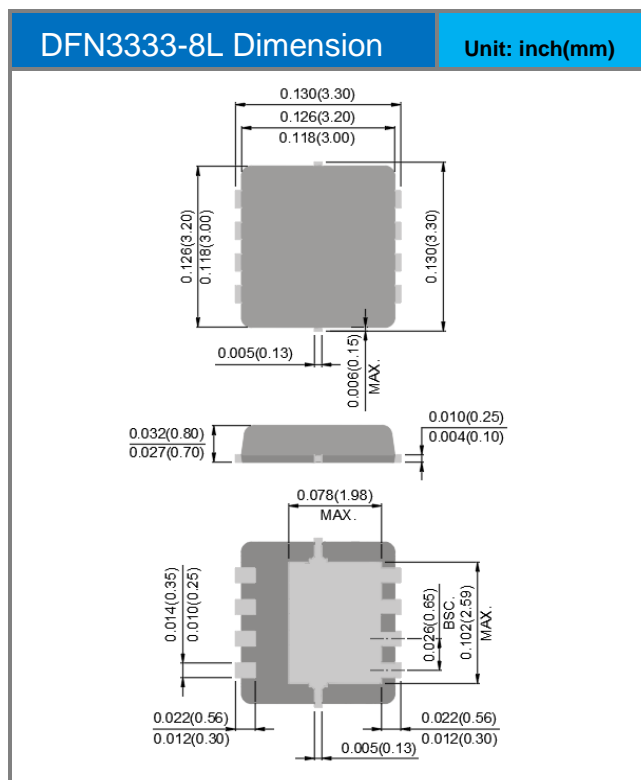


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## Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ4468AP_R2_00001	DFN3333-8L	5K pcs / 13" reel	4468	Halogen free RoHS compliant

## Packaging Information & Mounting Pad Layout



## PJQ4468AP

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