

### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	-60	- v	
Gate-Source Voltage		V <sub>GS</sub>	±20		
	Tc=25°C	- I <sub>D</sub>	-13		
Continuous Drain Current <sup>(Note 3)</sup>	$T_c=100^{\circ}C$			A	
			-9		
Pulsed Drain Current <sup>(Note 1)</sup>	T <sub>C</sub> =25°C	I <sub>DM</sub>	-24		
Power Dissipation	T <sub>c</sub> =25°C	Do	31	w	
	Tc=100°C	Po	15		
Continuous Drain Current <sup>(Note 4)</sup>	T <sub>A</sub> =25 <sup>°</sup> C		-3.9	A	
	T <sub>A</sub> =70 <sup>°</sup> C	I <sub>D</sub>	-3.2		
Power Dissipation	T <sub>A</sub> =25 <sup>°</sup> C	Po	2.5	W	
	T <sub>A</sub> =70 <sup>°</sup> C		1.8		
Single Pulse Avalanche Current <sup>(Note 5)</sup>		las	7	А	
Single Pulse Avalanche Energy <sup>(Note 5)</sup>		Eas	20	mJ	
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~175	°C	
Thermal Resistance <sup>(Note 4)</sup>	Junction to Case	$R_{ extsf{ heta}JC}$	4.8	°C/W	
	Junction to Ambient	$R_{\thetaJA}$	60		



# PJQ44611AP-AU

#### Electrical Characteristics (TA=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	- ,	-60	-	-	v
Gate Threshold Voltage	V <sub>GS(th)</sub>		-1	-1.7	-2.5	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-6A	-	67.4	84	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3A	-	90	117	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V	-	-	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Dynamic <sup>(Note 6)</sup>						
Total Gate Charge	Qg	V <sub>DS</sub> =-30V, I <sub>D</sub> =-6A, V <sub>GS</sub> =-10V	-	18	24	nC
Gate-Source Charge	Qgs		-	3	-	
Gate-Drain Charge	$Q_{gd}$		-	3.5	-	
Input Capacitance	Ciss	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V, f=1MHz	-	844	1200	pF
Output Capacitance	Coss		-	52	95	
Reverse Transfer Capacitance	Crss		-	35	65	
Gate resistance	Rg	f=1MHz	-	14	-	Ω
Turn-On Delay Time	td <sub>(on)</sub>		-	4.9	-	ns
Turn-On Rise Time	tr	V <sub>DS</sub> =-30V, I <sub>D</sub> =-6A, V <sub>GS</sub> =-10V, R <sub>G</sub> =3Ω	-	3.1	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	27	-	
Turn-Off Fall Time	tf		-	12	-	
Drain-Source Diode		·				
Diode Forward Current	Is	<sup>0</sup> 0	-	-	-13	A
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> =25°C	-	-	-24	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-6A, V <sub>GS</sub> =0V	-	-0.85	-1.3	V
Reverse Recovery Time	Trr	V <sub>DD</sub> =-30V, V <sub>GS</sub> =0V,	-	11	-	ns
Reverse Recovery Charge	Qrr	Is=-6A,dIs/dt=100A/us	-	6	-	nC

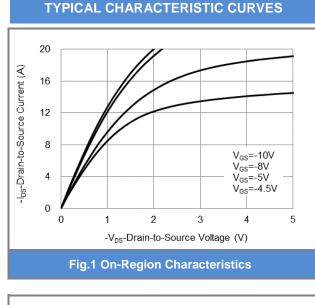
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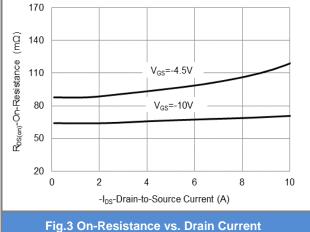
- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 5. E<sub>AS</sub> is calculated based on the condition of L=1mH, I<sub>AS</sub>=-6.3A, V<sub>DD</sub>=-30V, V<sub>GS</sub>=-10V. 100% test at L=0.5mH, I<sub>AS</sub>=-7A in production.
- 6. Guaranteed by design, not subject to production testing.

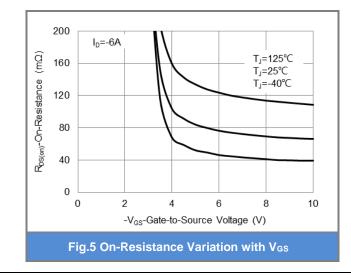
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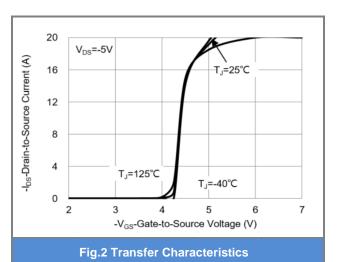
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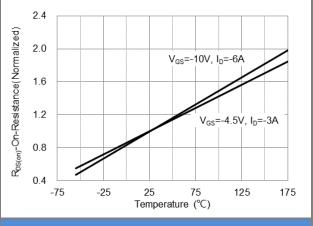
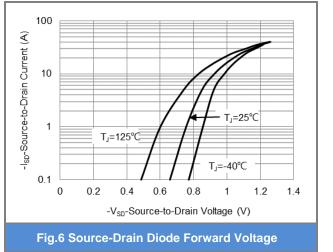


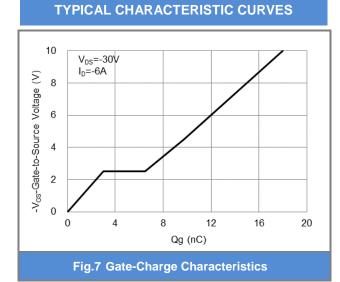
Fig.4 On-Resistance vs. Junction temperature

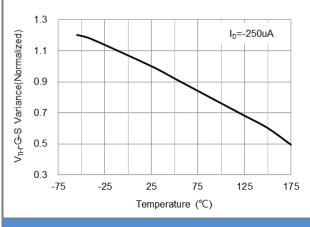


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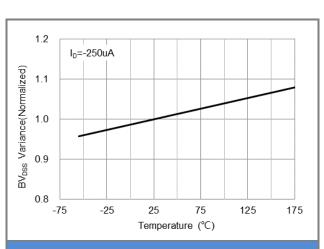
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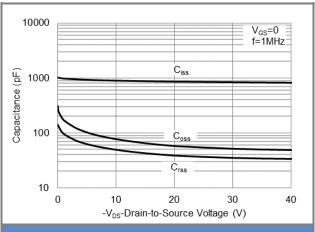
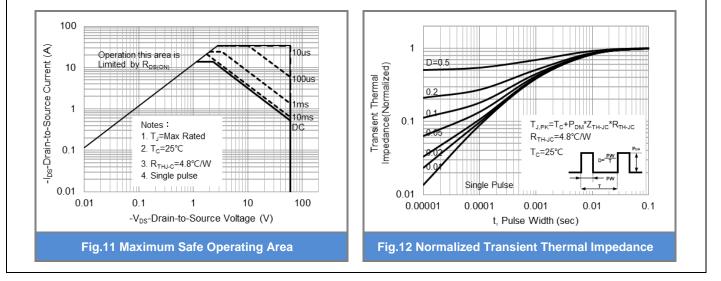


Fig.10 Capacitance vs. Drain-Source Voltage



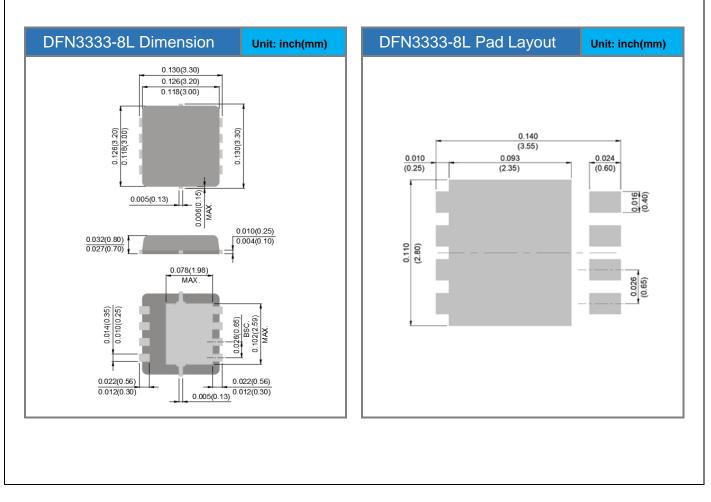


### PJQ44611AP-AU

#### **Product and Packing Information**

Part No.	Package Type	Packing Type	Marking
PJQ44611AP-AU	DFN3333-8L	5K pcs / 13" reel	44611A

### Packaging Information & Mounting Pad Layout





### PJQ44611AP-AU

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