

#### 30V N-Channel Enhancement Mode MOSFET

Voltage

30 V

Current

25 A

#### **Features**

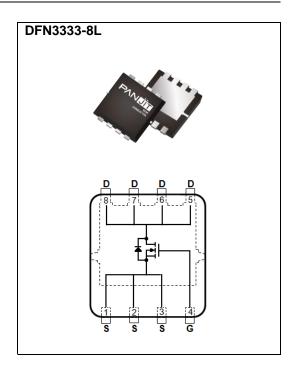
- R<sub>DS(ON)</sub>, V<sub>GS</sub>@10V,I<sub>D</sub>@9A<18mΩ</li>
- R<sub>DS(ON)</sub>, V<sub>GS</sub>@4.5V,I<sub>D</sub>@4.5A<28mΩ
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

• Case: DFN3333-8L Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.03 grams



### **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	30	V	
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20	V	
Continuous Drain Current	Tc=25°C	I <sub>D</sub>	25	А	
	Tc=100°C		16		
Pulsed Drain Current(Note 1)	Tc=25°C	I <sub>DM</sub>	100		
Power Dissipation	Tc=25°C	Po	21	W	
	Tc=100°C		8.4		
Continuous Drain Current	T <sub>A</sub> =25°C	lο	8	А	
	T <sub>A</sub> =70°C		6.5		
Power Dissipation	T <sub>A</sub> =25°C	-	2.0	107	
Power Dissipation	T <sub>A</sub> =70°C	Pb	1.3	W	
Operating Junction and Storage Temperature Range		$T_{J}$ , $T_{STG}$	-55~150	°C	
T : 1 T : (Note 4.5)	Junction to Case	Rejc	5.95	-0.44	
Typical Thermal resistance <sup>(Note 4,5)</sup>	Junction to Ambient	R <sub>0JA</sub>	62.5	°C/W	

Limited only By Maximum Junction Temperature



### **Electrical Characteristics** (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V,I <sub>D</sub> =250uA	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250uA	1.0	1.7	2.5	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V,I <sub>D</sub> =9A	-	16	18	mΩ
		V <sub>GS</sub> =4.5V,I <sub>D</sub> =4.5A	-	23	28	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1.0	uA
Gate-Source Leakage Current	Igss	V <sub>GS</sub> = <u>+</u> 20V,V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic <sup>(Note 6)</sup>						
Total Gate Charge	$Q_g$	V <sub>DS</sub> =15V, I <sub>D</sub> =8A, V <sub>GS</sub> =4.5V <sup>(Note 2,3)</sup>	-	4.3	-	nC
Gate-Source Charge	$Q_gs$		-	1.3	-	
Gate-Drain Charge	$Q_gd$		-	1.6	-	
Input Capacitance	Ciss	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V,	-	392	-	pF
Output Capacitance	Coss		-	76	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	54	-	
Turn-On Delay Time	td <sub>(on)</sub>	\/ 45\/   40	-	5.9	-	
Turn-On Rise Time	t <sub>r</sub>	$V_{DS}$ =15V, $I_{D}$ =1A, $V_{GS}$ =10V, $R_{G}$ =6 $\Omega$ (Note 2,3)	-	11	-	ns
Turn-Off Delay Time	td <sub>(off)</sub>		-	17	-	
Turn-Off Fall Time	t <sub>f</sub>	(11010 2,0)	-	3.8	-	
Drain-Source Diode						
Maximum Continuous Drain-Source			-	-	25	А
Diode Forward Current	I <sub>S</sub>					
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =1A,V <sub>GS</sub> =0V	-	0.73	1.0	V

#### NOTES:

- 1. Pulse width<300us, Duty cycle<2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub>=25°C.
- 4. The maximum current rating is package limited.
- 5. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.



#### **TYPICAL CHARACTERISTIC CURVES**

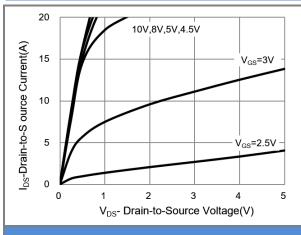


Fig.1 On-Region Characteristics

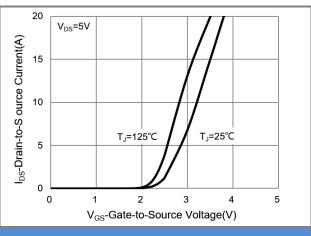


Fig.2 Transfer Characteristics

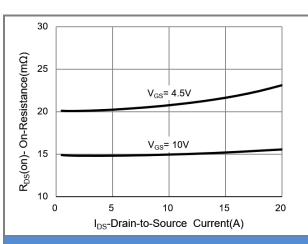


Fig.3 On-Resistance vs. Drain Current

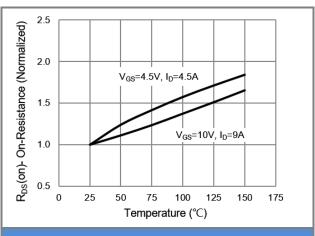


Fig.4 On-Resistance vs. Junction temperature

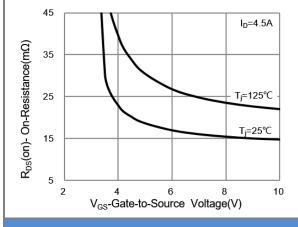


Fig.5 On-Resistance Variation with VGS.

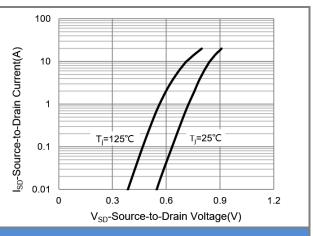


Fig.6 Source-Drain Diode Forward Voltage



#### **TYPICAL CHARACTERISTIC CURVES**

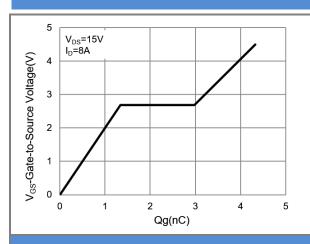


Fig.7 Gate-Charge Characteristics

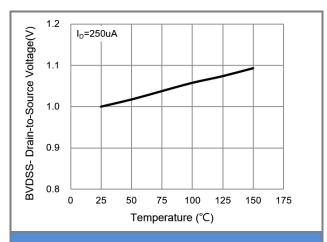


Fig.8 Breakdown Voltage Variation vs. Temperature.

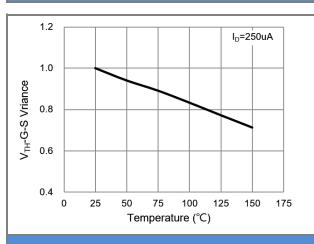


Fig.9 Threshold Voltage Variation with Temperature

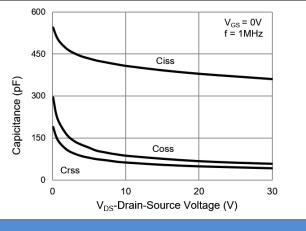


Fig.10 Capacitance vs. Drain-Source Voltage.

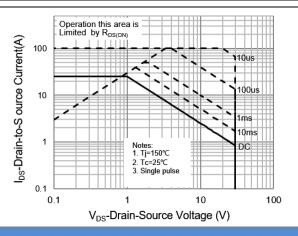


Fig.11 Maximum Safe Operating Area



#### **TYPICAL CHARACTERISTIC CURVES**

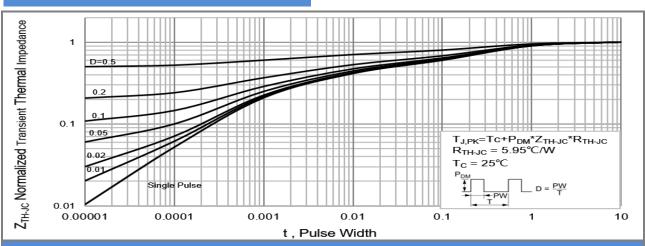


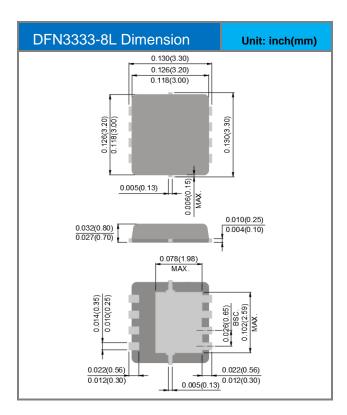
Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

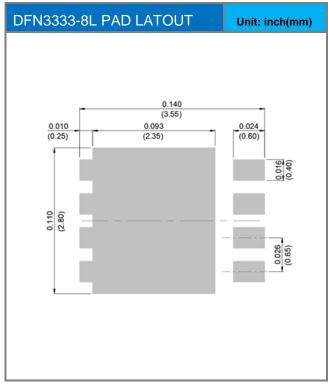


### Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ4414P_R2_00001	DFN3333-8L	5K pcs / 13" reel	4414	Halogen free RoHS compliant

### **Packaging Information & Mounting Pad Layout**







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