



# PJL9401

## 30V P-Channel Enhancement Mode MOSFET

**Voltage**

**-30 V**

**Current**

**-3.5A**

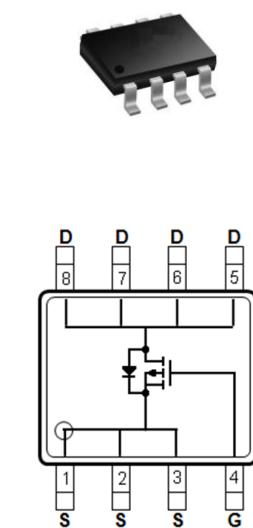
### Features

- $R_{DS(ON)}$ ,  $V_{GS} @ -10V$ ,  $I_D @ -3A < 110m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS} @ -4.5V$ ,  $I_D @ -2A < 150m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case: SOP-8 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0029 ounces, 0.083 grams

### SOP-8



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current <small><math>T_A=25^\circ C</math></small>	$I_D$	-3.5	A
		-2.8	
Pulsed Drain Current <small>(Note 1)</small>	$I_{DM}$	-14	
Power Dissipation <small><math>T_A=25^\circ C</math></small>	$P_D$	2.1	W
		1.3	
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	°C
Typical Thermal Resistance - Junction to Ambient <small>(Note 5)</small>	$R_{\theta JA}$	59.5	°C/W



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## Electrical Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	-1	-1.31	-2.1	
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-3A$	-	92	110	$m\Omega$
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-4.5V, I_D=-2A$	-	120	150	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-30V, V_{GS}=0V$	-	-	-1	$\mu A$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	$nA$
<b>Dynamic</b> (Note 6)						
Total Gate Charge	$Q_g$	$V_{DS}=-15V, I_D=-3A,$ $V_{GS}=-10V$ (Note 1,2)	-	9.8	-	nC
Gate-Source Charge	$Q_{gs}$		-	1.5	-	
Gate-Drain Charge	$Q_{gd}$		-	2.2	-	
Input Capacitance	$C_{iss}$	$V_{DS}=-15V, V_{GS}=0V,$ $f=1.0MHz$	-	396	-	pF
Output Capacitance	$C_{oss}$		-	47	-	
Reverse Transfer Capacitance	$C_{rss}$		-	36	-	
Turn-On Delay Time	$td_{(on)}$	$V_{DD}=-15V, I_D=-3A,$ $V_{GS}=-10V,$ $R_G=6\Omega$ (Note 1,2)	-	5.1	-	ns
Turn-On Rise Time	$tr$			30		
Turn-Off Delay Time	$td_{(off)}$		-	25	-	
Turn-Off Fall Time	$tf$		-	7.8	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_s$	---	-	-	-3.5	A
Diode Forward Voltage	$V_{SD}$	$I_s=1A, V_{GS}=0V$	-	-0.77	-1.2	V

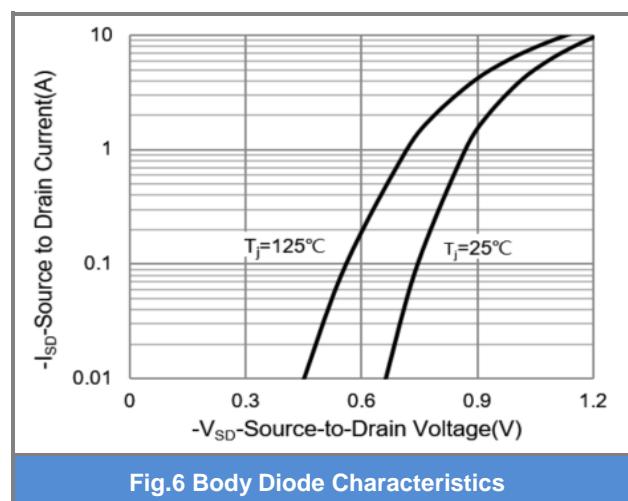
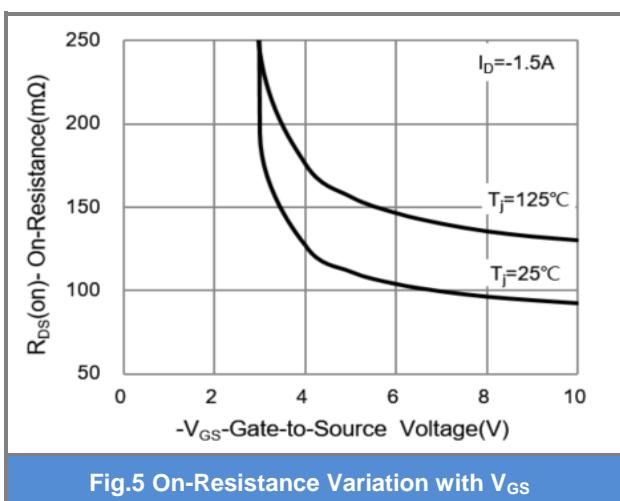
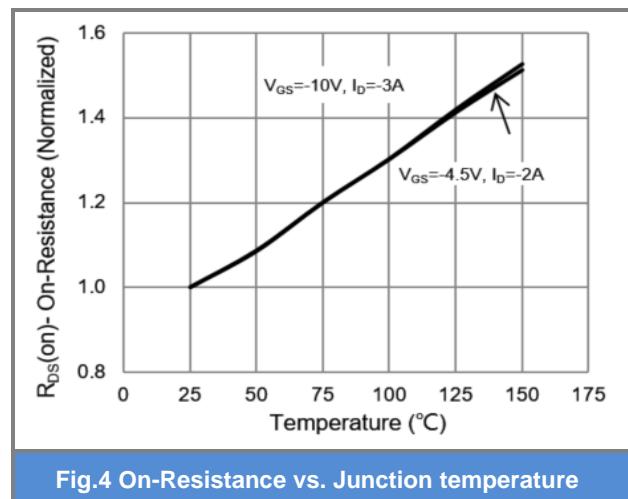
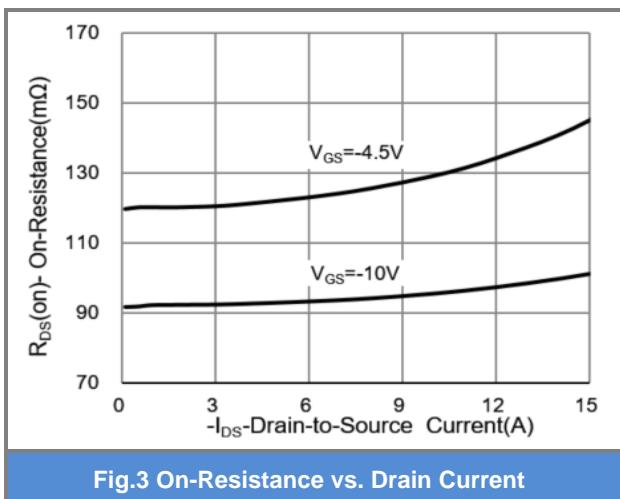
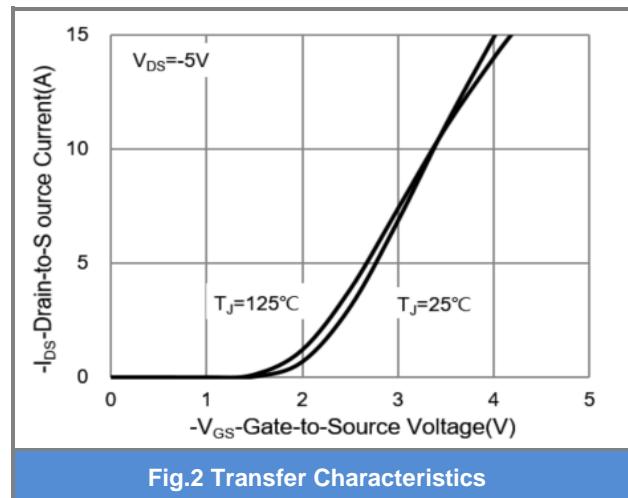
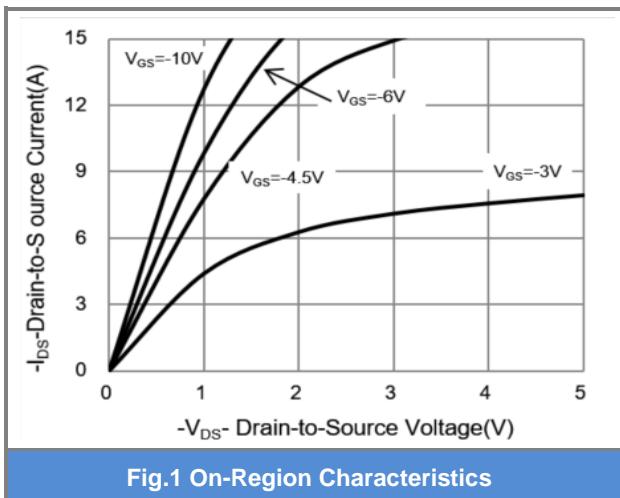
### NOTES :

1. Pulse width $\leq 300\mu s$ , Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. The maximum current rating is package limited.
4. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ C$ . Ratings are based on low frequency and duty cycles to keep initial  $T_J=25^\circ C$ .
5.  $R_{QJA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing.



# PJL9401

## TYPICAL CHARACTERISTIC CURVES





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## TYPICAL CHARACTERISTIC CURVES

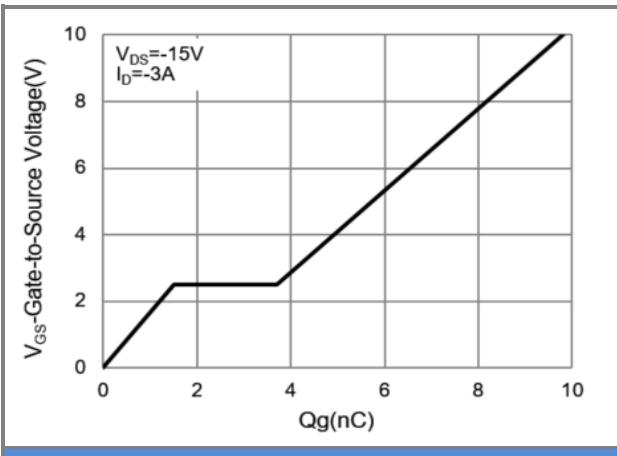


Fig.7 Gate-Charge Characteristics

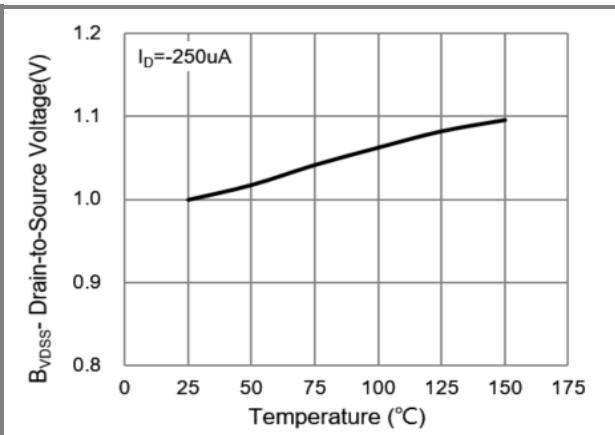


Fig.8 Breakdown Voltage Variation vs. Temperature

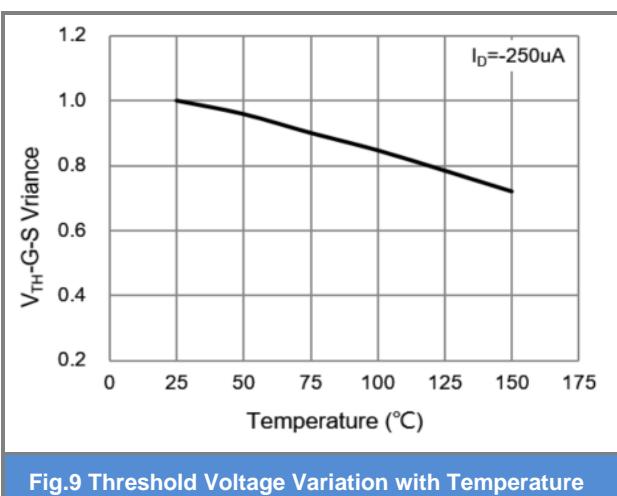


Fig.9 Threshold Voltage Variation with Temperature

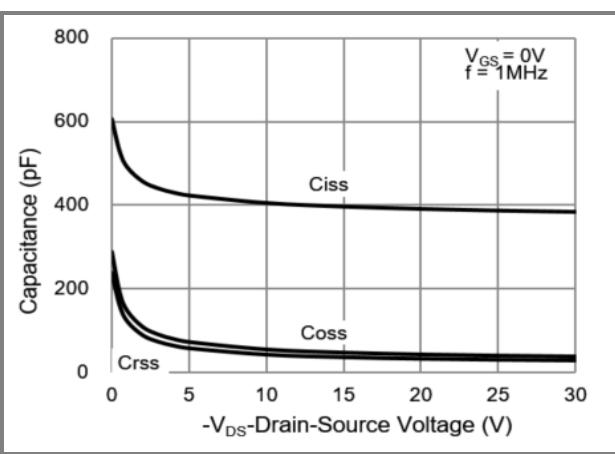


Fig.10 Capacitance vs. Drain-Source Voltage

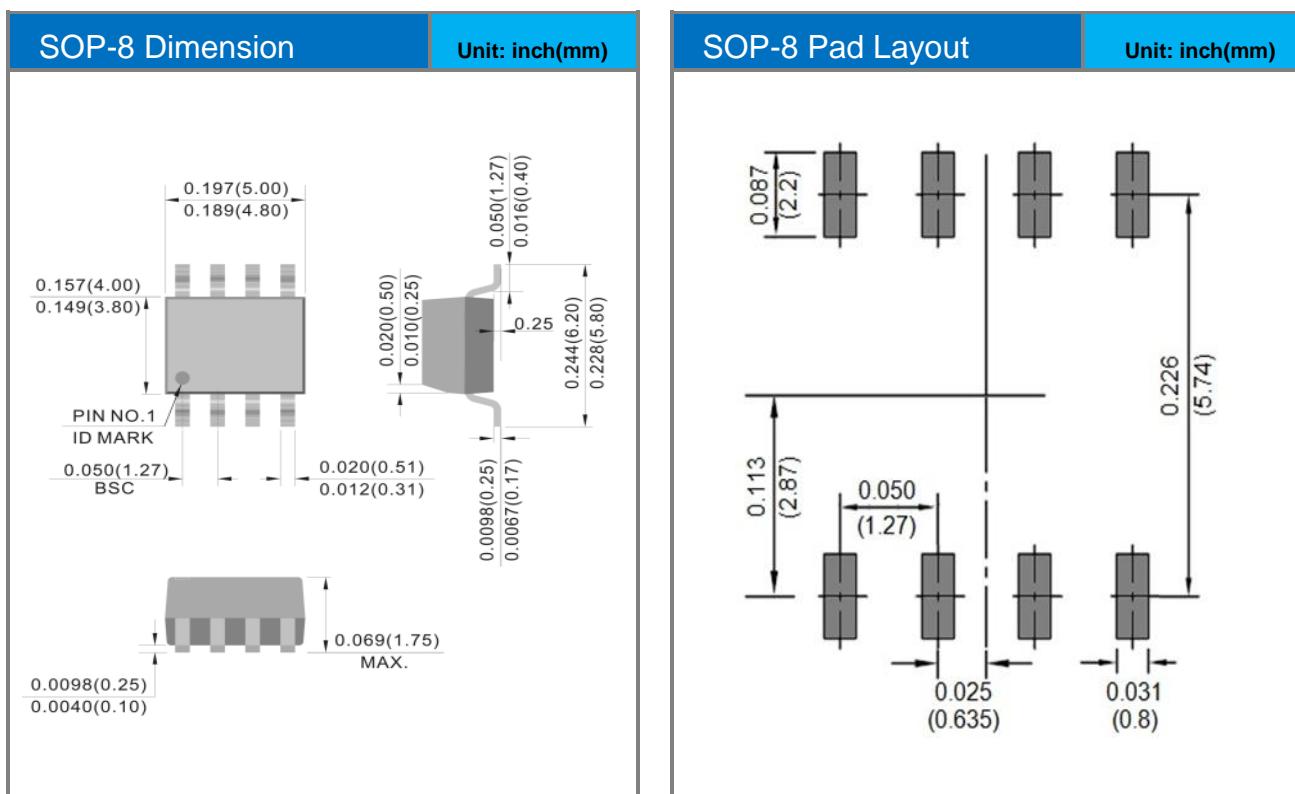


# PJL9401

## Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJL9401_R2_00001	SOP-8	2.5K pcs / 13" reel	L9401	Halogen free

## Packaging Information & Mounting Pad Layout





## PJL9401

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