



### **Very Low Capacitance TVS/ESD Protection**

V<sub>RWM</sub> 3 V

#### **Features**

- Bidirectional ESD protection of one line
- IEC61000-4-2(ESD):±30kV Air,±30kV Contact Compliance
- IEC61000-4-4(EFT):40A(5/50nS)
- IEC61000-4-5(Lightning):15A(8/20uS)
- Very Low Capacitance: 1.2 pF Maximum
- Protect one data, control or power line
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

#### **Mechanical Data**

- Case: SOD-323, Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.00014 ounces, 0.0041 grams
- Marking: B1

#### **Applications**

- Mobile Phones and accessories
- Desktops, Servers and Notebook
- Hand held portable
- Digital Cameras
- Computer Interfaces Protection
- Serial and Parallel Ports Protection
- Control Signal Lines Protection

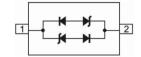


Fig.97(TOP VIEW)

# 0.078(1.95) 0.068(1.75) 0.014(0.35) 1 0.009(0.25) 0.036(0.90) 0.027(0.70) 1 0.006(0.15) 1 0.002(0.05) 0.012(0.30)MIN.

**SOD-323** 

### **Maximum Ratings** (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
ESD IEC61000-4-2(Air)	±30		137
ESD IEC61000-4-2(Contact)	V <sub>ESD</sub>	±30	kV
Operating Junction Temperature	TJ	-55 to +125	$^{\circ}\!\mathbb{C}$
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	$^{\circ}\!\mathbb{C}$





# **Electrical Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Reverse Stand-Off Voltage	$V_{RWM}$	-	-	-	3.0	V
Reverse Break Voltage	$V_{BR}$	I <sub>T</sub> =1mA	4.75	-	5.25	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>R</sub> =3.0V	-	-	20	μА
Clamping Voltage	V <sub>CL</sub>	I <sub>PP</sub> =1A, t <sub>P</sub> =8/20µ s	-	-	7.5	V
		I <sub>PP</sub> =5A, t <sub>P</sub> =8/20µ s	-	10.5	-	
Clamping Voltage TLP(Note 1)	V <sub>CL</sub>	I <sub>PP</sub> =4A, t <sub>P</sub> =100ns	-	10	-	V
		I <sub>PP</sub> =8A, t <sub>P</sub> =100ns	-	13	-	
Dynamic Resistance	R <sub>DYN</sub>	t <sub>P</sub> =100ns	-	0.75	-	Ω
Off State Junction Capacitance	CJ	0Vdc Bias f=1MHz	-	0.9	1.2	pF

#### NOTE:

1. Testing using Transmission Line Pulse (TLP) conditions:  $Z_0 = 50\Omega$  ,  $t_P = 100$  ns.





#### **TYPICAL CHARACTERISTIC CURVES**

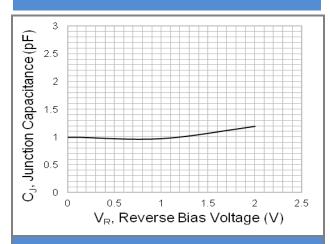


Fig.1 Typical Junction Capacitance

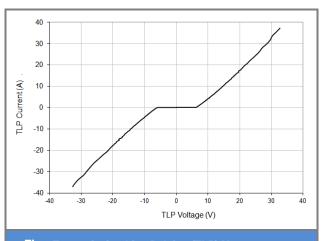


Fig.2 Transmission Line Pulsing (TLP) Measurement

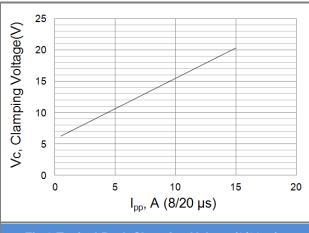
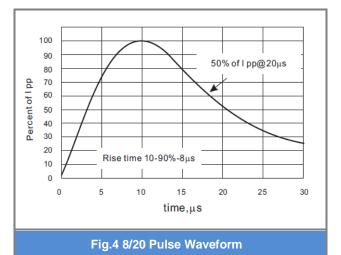


Fig.3 Typical Peak Clamping Voltage(8/20µs)



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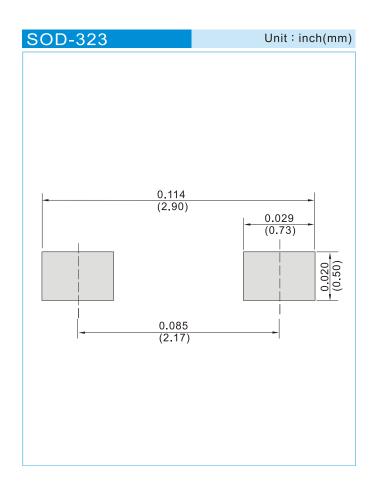




#### PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJEC3V0V1WS_R1_00001	SOD-323	5K pcs / 7" reel	B1	Halogen free
PJEC3V0V1WS_R2_00001	SOD-323	12K pcs / 13" reel	B1	Halogen free

#### **MOUNTING PAD LAYOUT**







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