

## PJD9P06A-AU

### 60V P-Channel Enhancement Mode MOSFET

**Voltage**

**-60 V**

**Current**

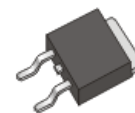
**-7 A**

#### Features

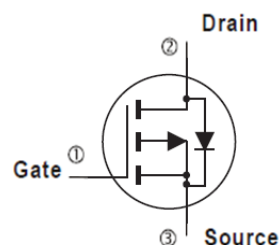
- $R_{DS(ON)}$ ,  $V_{GS}@-10V, I_D@-3.5A < 170m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@-4.5V, I_D@-2A < 220m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 Standard

#### Mechanical Data

- Case : TO-252AA Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0104 ounces, 0.297grams



TO-252AA



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		$V_{DS}$	-60	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_C=25^\circ\text{C}$	$I_D$	-7.0	A
	$T_C=100^\circ\text{C}$		-4.3	
Pulsed Drain Current (Note 1)	$T_C=25^\circ\text{C}$	$I_{DM}$	-28	
Power Dissipation	$T_C=25^\circ\text{C}$	$P_D$	15.6	W
	$T_C=100^\circ\text{C}$		6.2	
Continuous Drain Current	$T_A=25^\circ\text{C}$	$I_D$	-2.5	A
	$T_A=70^\circ\text{C}$		-2.0	A
Power Dissipation	$T_A=25^\circ\text{C}$	$P_D$	2.0	W
Power Dissipation	$T_A=70^\circ\text{C}$		1.3	
Single Pulse Avalanche Energy (Note 6)		$E_{AS}$	32	mJ
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150	$^\circ\text{C}$
Typical Thermal resistance (Note 4,5)	Junction to Case	$R_{\theta JC}$	8	$^\circ\text{C/W}$
	Junction to Ambient	$R_{\theta JA}$	62.5	

- Limited only By Maximum Junction Temperature



## PJD9P06A-AU

### Electrical Characteristics ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1.0	-1.88	-2.5	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-3.5A	-	150	170	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A	-	190	220	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V	-	-	-1.0	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Dynamic (Note 7)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-30V, I <sub>D</sub> =-3A, V <sub>GS</sub> =-10V (Note 2,3)	-	8.3	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.8	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	1.6	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V, f=1.0MHZ	-	430	-	pF
Output Capacitance	C <sub>oss</sub>		-	33	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	29	-	
Turn-On Delay Time	td <sub>(on)</sub>	V <sub>DS</sub> =-30V, I <sub>D</sub> =-1.0A, V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω (Note 2,3)	-	5.1	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	20	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	36	-	
Turn-Off Fall Time	t <sub>f</sub>		-	11	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>	---	-	-	-7	A
Reverse Recovery Time	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V	-	-0.76	-1.0	V

#### NOTES :

1. Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics
3. Repetitive rating, pulse width limited by junction temperature  $T_J(\text{MAX})=150^{\circ}\text{C}$ . Ratings are based on low frequency and duty cycles to keep initial  $T_J=25^{\circ}\text{C}$ .
4. The maximum current rating is package limited
5.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper
6.  $L=1\text{mH}$ ,  $I_{AS}=-8A$ ,  $V_{GS}=-10V$ ,  $V_{DS}=-25V$ ,  $R_G=25\text{ ohm}$
7. Guaranteed by design, not subject to production testing.

## PJD9P06A-AU

### TYPICAL CHARACTERISTIC CURVES

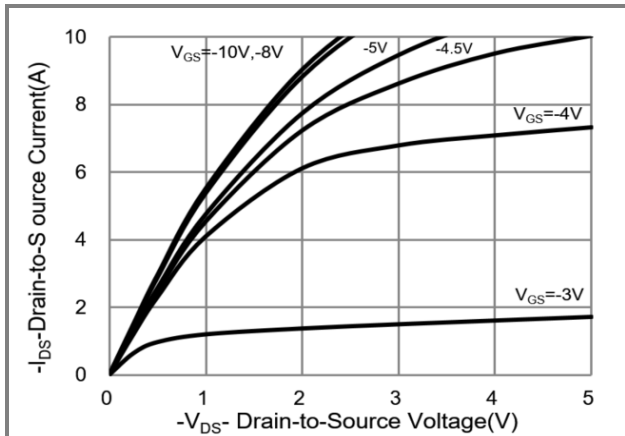


Fig.1 On-Region Characteristics

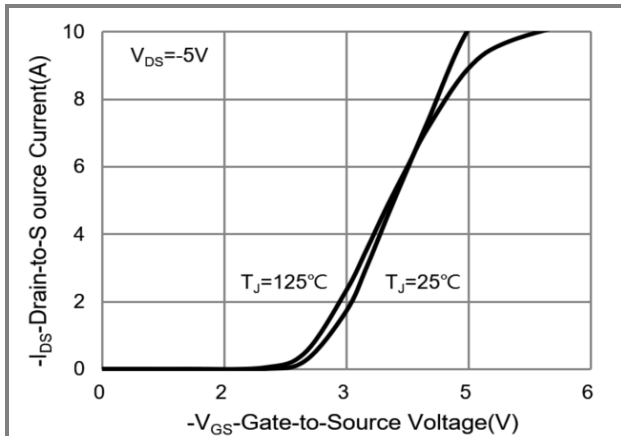


Fig.2 Transfer Characteristics

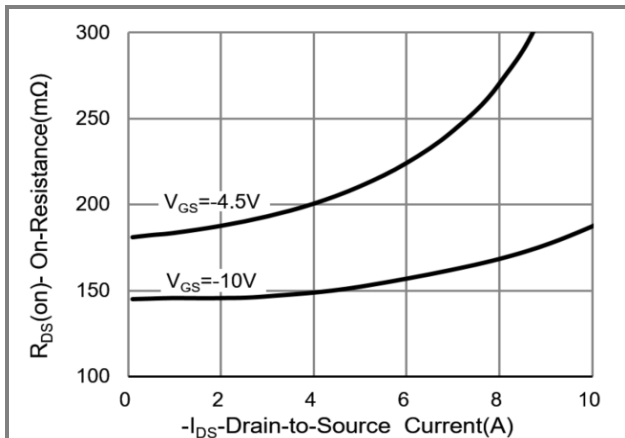


Fig.3 On-Resistance vs. Drain Current

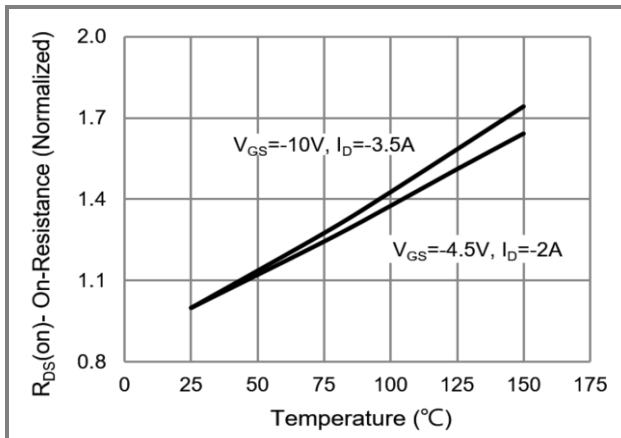


Fig.4 On-Resistance vs. Junction temperature

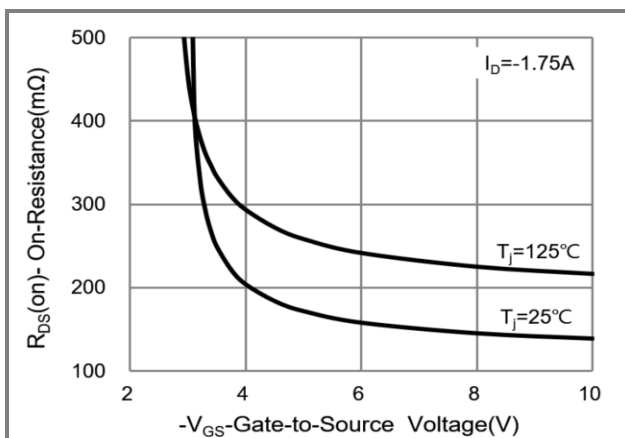


Fig.5 On-Resistance Variation with V\_GS.

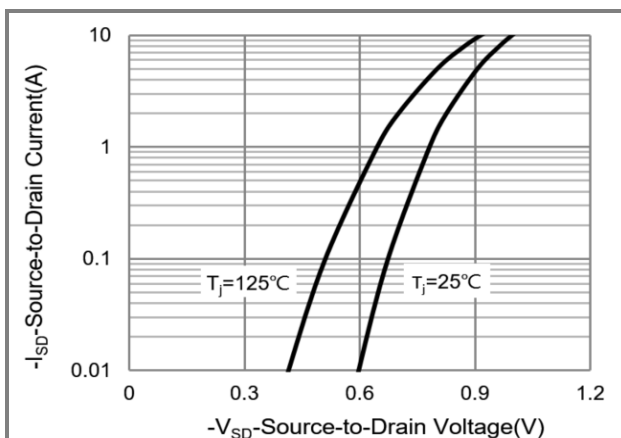
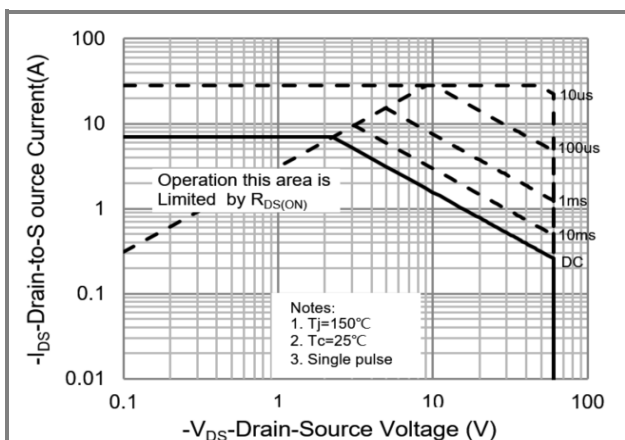
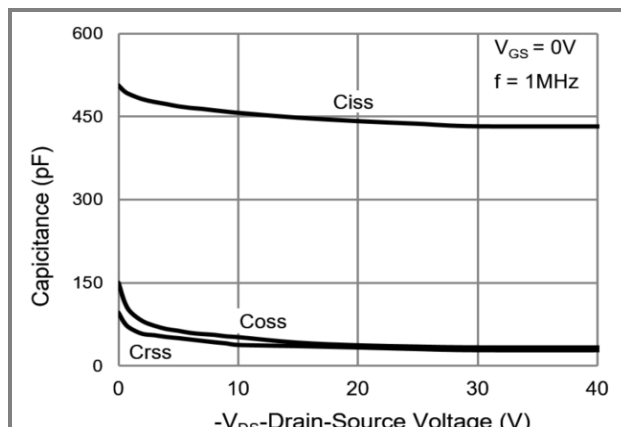
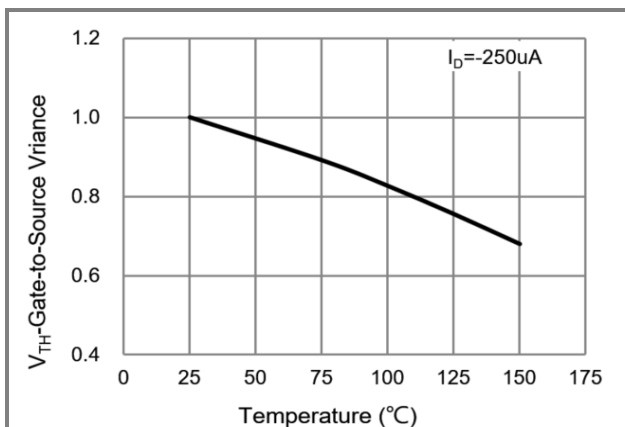
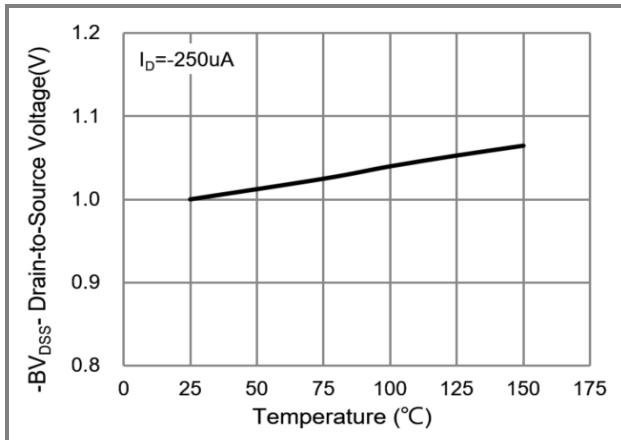
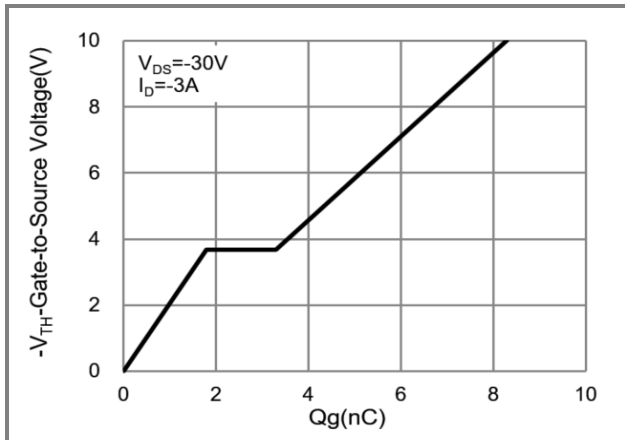


Fig.6 Body Diode Characteristics

## PJD9P06A-AU

### TYPICAL CHARACTERISTIC CURVES





## PJD9P06A-AU

### TYPICAL CHARACTERISTIC CURVES

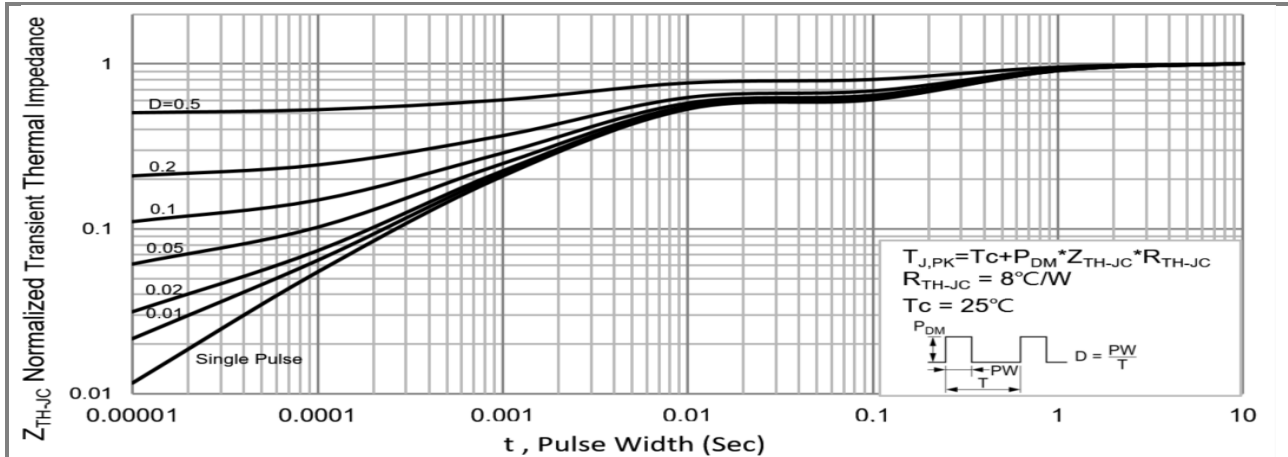


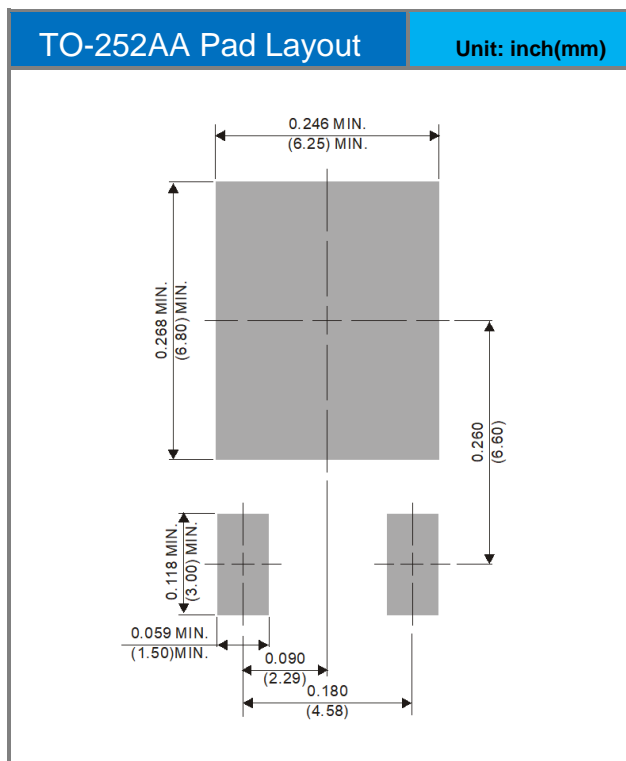
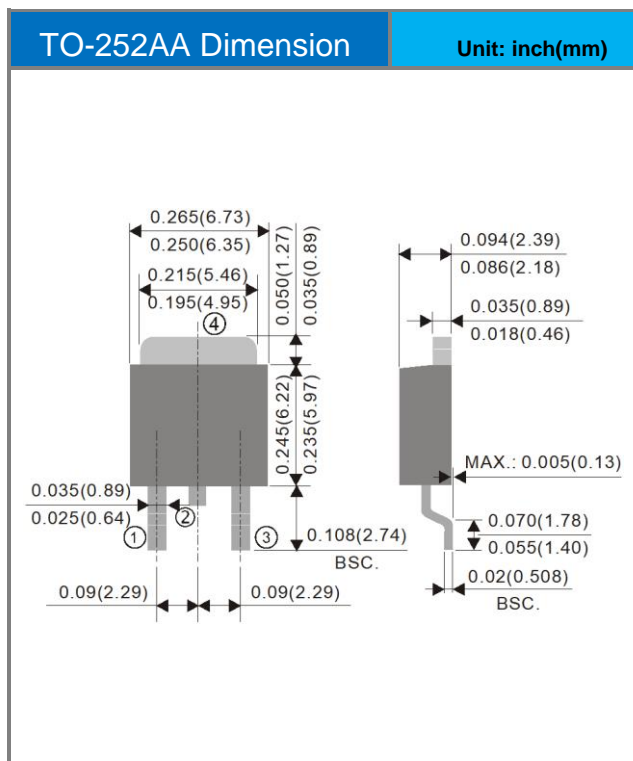
Fig.12 Normalized Thermal Transient Impedance

## PJD9P06A-AU

### Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJD9P06A-AU_L2_000A1	TO-252AA	3,000pcs / 13" reel	D9P06A	Halogen free

### Packaging Information & Mounting Pad Layout





## **PJD9P06A-AU**

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