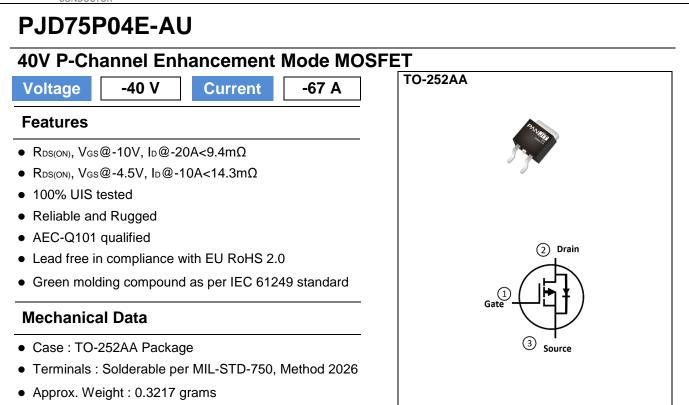
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#### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETE	R	SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	-40	V
Gate-Source Voltage		V <sub>GS</sub>	±25	
Continuous Drain Current <sup>(Note 3)</sup>	Tc=25°C		-67	
	T <sub>c</sub> =100 <sup>°</sup> C	ID	-47	А
Pulsed Drain Current <sup>(Note 1)</sup>	T <sub>C</sub> =25°C	I <sub>DM</sub>	-228	
Power Dissipation	T <sub>C</sub> =25°C	<b>D</b> _	75	10/
	Tc=100°C	Po	38	W
Continuous Drain Current <sup>(Note 4)</sup>	T <sub>A</sub> =25°C		-13.3	^
	T <sub>A</sub> =70°C	ID	-11	— A
Power Dissipation	T <sub>A</sub> =25 <sup>°</sup> C	Po	3	w
	T <sub>A</sub> =70 <sup>°</sup> C	PD	2.1	vv
Single Pulse Avalanche Energy <sup>(Note 5)</sup>		Eas	132	mJ
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~175	°C
Thermal Resistance <sup>(Note 4)</sup>	Junction to Case	R <sub>θJC</sub>	2	°C/W
	Junction to Ambient	R <sub>0JA</sub>	50	



## PJD75P04E-AU

#### Electrical Characteristics (TA=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static	4					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-40	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1	-1.9	-2.5	
Drain-Source On-State Resistance		V <sub>GS</sub> =-10V, I <sub>D</sub> =-20A	- 7.5 9.4		9.4	
	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-10A	-	11	14.3	mΩ
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =-40V, $V_{GS}$ =0V	-	-	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	$V_{GS}=\pm 25V, V_{DS}=0V$	-	-	±100	nA
Dynamic <sup>(Note 6)</sup>						
Total Gate Charge	Qg		-	59	-	
Gate-Source Charge	Qgs	V <sub>DS</sub> =-32V, I <sub>D</sub> =-20A, V <sub>GS</sub> =-10V	-	9	-	nC
Gate-Drain Charge	$Q_gd$	V <sub>GS</sub> =-10V	-	20	-	
Input Capacitance	Ciss		-	3477	-	pF
Output Capacitance	Coss	V <sub>DS</sub> =-25V, V <sub>GS</sub> =0V, f=1MHz	-	377	-	
Reverse Transfer Capacitance	Crss		-	233	-	
Gate resistance	Rg	f=1MHz	-	4	-	Ω
Turn-On Delay Time	td <sub>(on)</sub>		-	13	-	
Turn-On Rise Time	tr	$V_{DS}$ =-32V, $I_{D}$ =-20A,	-	16	-	]
Turn-Off Delay Time	td <sub>(off)</sub>	V <sub>GS</sub> =-10V, R <sub>G</sub> =3Ω	-	54	-	ns
Turn-Off Fall Time	tf		-	33	-	
Drain-Source Diode						
Diode Forward Current	I <sub>S</sub>	Tc=25°C	-	-	-67	
Pulsed Diode Forward Current	I <sub>SM</sub>	Tc=25 C	-	-	-228	A
Diode Forward Voltage	V <sub>SD</sub>	Is=-20A, V <sub>GS</sub> =0V	-	-0.85	-1.3	V
Reverse Recovery Time	Trr	V <sub>GS</sub> =0V, I <sub>S</sub> =-20A	-	23	-	ns
Reverse Recovery Charge	Qrr	dls/dt=100A/us	-	11	-	nC

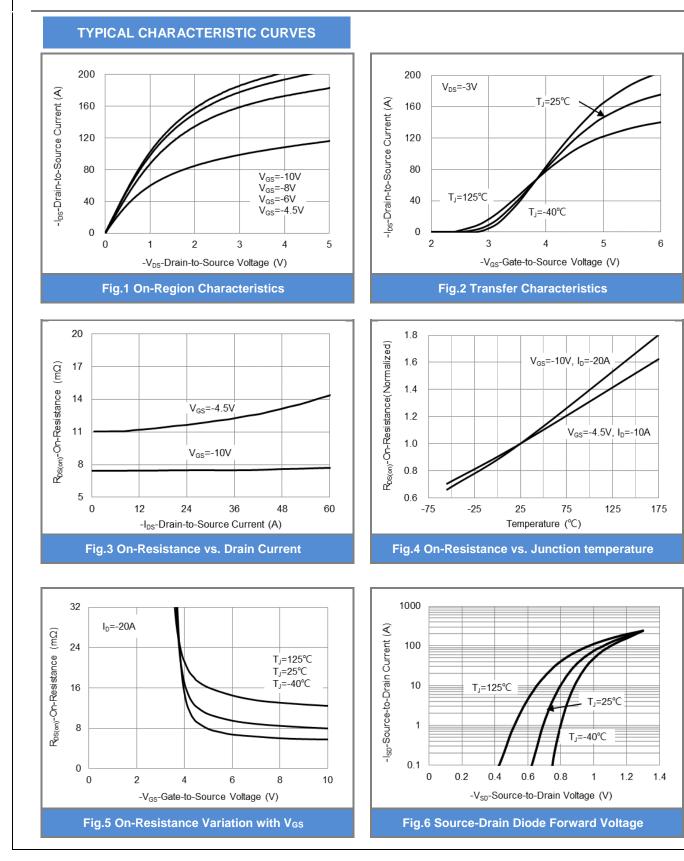
NOTES :

- 1. Pulse width <300us, Duty cycle <2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 5. The test condition is L=0.5mH,  $I_{AS}$ =-23A,  $V_{DD}$ =-30V,  $V_{GS}$ =-10V, Starting  $T_J$ =25°C.
- 6. Guaranteed by design, not subject to production testing.

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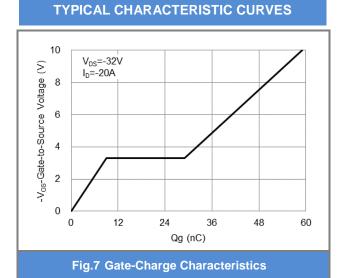


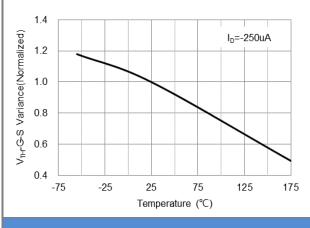
September 11,2023

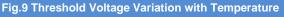
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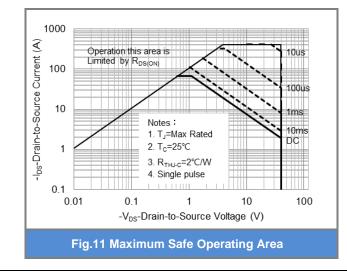
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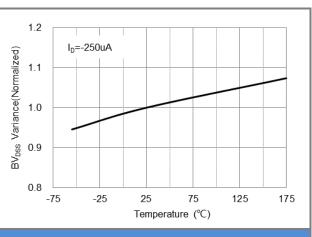
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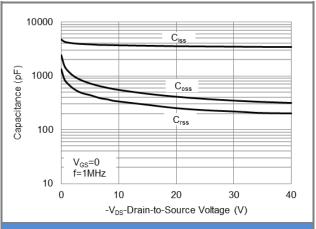
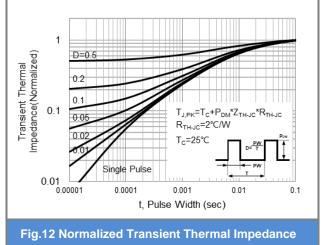


Fig.10 Capacitance vs. Drain-Source Voltage



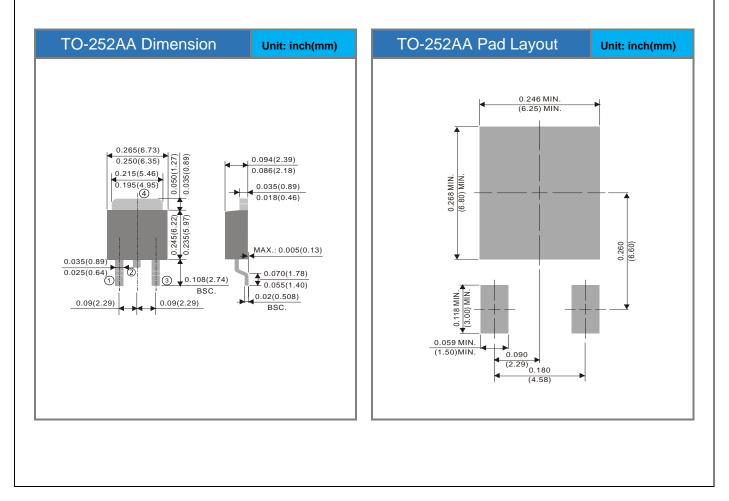


### PJD75P04E-AU

#### **Product and Packing Information**

Part No.	Package Type	Packing Type	Marking	
PJD75P04E-AU	TO-252AA	3K pcs / 13" reel	D75P04E	

#### Packaging Information & Mounting Pad Layout





### PJD75P04E-AU

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