



### **60V N-Channel Enhancement Mode MOSFET**

Voltage

60 V

Current

25 A

#### **Features**

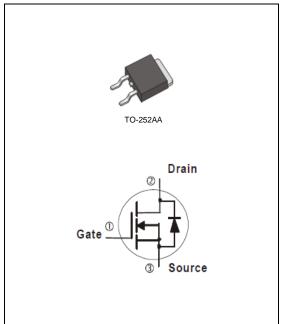
- $R_{DS(ON)}$ ,  $V_{GS}@10V$ , $I_{D}$  $\overline{@15A<34m\Omega}$
- $R_{DS(ON)}$ ,  $V_{GS}@4.5V$ , $I_{D}@10A<40m\Omega$
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

• Case : TO-252AA Package

Terminals : Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.0104 ounces, 0.297grams



# Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		$V_{DS}$	60	V	
Gate-Source Voltage		$V_{GS}$	<u>+</u> 20	V	
Continuous Drain Current	T <sub>C</sub> =25°C	I <sub>D</sub>	25	A	
	T <sub>C</sub> =100°C		16		
Pulsed Drain Current(Note 1)	T <sub>C</sub> =25°C	I <sub>DM</sub>	100		
Power Dissipation	T <sub>C</sub> =25°C	Po	48.4	W	
	T <sub>C</sub> =100°C		24.2		
Continuous Drain Current	T <sub>A</sub> =25°C	I <sub>D</sub>	5.5	А	
	T <sub>A</sub> =70°C		4.4	А	
Power Dissipation	T <sub>A</sub> =25°C	1	2.4	W	
Power Dissipation	T <sub>A</sub> =70°C	Pb	1.6		
Single Pulse Avalanche Energy (Note 6)		E <sub>AS</sub>	24	mJ	
Operating Junction and Storage Temperature Range		$T_J$ , $T_{STG}$	-55~175	°C	
Typical Thermal resistance <sup>(Note 4,5)</sup>	Junction to Case	$R_{ heta JC}$	3.1	°C/W	
	Junction to Ambient	$R_{\theta JA}$	62.5		

Limited only By Maximum Junction Temperature





### **Electrical Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS		
Static								
Drain-Source Breakdown Voltage	$BV_{DSS}$	V <sub>GS</sub> =0V,I <sub>D</sub> =250uA	60	-	-	V		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250uA$	1.0	1.83	2.5	V		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V,I <sub>D</sub> =15A	-	28	34	mΩ		
		V <sub>GS</sub> =4.5V,I <sub>D</sub> =10A		33	40			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =60V, $V_{GS}$ =0V	-	-	1.0	uA		
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\underline{+}20V, V_{DS}=0V$	-	-	<u>+</u> 100	nA		
Dynamic (Note 7)								
Total Gate Charge	$Q_{g}$	V <sub>DS</sub> =30V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V <sup>(Note 1,2)</sup>	-	20	-	nC		
Gate-Source Charge	$Q_gs$		-	3.8	-			
Gate-Drain Charge	$Q_{gd}$		-	3.9	-			
Input Capacitance	Ciss	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHZ	-	1173	-	pF		
Output Capacitance	Coss		-	63	-			
Reverse Transfer Capacitance	Crss	I=1.0IVIMZ	-	44	-			
Turn-On Delay Time	td <sub>(on)</sub>	\/ 45\/   45	-	7.1	-	ns		
Turn-On Rise Time	t <sub>r</sub>	$V_{DD}$ =15V, $I_{D}$ =1A, $V_{GS}$ =10V, $R_{G}$ =6 $\Omega$ (Note 1,2)	-	25	-			
Turn-Off Delay Time	td <sub>(off)</sub>		-	31	-			
Turn-Off Fall Time	t <sub>f</sub>		-	20	-			
Drain-Source Diode								
Maximum Continuous Drain-Source					25	Λ .		
Diode Forward Current	I <sub>S</sub>		-	-	25	Α		
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =1A,V <sub>GS</sub> =0V	-	0.72	1.2	V		

#### NOTES:

- 1. Pulse width<a>300us</a>, Duty cycle<2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
- 4. The maximum current rating is package limited.
- 5. Roja is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. The test condition is L=0.1mH,  $I_{AS}$ =22A,  $V_{DD}$ =25V,  $V_{GS}$ =10V
- 7. Guaranteed by design, not subject to production testing.





#### TYPICAL CHARACTERISTIC CURVES

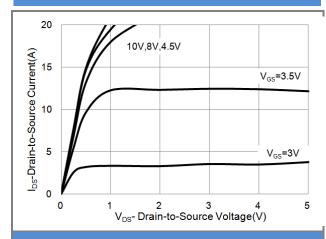
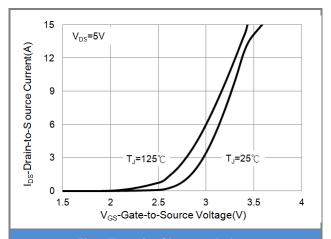


Fig.1 Output Characteristics



**Fig.2 Transfer Characteristics** 

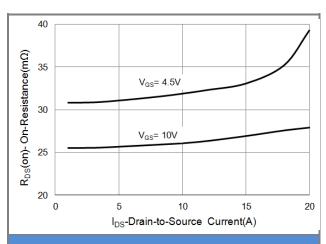


Fig.3 On-Resistance vs. Drain Current

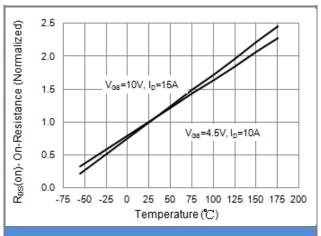


Fig.4 On-Resistance vs. Junction temperature

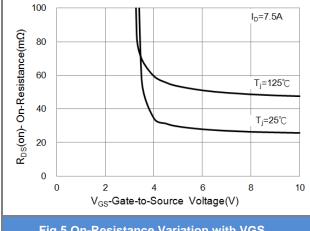


Fig.5 On-Resistance Variation with VGS.

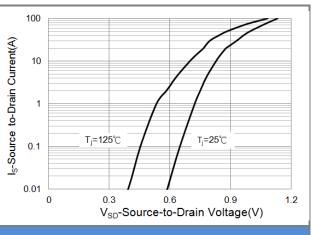


Fig.6 Source-Drain Diode Forward Voltage





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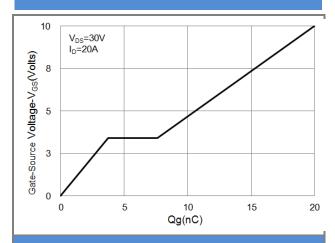


Fig.7 Gate-Charge Characteristics

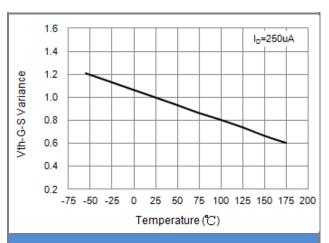


Fig.9 Threshold Voltage Variation with Temperature

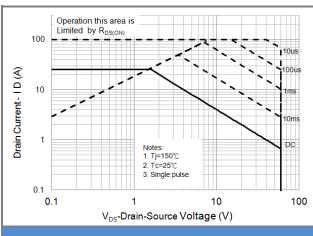


Fig.11 Maximum Safe Operating Area

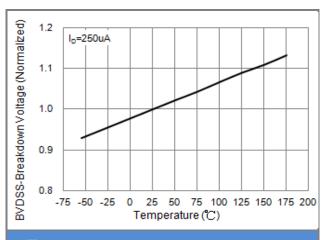


Fig.8 Breakdown Voltage Variation vs. Temperature

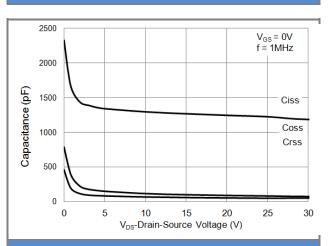


Fig.10 Capacitance vs. Drain-Source Voltage





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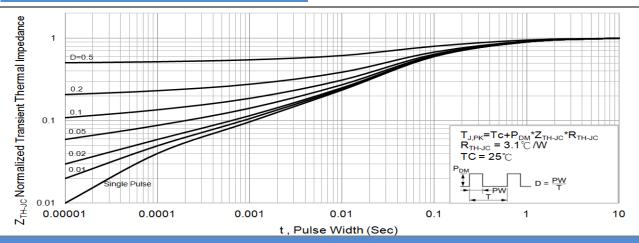


Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

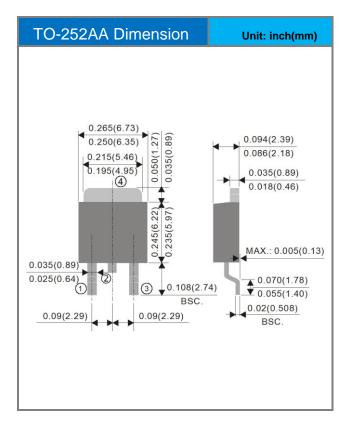


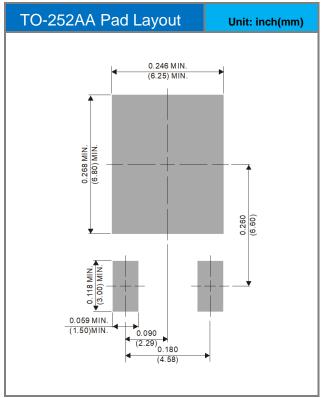


#### PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version	
PJD25N06A-AU_L2_000A1	TO-252AA	3,000pcs / 13" reel	D25N06A	Halogen free	

### **Packaging Information & Mounting Pad Layout**









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