



30V P-Channel Enhancement Mode MOSFET

Voltage

-30 V

Current

-100 A

Features

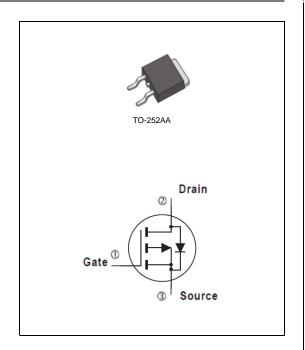
- $R_{DS(ON)}$, $V_{GS}@-10V$, $I_D@-20A<4.5m\Omega$
- $R_{DS(ON)}$, $V_{GS}@-4.5V$, $I_D@-15A<7m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard



• Case: TO-252AA Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Weight: 0.0104 ounces, 0.297grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMET	ΓER	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-30	V	
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V	
Continuos Dunin Comment	T _C =25°C	-100			
Continuous Drain Current	T _C =100°C	l _D	-63	Α	
Pulsed Drain Current (Note 1)	T _C =25°C	I _{DM}	-400		
Power Dissipation	T _C =25°C	Po	104	W	
	T _C =100°C		42		
Continuous Drain Current	T _A =25°C	I _D	-15.8	А	
	T _A =70°C		-12.6		
Power Dissipation	T _A =25°C	Б	2.0	W	
Power Dissipation	T _A =70°C	Po	1.3		
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
T : 1 T	Junction to Case	$R_{ heta JC}$	1.2	°C/W	
Typical Thermal Resistance (Note 4,5)	Junction to Ambient	$R_{\theta JA}$	62.5		

Limited only By Maximum Junction Temperature





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =-250uA	-30	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-1	-1.6	-2.5	V	
Danier Courses On Chata Basista	R _{DS(on)}	V _{GS} =-10V,I _D =-20A	-	3.9	4.5	mΩ	
Drain-Source On-State Resistance		V _{GS} =-4.5V,I _D =-15A	-	5.7	7		
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-30V, V_{GS} =0V	-	-	-1	uA	
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA	
Dynamic (Note 6)							
Total Gate Charge	Q_g	V _{DS} =-15V, I _D =-10A, V _{GS} =-10V (Note 2,3)	-	107	-	nC	
Gate-Source Charge	Q_gs		-	18	-		
Gate-Drain Charge	Q_{gd}		-	18	-		
Input Capacitance	Ciss	V _{DS} =-25V, V _{GS} =0V, f=1.0MHZ	-	6067	-	pF	
Output Capacitance	Coss		-	709	-		
Reverse Transfer Capacitance	Crss	I=1.0IVIDZ	-	361	-		
Turn-On Delay Time	td _(on)	V_{DS} =-15V, I_{D} =-1A, V_{GS} =-10V, R_{G} =6 Ω (Note 2,3)	-	22	-		
Turn-On Rise Time	t _r		-	48	-	ns	
Turn-Off Delay Time	td _(off)		-	197	-		
Turn-Off Fall Time	t _f		-	90	-		
Drain-Source Diode							
Maximum Continuous Drain-Source	,				-100	Α	
Diode Forward Current	I _S		-	-	-100	^	
Diode Forward Voltage	V_{SD}	I _S =-1A,V _{GS} =0V	-	-0.68	-1	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics
- 3. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 4. The maximum current rating is package limited
- 5. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing





TYPICAL CHARACTERISTIC CURVES

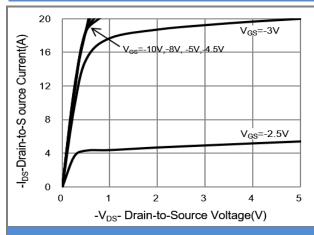


Fig.1 Output Characteristics

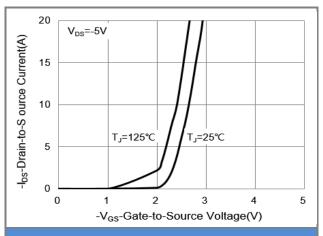


Fig.2 Transfer Characteristics

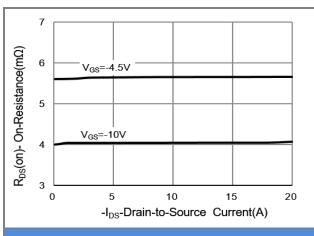


Fig.3 On-Resistance vs. Drain Current

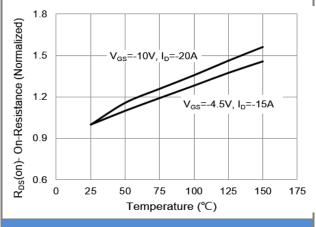
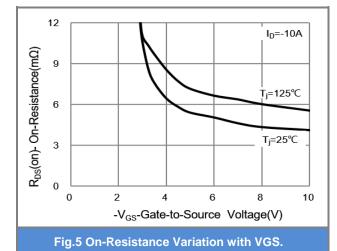


Fig.4 On-Resistance vs. Junction Temperature

10



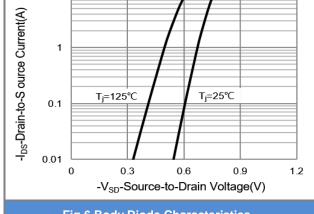


Fig.6 Body Diode Characteristics





TYPICAL CHARACTERISTIC CURVES

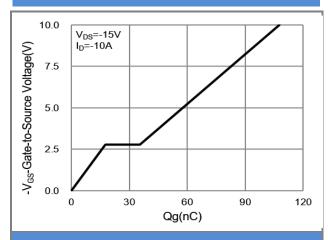


Fig.7 Gate Charge

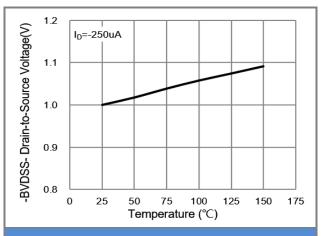


Fig.8 Breakdown Voltage Variation vs. Temperature

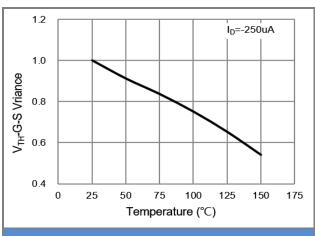


Fig.9 Threshold Voltage Variation with Temperature

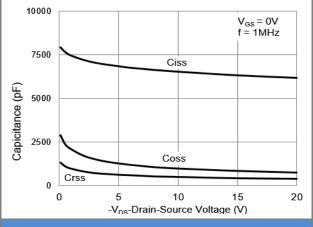


Fig.10 Capacitance vs. Drain-Source Voltage

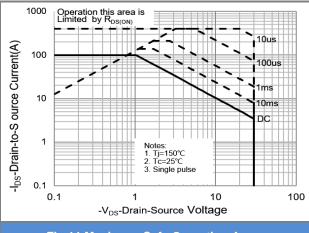


Fig.11 Maximum Safe Operating Area





TYPICAL CHARACTERISTIC CURVES

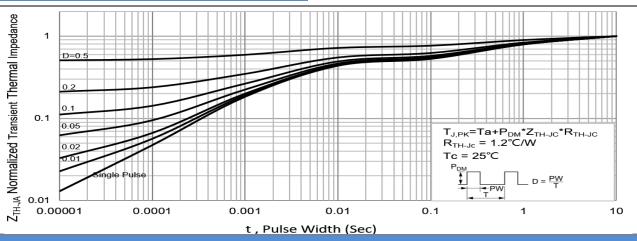
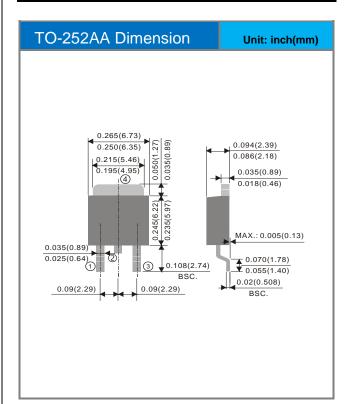


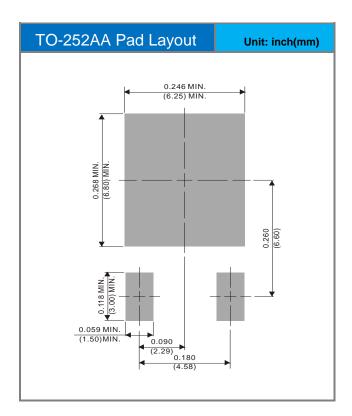
Fig.13 PJP100P03 Normalized Transient Thermal Impedance vs. Pulse Width





Packaging Information









PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version	
PJD100P03_L2_00001	TO-252AA	3,000pcs / 13" reel	D100P03	Halogen free	





Disclaimer

- Reproducing and modifying information of the document is prohibited without permission from Panjit International Inc..
- Panjit International Inc. reserves the rights to make changes of the content herein the document anytime without notification. Please refer to our website for the latest document.
- Panjit International Inc. disclaims any and all liability arising out of the application or use of any product including damages incidentally and consequentially occurred.
- Panjit International Inc. does not assume any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.
- Applications shown on the herein document are examples of standard use and operation. Customers are
 responsible in comprehending the suitable use in particular applications. Panjit International Inc. makes no
 representation or warranty that such applications will be suitable for the specified use without further testing or
 modification.
- The products shown herein are not designed and authorized for equipments requiring high level of reliability or relating to human life and for any applications concerning life-saving or life-sustaining, such as medical instruments, transportation equipment, aerospace machinery et cetera. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Panjit International Inc. for any damages resulting from such improper use or sale.
- Since Panjit uses lot number as the tracking base, please provide the lot number for tracking when complaining.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Panjit:

PJD100P03_L2_00001