PJA3432-AU	
30V N-Channel Enhancement Mode MOSFET – ESI	D Protected SOT-23
Voltage 30 V Current 1.6 A Features	
 R_{DS(ON)}, <u>V_{GS}@4.5V</u>, I_D@1.6A<200mΩ R_{DS(ON)}, V_{GS}@2.5V, I_D@1.1A<270mΩ 	
• R _{DS(ON)} , V _{GS} @1.8V, I _D @0.2A<570mΩ	
Advanced Trench Process TechnologyESD Protected 2KV HBM	
Specially Designed for Switch Load, PWM Application, etcAEC-Q101 qualified	D
 Lead free in compliance with EU RoHS 2.0 Green molding compound as per IEC 61249 standard 	
Green molaing compound as per IEC 61249 standard	
Case : SOT-23 Package	1 2 G 5

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0084 grams

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V _{DS}	30		
Gate-Source Voltage	V _{GS}	<u>+</u> 8		
Continuous Drain Current ^(Note 4)	lь	1.6	A	
Pulsed Drain Current ^(Note 1)		I _{DM}		
Power Dissipation	Ta=25°C	PD	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3,4)		R _{θJA}	100	°C/W



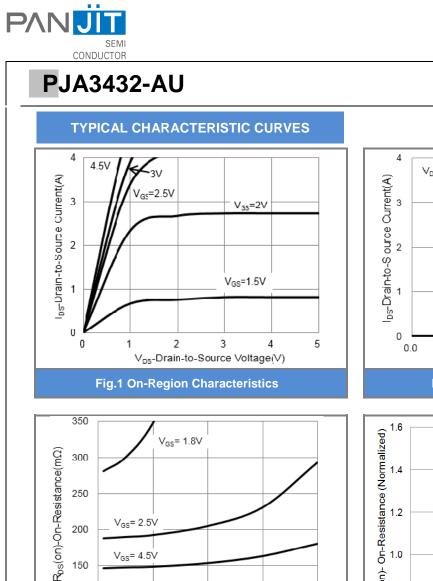
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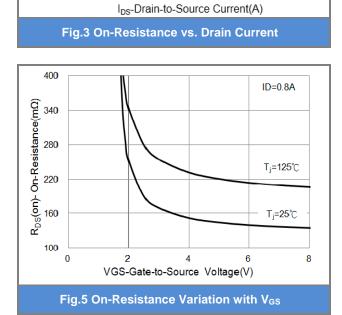
Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} =0V, I _D =250uA	30	-	- V	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	0.5	0.78	1.3	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =1.6A	-	145	200	
		V _{GS} =2.5V, I _D =1.1A	-	185	270	mΩ
		V _{GS} =1.8V, I _D =0.2A	-	330	570	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	lgss	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	-	<u>+</u> 10	
Dynamic ^(Note 5)						
Total Gate Charge	Qg		-	1.5	-	nC
Gate-Source Charge	Qgs	$V_{DS}=15V, I_{D}=1.6A,$	-	0.3	-	
Gate-Drain Charge	Q_{gd}	V _{GS} =4.5V ^(Note 1,2)	-	0.3	-	
Input Capacitance	Ciss		-	93	-	pF
Output Capacitance	Coss	V _{DS} =15V, V _{GS} =0V,	-	19	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	6	-	
Turn-On Delay Time	td _(on)		-	6.4	-	
Turn-On Rise Time	tr	V _{DD} =15V, I _D =1.6A,	-	33	-	
Turn-Off Delay Time	td _(off)	V _{GS} =4.5V,	-	37	-	ns
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	32	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		_	_	1	А
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0V	-	0.81	1.2	V

NOTES :

- 1. Pulse width
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.



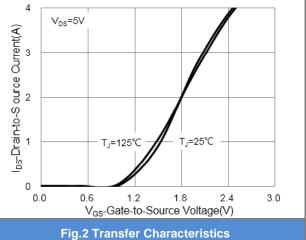


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1

3

4



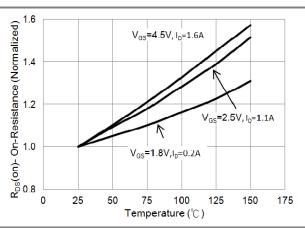
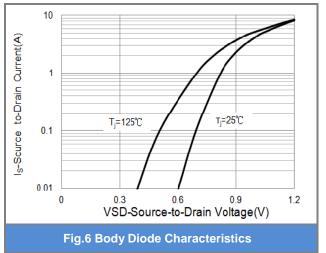


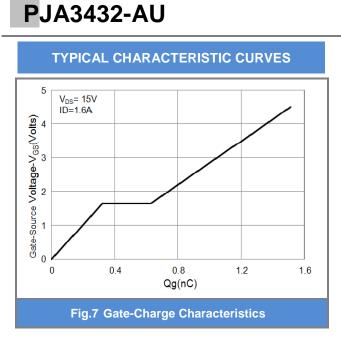
Fig.4 On-Resistance vs. Junction temperature

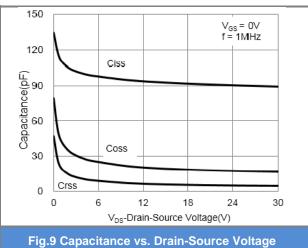


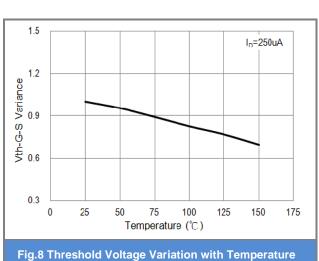
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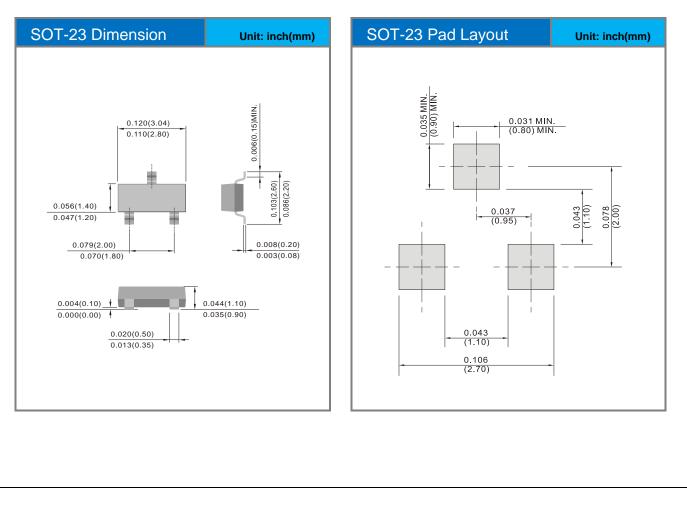


PJA3432-AU

Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJA3432-AU_R1_000A1	SOT-23	3K pcs / 7" reel	A32	Halogen free

Packaging Information & Mounting Pad Layout





PJA3432-AU

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