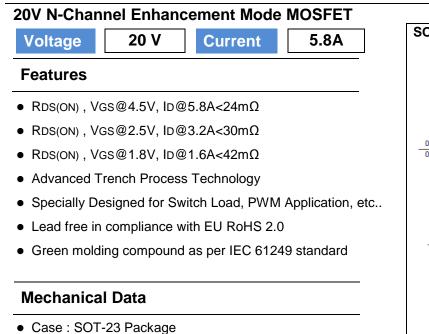
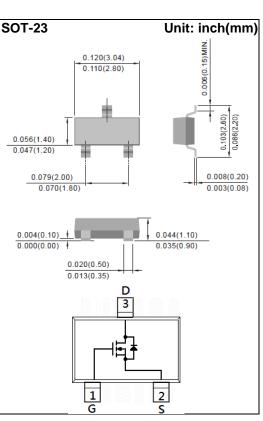
	1 A A A A A A A A A A A A A A A A A A A
ΡΛΝ	JIT
	SEMI
	CONDUCTOR



- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0084 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage	V _{GS}	<u>+</u> 12	V	
Continuous Drain Current		lь	5.8	А
Pulsed Drain Current		I _{DM}	23.2	А
Power Dissipation	T₂=25°C	PD	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		Tյ,Tsтg	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		R _{θJA}	100	°C/W



Electrical Characteristics (TA=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =250uA	0.5	0.68	1.2	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =5.8A	-	20	24	mΩ
		V _{GS} =2.5V, I _D =3.2A	-	25	30	
		Vgs=1.8V, Id=1.6A	-	32	42	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =20V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	lgss	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic ^(Note 5)						
Total Gate Charge	Qg	V _{DS} =10V, I _D =5.8A, V _{GS} =4.5V ^(Note 1,2)	-	10.4	-	nC
Gate-Source Charge	Q _{gs}		-	0.8	-	
Gate-Drain Charge	Q_{gd}		-	3.2	-	
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V, f=1.0MHZ	-	592	-	pF
Output Capacitance	Coss		-	91	-	
Reverse Transfer Capacitance	Crss		-	82	-	
Turn-On Delay Time	td _(on)	V _{DD} =10V, I _D =5.8A, V _{GS} =4.5V,	-	4.3	-	
Turn-On Rise Time	tr		-	39	-	
Turn-Off Delay Time	td _(off)		-	31	-	ns
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	30	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	1.5	A
Diode Forward Voltage	V_{SD}	I _S =1.0A, V _{GS} =0V	-	0.76	1.2	V

NOTES :

1. Pulse width</br>

- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{⊕JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited
- 5. Guaranteed by design, not subject to production testing



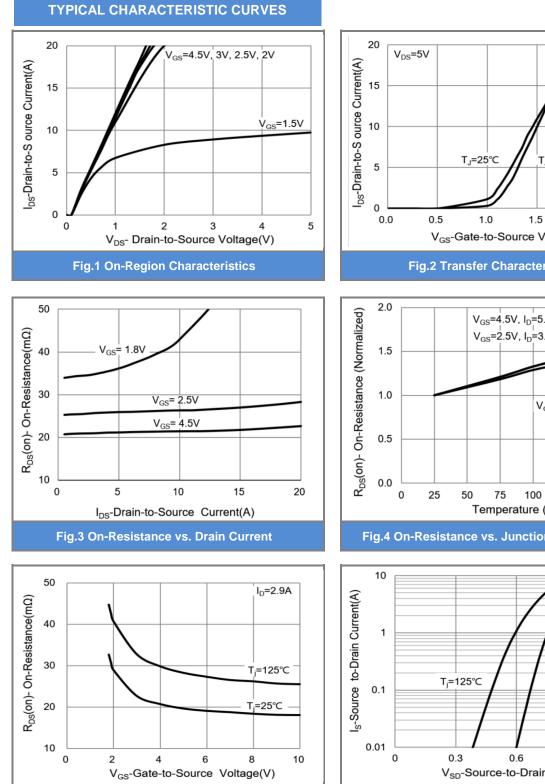


Fig.5 On-Resistance Variation with VGS.

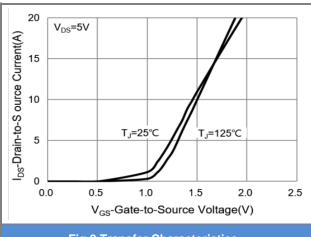


Fig.2 Transfer Characteristics

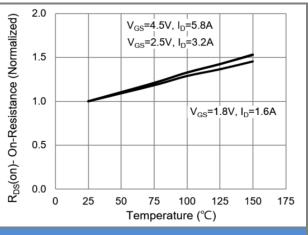
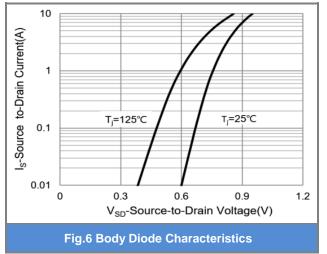
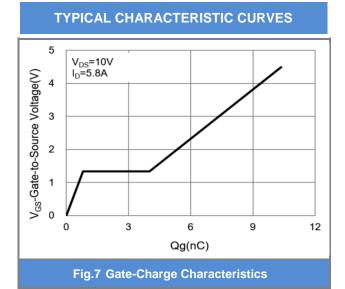
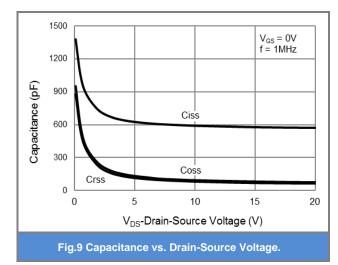


Fig.4 On-Resistance vs. Junction temperature









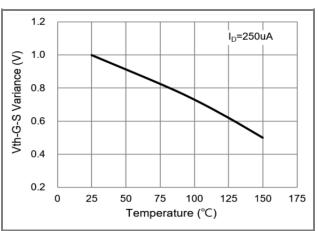


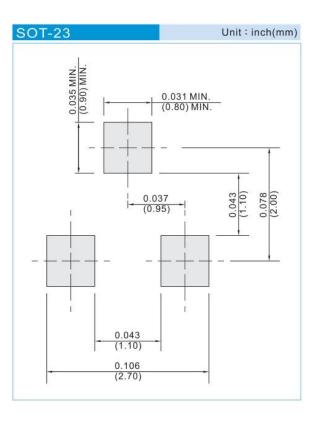
Fig.8 Threshold Voltage Variation with Temperature.



PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJA3416A_R1_00001	SOT-23	3K pcs / 7" reel	A6A	Halogen free RoHS compliant
PJA3416A_R2_00001	SOT-23	12K pcs / 13" reel	A6A	Halogen free RoHS compliant

MOUNTING PAD LAYOUT





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