ΡΛΝ	JIT
	SEMI
	CONDUCTOR

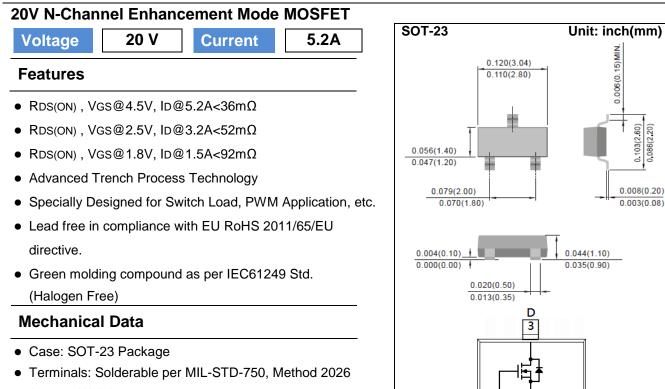
0.103(2.60)

1

2

0.086(2.20)

PJA3414



- Approx. Weight: 0.0003 ounces, 0.0084 grams ٠
- Marking: A14

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 12	V
Continuous Drain Current		I _D	5.2	А
Pulsed Drain Current		I _{DM}	20.8	А
Power Dissipation	T _a =25°C	P _D	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient ^(Note 3)		R _{θJA}	100	°C/W



Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static		•				
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250$ uA	0.5	0.77	1.2	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =5.2A	-	29	36	mΩ
		V _{GS} =2.5V, I _D =3.2A	-	39	52	
		V _{GS} =1.8V, I _D =1.5A	-	58	92	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V	-	0.01	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Q_{g}	V _{DS} =10V, I _D =5.2A, V _{GS} =4.5V ^(Note 1,2)	-	4.1	-	
Gate-Source Charge	Q_gs		-	1.1	-	nC
Gate-Drain Charge	Q_gd		-	0.7	-	
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V,	-	396	-	pF
Output Capacitance	Coss		-	54	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	40	-	
Switching						
Turn-On Delay Time	td _(on)		-	14	-	
Turn-On Rise Time	tr	$V_{DD}=10V, I_{D}=5.2A,$		10		
Turn-Off Delay Time	td _(off)	$V_{GS}=4.5V,$ R _G =6 $\Omega^{(Note 1,2)}$	-	30	-	ns
Turn-Off Fall Time	tf	$R_{G}=0\Omega$		7		
Drain-Source Diode						
Maximum Continuous Drain-Source	I				4.5	
Diode Forward Current	I _S		-	-	1.5	A
Diode Forward Voltage	V_{SD}	I _S =1.0A, V _{GS} =0V	-	0.75	1.2	V

NOTES :

- 1. Pulse width<300us, Duty cycle<2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{®JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited



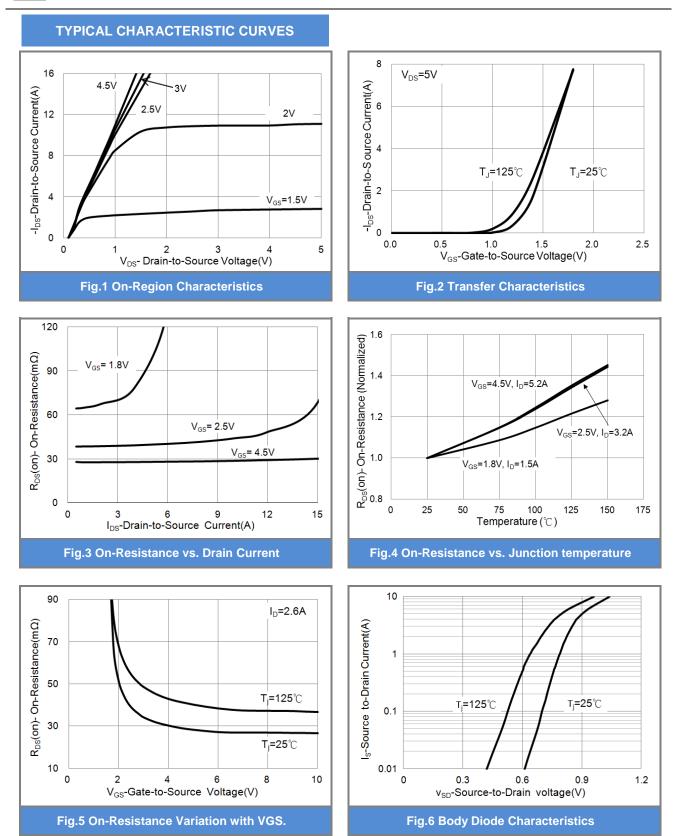




fig.7 Gate-Charge Characteristics

TYPICAL CHARACTERISTIC CURVES

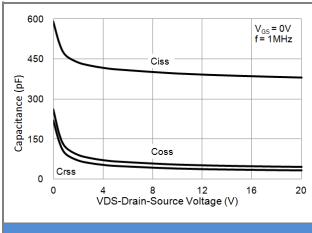
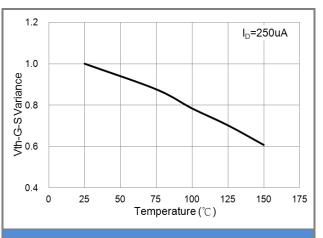


Fig.9 Capacitance vs. Drain-Source Voltage.





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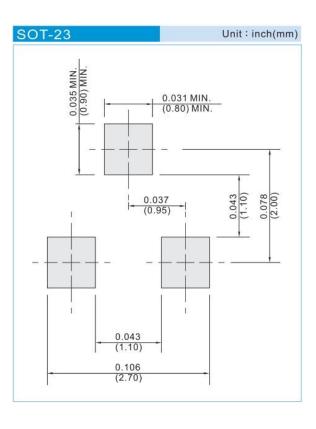




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJA3414_R1_00001	SOT-23	3K pcs / 7" reel	A14	Halogen free
PJA3414_R2_00001	SOT-23	12K pcs / 13" reel	A14	Halogen free

MOUNTING PAD LAYOUT







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