

20V P-Channel Enhancement Mode MOSFET

Voltage

-20 V

Current

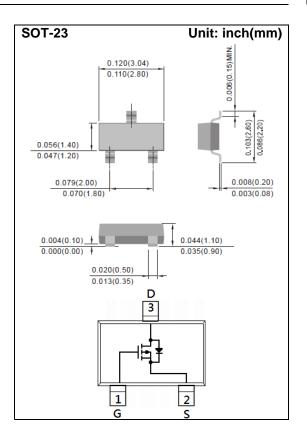
-3.1A

Features

- RDS(ON), VGS@-4.5V, ID@-3.1A<100mΩ
- RDS(ON) , VGS@-2.5V, ID@-2.0A<135mΩ
- RDS(ON), VGS@-1.8V, ID@-1.1A<190m Ω
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.009 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _G s	<u>+</u> 12	V
Continuous Drain Current		I _D	-3.1	Α
Pulsed Drain Current		I _{DM}	-12.4	Α
Power Dissipation	T _a =25°C	_	1.25	W
	Derate above 25°C	P₀	10	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance				
- Junction to Ambient (Note 3)		$R_{\theta JA}$	100	°C/W



Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA	-0.4	-0.71	-1.2	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-3.1A	-	84	100	mΩ	
		V _{GS} =-2.5V, I _D =-2.0A	-	104	135		
		V _{GS} =-1.8V, I _D =-1.1A	-	134	190		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	-0.01	-1	uA	
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA	
Dynamic							
Total Gate Charge	Q_g		-	5.4	-	nC	
Gate-Source Charge	Q_gs	V _{DS} =-10V, I _D =-3.1A, V _{GS} =-4.5V (Note 1,2)	-	0.7	-		
Gate-Drain Charge	Q_{gd}	VGS=-4.5V (1888 1,2)	-	1.3	-		
Input Capacitance	Ciss	\/ 40\/ \/ 0\/	-	416	-	pF	
Output Capacitance	Coss	V _{DS} =-10V, V _{GS} =0V, f=1.0MHZ	-	43	-		
Reverse Transfer Capacitance	Crss	I=1.0WHZ	-	32	-		
Switching							
Turn-On Delay Time	td _(on)	\/ 10\/ I- 2.1A	-	4	-	ns	
Turn-On Rise Time	tr	V _{DD} =-10V, I _D =-3.1A, V _{GS} =-4.5V,	-	27	-		
Turn-Off Delay Time	td _(off)	$R_{G}=6\Omega$ (Note 1,2)	-	78	-		
Turn-Off Fall Time	tf	1\(\text{G}=\text{0\(\text{2\(\text{2\(\text{V}\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	-	45	-		
Drain-Source Diode							
Maximum Continuous Drain-Source	Is		_	_	-1.5	А	
Diode Forward Current	IS		_	_	-1.5	Α	
Diode Forward Voltage	V_{SD}	I _S =-1.0A, V _{GS} =0V	-	0.8	-1.2	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited



TYPICAL CHARACTERISTIC CURVES

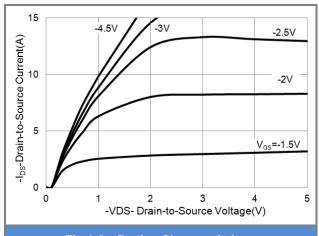


Fig.1 On-Region Characteristics

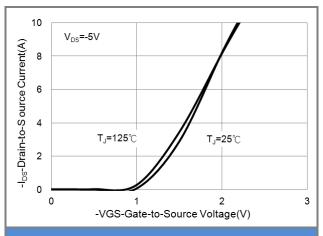


Fig.2 Transfer Characteristics

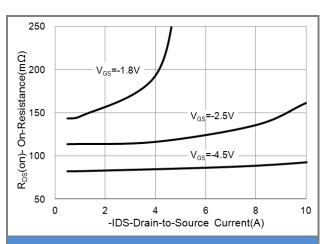


Fig.3 On-Resistance vs. Drain Current

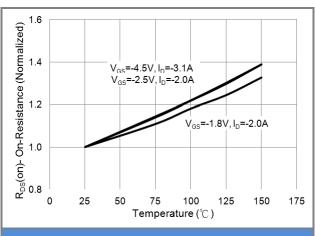
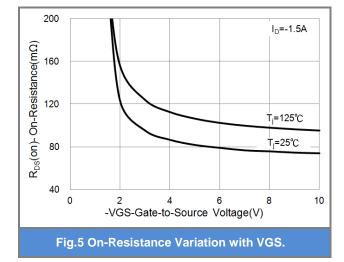
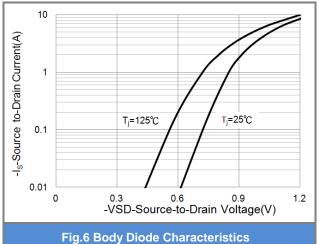


Fig.4 On-Resistance vs. Junction temperature







TYPICAL CHARACTERISTIC CURVES

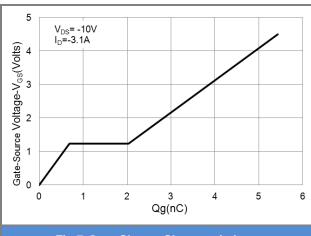


Fig.7 Gate-Charge Characteristics

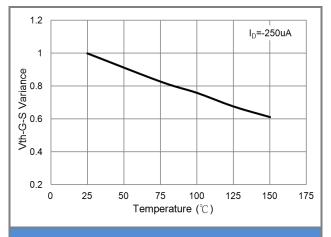
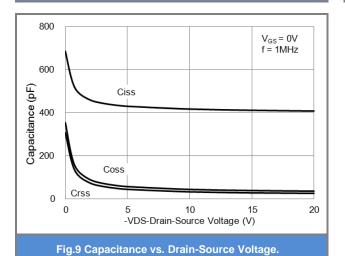


Fig.8 Threshold Voltage Variation with Temperature.

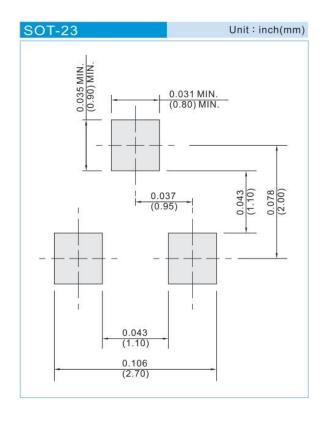




Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJA3411-AU	SOT-23	3K pcs / 7" reel	A11
PJA3411-AU	SOT-23	12K pcs / 13" reel	A11

Mounting Pad Layout





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April 2,2019 PJA3411-AU-REV.01S Page 6

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