

#### **GENERAL DESCRIPTION**

The PJ76339 consists of four independent voltage comparators. These were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage. The outputs can be connected to other open- collector outputs to achieve wired-AND relationships.

Available Package : SOP-14P and TSSOP-14P.

#### **FEATURES**

- Wide Supply Voltage Range : 2 V to 36 V or
  ±1 V to ±18 V
- Low Supply Current Drain independent from the Supply Voltage
- Low Input Biasing Current
- Low Input Offset Current
- Low Input Offset Voltage
- Input Common-mode Voltage Range includes
  GND
- Differential Input Voltage Range Equal to the Power Supply Voltage
- Low Output Saturation Voltage
- Output Voltage Compatible with TTL, MOS and CMOS Logic.
- Temperature Range: -40 °C to 85 °C

#### **APPLICATIONS**

- Vacuum robot
- Single phase UPS
- Server PSU
- Cordless power tool
- Building automation
- Factory automation & control
- Motor drives
- Infotainment & cluster



#### **ORDERING INFORMATION**

ORDER NUMBER	Marking ID	Package	Description
PJ76339P_R2	PJ76339 PYMDNN	SOP-14P	Halogen free RoHS compliant in T/R, 4,000 pcs/Reel
PJ76339B_R2	PJ76339 BYMDNN	TSSOP-14P	Halogen free RoHS compliant in T/R, 4,000 pcs/Reel

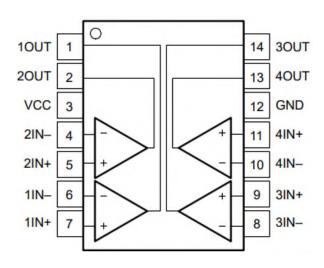
#### Note 1

1. Panjit can meet RoHS 2.0/REACH requirement. So most package types Panjit offers only states halogen free, instead of lead free.

#### **MARKING INFORMATION**

Marking ID	Package	Definition
PJ76339 PYMDNN	SOP-14P	PJ76339: Product code P: Package code Y: Year code M: Month code D: Day code NN: Serial No.
PJ76339 BYMDNN	TSSOP-14P	PJ76339: Product code B: Package code Y: Year code M: Month code D: Day code NN: Serial No.

#### **PIN CONFIGURATION**



SOP-14P / TSSOP-14P (TOP VIEW)



# **FUNCTIONAL PIN DESCRIPTION**

TERMINAL		DESCRIPTION		
NUMBER	NAME			
1	10UT	Output pin of the comparator 1		
2	20UT	Output pin of the comparator 2		
3	VCC	Positive Power Supply		
4	2IN-	Negative input pin of the comparator 2		
5	2IN+	Positive input pin of the comparator 2		
6	1IN-	Negative input pin of the comparator 1		
7	1IN+	Positive input pin of the comparator 1		
8	3IN-	Negative input pin of the comparator 3		
9	3IN+	Positive input pin of the comparator 3		
10	4IN-	Negative input pin of the comparator 4		
11	4IN+	Positive input pin of the comparator 4		
12	GND	Ground Pin / Negative supply		
13	40UT	Output pin of the comparator 4		
14	3OUT	Output pin of the comparator 3		



#### **ABSOLUTE MAXIMUM RATINGS**

Over operating free-air temperature range (unless otherwise noted) <sup>(1)</sup>

PARAME	TER	MIN	MAX	Unit
Supply Voltage	Ver		36	V
Supply Voltage	Vcc	-18	18	V
Differential Input Voltage	VIND	-36	36	V
Input Voltage	V <sub>IN</sub>	-0.3	36	V
Input Current (V <sub>IN</sub> < -0.3 V)	Ію		50	mA
Maximum Junction Temperature	TJ		150	°C
Storage temperature range	Tstg	-65	150	°C

(1) Stresses beyond those listed under *absolute maximum ratings* may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *recommended operating conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

#### **RECOMMENDED OPERATING CONDITIONS**

	PARAMETER	MIN	ТҮР	МАХ	UNIT
Vcc	Supply Voltage	2		32	V
T <sub>A</sub>	Operating Ambient temperature	-40		85	°C



#### **ELECTRICAL CHARACTERISTICS**

Test Condition :  $V_{CC}$  = 5.0V, unless otherwise specified, all limits are 100% test at T<sub>A</sub>=25°C. <sup>(1)</sup>

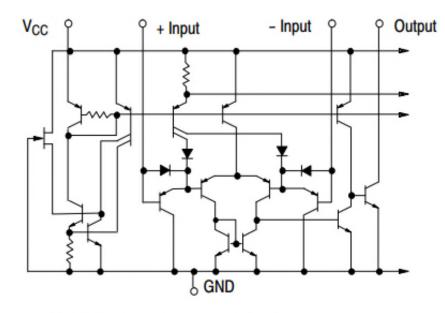
	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
	lanut Offact Vallage			2	5	mV
V <sub>IO</sub>	Input Offset Voltage				9	mV
	Input Offeet Current	$V_0 = 1.4 \text{ V},  \text{T}_A = 25^{\circ}\text{C}$		5	50	nA
I <sub>IO</sub>	Input Offset Current	$V_0 = 1.4 \text{ V}, \text{ T}_A = -40 \text{ to } 85^{\circ}\text{C}$			150	nA
	lagut Ding Ourset	V <sub>0</sub> = 1.4 V, T <sub>A</sub> = 25°C		-25	-250	nA
I <sub>IB</sub>	Input Bias Current	$V_0 = 1.4 \text{ V}, \text{ T}_A = -40 \text{ to } 85^{\circ}\text{C}$			-400	nA
M	Common-mode Input Voltage	$T_A = 25^{\circ}C$	0		V <sub>cc</sub> -1.5	V
VICR	Range <sup>(1)</sup>	T <sub>A</sub> = -40 to 85°C	0		V <sub>cc</sub> -2.0	V
A <sub>VD</sub>	Large-signal Differential Voltage Amplification	$\begin{split} V_{CC} &= 15 \text{ V}, V_O = 1.4 \text{ V to } 11.4 \text{ V}, \\ R_L &\geq 15 \text{ k}\Omega \text{ to } V_{CC}, T_A = 25^\circ\text{C} \end{split}$	50	200		V/mV
		$I_{OL} = 4 \text{ mA}, V_{ID} = -1 \text{ V}, T_A = 25^{\circ}\text{C}$		150	400	mV
V <sub>OL</sub>	Low-Level Output Voltage	$I_{OL} = 4 \text{ mA}, V_{ID} = -1 \text{ V},$ $T_A = -40 \text{ to } 85^{\circ}\text{C}$			700	mV
		$V_{OH}$ = 5 V, $V_{ID}$ = 1 V, $T_A$ = 25°C		0.1	50	nA
I <sub>он</sub>	High-Level Output Current	$V_{OH} = 30 \text{ V}, V_{ID} = 1 \text{ V},$ $T_A = -40 \text{ to } 85^{\circ}\text{C}$			1	uA
IOL	Low-Level Output Current	$V_{OL} = 1.5 \text{ V}, V_{ID} = -1 \text{ V}, T_A = 25^{\circ}\text{C}$	6			mA
		$R_L = \infty$ , $V_{CC} = 5$ V, $T_A = 25^{\circ}C$		0.8	2	mA
I <sub>CC</sub>	Supply Current	$\label{eq:RL} \begin{split} R_L &= \infty,  V_{CC} = 30 \ V, \\ T_A &= -40 \ to \ 85^\circ C \end{split}$			2.5	mA
		$ \begin{array}{l} {\sf R}_{\sf L} \mbox{ connected to 5 V through} \\ {\sf 5.1 k}\Omega,  {\sf C}_{\sf L} = 15 \mbox{ pF}^{(2)}, \mbox{ 100 mV input} \\ {\sf step with 5 mV over-drive} \end{array} $		1.3		uS
t <sub>RES</sub>	Response Time	$R_L$ connected to 5 V through 5.1 k $\Omega$ , $C_L$ = 15 pF <sup>(2)</sup> , TTL-level input step		0.3		uS

(1) The voltage at either input or common-mode should not be allowed to go negative by more than 0.3V. The upper end of the common- mode voltage range is V<sub>cc</sub>-1.5V, but either or both inputs can go to 30V without damage.

(2) The response time specified is the interval between the input step function and the instant, when the output crosses 1.4 V. C<sub>L</sub> includes probe and jig capacitance.



### **BLOCK DIAGRAM**

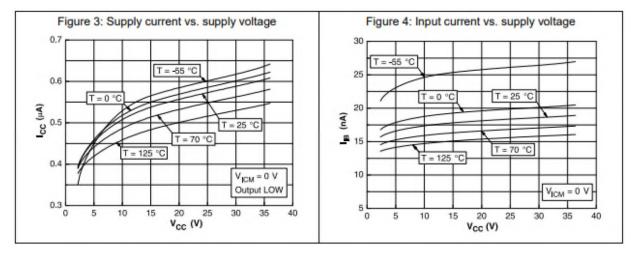


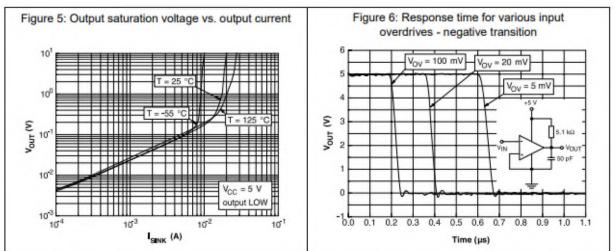
NOTE: Diagram shown is for 1 comparator.

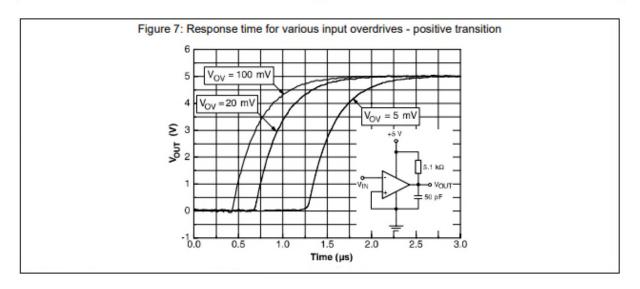


# **PJ76339** Dual Differential Comparator In a SOP-14P and TSSOP-14P Package

### **TYPICAL PERFORMANCE CHARACTERISTICS**





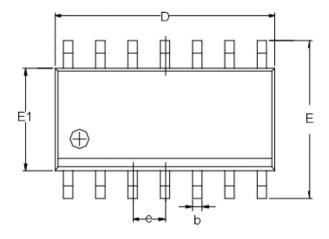


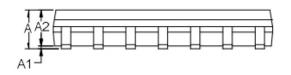


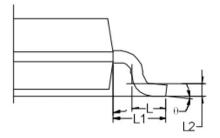
# PJ76339 Dual Differential Comparator In a SOP-14P and TSSOP-14P Package

# PACKAGE OUTLINE DIMENSION (SOP-14P)

#### SOP-14P Unit (mm)







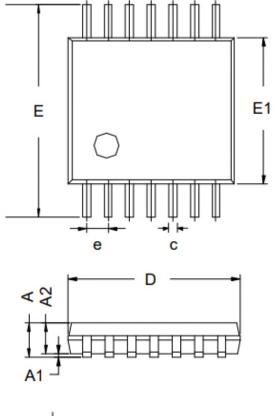
	Dimensions			
Symbol		In Millimeters	-	
	MIN	TYP	MAX	
Α	1.35	1.60	1.75	
A1	0.10	0.15	0.25	
A2	1.25	1.45	1.65	
b	0.31		0.51	
D	8.45	8.63	8.85	
E	5.80	6.00	6.20	
E1	3.80	3.90	4.00	
е		1.27 BSC		
L	0.40	0.60	0.80	
L1		1.05 REF		
L2	0.25 BSC			
θ	0°		8°	

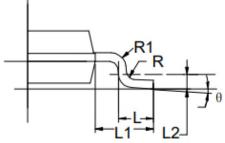


# PJ76339 Dual Differential Comparator In a SOP-14P and TSSOP-14P Package

# PACKAGE OUTLINE DIMENSION (TSSOP-14P)

TSSOP-14P Unit (mm)





	Dimensions			
Complex	In Millimeters			
Symbol	MIN	ТҮР	MAX	
А	-	-	1.20	
A1	0.05		0.15	
A2	0.80	-	1.05	
с	0.19	-	0.30	
D	4.86	5.00	5.10	
E	6.20	6.40	6.60	
E1	4.30	4.40	4.50	
e		0.65 BSC		
L	0.45	0.60	0.75	
L1		1.00 REF		
L2	0.25 BSC			
R	0.09	-	-	
θ	0°	-	8°	



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