



System On Module iW-RainboW-G47M

Kintex Ultrascale+ FPGA SOM

Kintex Ultrascale+ FPGA System On Module supports Kintex Ultrascale+ FPGA and ARM Cortex A7 processor. The SOM features FPGA with programmable logic cells up to 1842K and Power-efficient 32-bit Dual Arm Cortex-A7 core processor, running up to 1.2 GHz.

The SOM is compatible with KU19P Kintex Ultrascale+ FPGA and KU095 & KU115 Kintex Ultrascale FPGAs. The module is capable of high speed connectivity peripherals such as 100G Ethernet, PCle, USB3.0, SATA3.1, Gigabit Ethernet and dual 64-bit DDR4 with ECC and 32-bit DDR4 with ECC.

APPLICATIONS:

Data Centre Network Acceleration, Industrial IoT, Cloud Computing

iW-RainboW-G47M HIGHLIGHTS

Xilinx Kintex Ultrascale+ FPGA with B2104 package

Dual 4GB FPGA-DDR4 with ECC (64bit + 8bit)

48 channels of GTY transceivers up to 32Gbps

Two 240pin High-Speed Connectors with 172 user IOs

Dual ARM Cortex-A7 core processor of 1.2GHz speed

2GB DDR4 for CPU with ECC (32bit+4bit)

QorlQ Trust Architecture and Arm TrustZone

4 lanes of 6Gbps SERDES from CPU

Industrial Grade Availability

SPECIFICATIONS

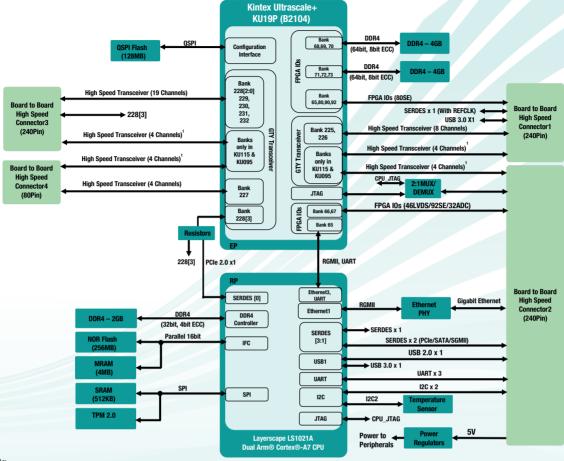
Kintex UltraScale+ FPGA Kintex Ultrascale+ with B2104 package Upto 1,842K Logic Cells Upto 1,080 DSP Slices Dual ARM Cortex-A7 core processor Operation frequency of 1.2GHz QorlQ Trust Architecture and Arm TrustZone High-speed serial interfaces (SERDES) x4 @6Gbps **Memory Interfaces** From FPGA Dual 64-bit DDR4 with ECC 128MB QSPI flash **From CPU** 32bit DDR4 with ECC 256MB parallel NOR Flash 4MB MRAM 512KB SRAM via SPI **On SOM Features** 10/100/1000 Ethernet PHY **Temperature Sensor** TPM2.0 Module via SPI

From FPGA
GTY Transceiver x32 up to 32Gbps
46 LVDS Pairs
80 SE FPGA IOs
From CPU
SERDES x3 @6Gbps (PCIe, SATA, SGMII)
Gigabit Ethernet x 1
USB 2.0 OTG x 1
USB 3.0 x1
I2C x 2
Debug UART
UART x 2
JTAG
Operating Systems
Linux
Power Supply
5V through Board to Board connector
Temperature Support
-40°C to +85°C (Industrial grade operation)
Form Factor
75mm x 110mm
Environmental Specifications
REACH & RoHS3

Quad Board to Board Connector interfaces



Kintex Ultrascale+ FPGA SOM Block Diagram



KU19P Kintex Ultrascale+ FPGA and KU115 & KU095 Kintex Ultrascale FPGAs. Also compatible with VU13P, VU11P, VU9P, VU7P, VU5P, VU190, VU160, VU125, VU080 & Vu095. Bank Numbers mentioned in the block diagram is as per KU19P. Bank Number &

OS SUPPORT

Linux

DELIVERABLES

Kintex UltraScale + FPGA Module **Board Support Package** Example FPGA Design **User Manuals**

OPTIONAL KITS/Modules

Kintex UltraScale+ Development Kit **Heat Spreader**

CUSTOM DEVELOPMENT

BSP Development/OS Porting **Custom SOM/Carrier Development** Custom Application/GUI Development **Design Review and Support**

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*Optional items not included in the standard deliverables.

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The device can be ordered online from the iWave Website https://www.iwavesystems.com/product/ku19p-fpga-system-on-module/ Or from our Local Partners in your region

http://www.iwavesystems.com/about-us/business-partner.html

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[†] These Transceiver Banks are not available in KU19P and available only in KU115 & KU095.
[‡] Transceiver speed is limited to 16Gbps for Transceivers in Board to Board connector1 & 2 b or 1.8/2 hecause of connector speed limit

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